

### **Features**

- Trench Power LV MOSFET Technology
- · Excellent Package for Heat Dissipation
- High Density Cell Design for Low R<sub>DS(on)</sub>
- · Halogen Free. "Green" Device (Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)
- · Moisture Sensitivity Level 1

## **Maximum Ratings**

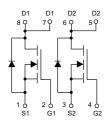
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 41.5°C/W Junction to Ambient (2)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DS</sub>	40	V
Gate-Source Volltage	V <sub>GS</sub>	±20	V
Continuous Drain Current	I <sub>D</sub>	25	Α
Pulsed Drain Current (3)	I <sub>DM</sub>	100	Α
Total Power Dissipation	P <sub>D</sub>	3	W

#### Note:

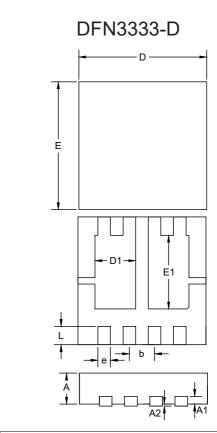
- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. Surface Mounted on FR4 Board,  $t \le 10$  sec.
- 3. Repetitive Rating: Pulse width limited by maximum junction temperature.

# **Internal Structure and Marking Code**





# Dual N-CHANNEL MOSFET



	DIMENSIONS					
DIM	INCHES		MM		NOTE	
DIIVI	MIN	MAX	MIN	MAX	NOTE	
Α	0.030	0.033	0.750	0.850		
A1	0.008		0.200		TYP	
A2	-	0.002	-	0.050		
D	0.128	0.132	3.250	3.350		
Е	0.128	0.132	3.250	3.350		
D1	0.039	0.043	1.000	1.100		
E1	0.073	0.077	1.850	1.950		
b	0.026		0.650		BSC	
е	0.012	0.014	0.300	0.350		
L	0.017	0.021	0.425	0.525		

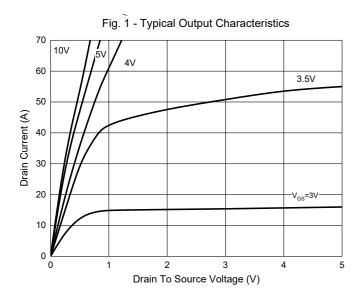


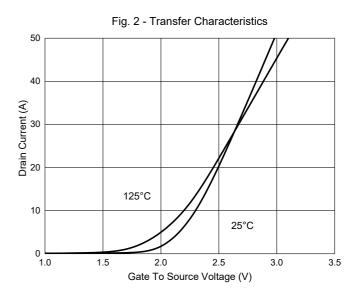
# Electrical Characteristics @ 25°C (Unless Otherwise Specified)

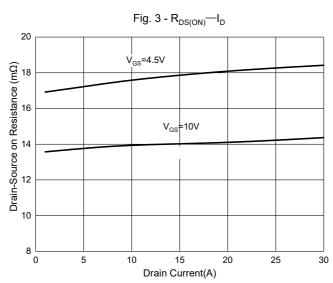
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics			1				
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	40			V	
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V			1	μA	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1	1.5	2.5	V	
Drain-Source On-Resistance		V <sub>GS</sub> =10V, I <sub>D</sub> =8A		14	18	mΩ	
	$R_{DS(on)}$	V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A		18	24	mΩ	
Diode Characteristics							
Continuous Body Diode Current	Is				25	Α	
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =10A			1.2	V	
Reverse Recovery Time	t <sub>rr</sub>	1 -404 4:/-4-4004/		29		ns	
Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>S</sub> =10A,di/dt=100A/µs		26		nC	
Dynamic Characteristics	•		,				
Input Capacitance	C <sub>iss</sub>			750			
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =20V, $V_{GS}$ =0V,f=1MHz		150		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			80			
Total Gate Charge	$Q_g$			15			
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =20V, $V_{GS}$ =10V, $I_{D}$ =10A		3		nC	
Gate-Drain Charge	$Q_{gd}$			2.5			
Turn-On Delay Time	t <sub>d(on)</sub>			6			
Turn-On Rise Time	t <sub>r</sub>	$V_{DS}$ =20V, $V_{GEN}$ =10V, $R_G$ =3 $\Omega$ , $R_L$ =1 $\Omega$ ,		17.5		no	
Turn-Off Delay Time	t <sub>d(off)</sub>	$R_{G}=3\Omega$ , $R_{L}=\Omega$ , $I_{DS}=2A$		31		ns	
Turn-Off Fall Time	t <sub>f</sub>			17			

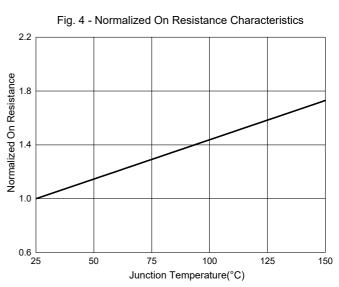


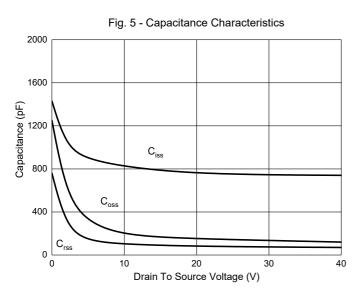
### **Curve Characteristics**

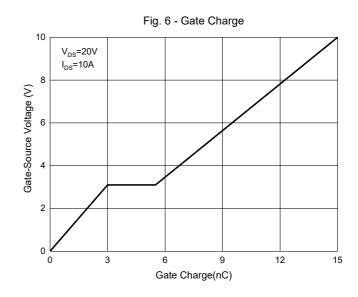








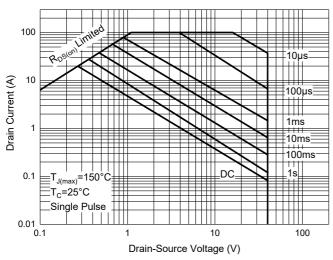






# **Curve Characteristics**







## **Ordering Information**

Device	Packing	
Part Number-TP	Tape&Reel: 5Kpcs/Reel	

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