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BD175/177/179

Medium Power Linear and Switching Applications

• Complement to BD 176/178/180 respectively



NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter		Value	Units
V _{CBO}	*Collector-Base Voltage	: BD175	45	V
020		: BD177	60	V
		: BD179	80	V
V _{CEO}	Collector-Emitter Voltage	: BD175	45	V
		: BD177	60	V
		: BD179	80	V
V _{EBO}	Emitter-Base Voltage		5	V
I _C	Collector Current (DC)		3	Α
I _{CP}	*Collector Current (Pulse)		7	Α
P _C	Collector Dissipation (T _C =25°C)		30	W
T _J	Junction Temperature		150	°C
T _{STG}	Storage Temperature		- 65 ~ 150	°C

Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter		Test Condition	Min.	Тур.	Max.	Units
V _{CEO} (sus)	* Collector-Emitter Sustaining	Voltage					
		: BD175	$I_C = 100 \text{mA}, I_B = 0$	45			V
		: BD177		60			V
		: BD179		80			V
I _{CBO}	Collector Cut-off Current	: BD175	$V_{CB} = 45V, I_{E} = 0$			100	μΑ
		: BD177	$V_{CB} = 60V, I_{E} = 0$			100	μΑ
		: BD179	$V_{CB} = 80V, I_{E} = 0$			100	μΑ
I _{EBO}	Emitter Cut-off Current		$V_{EB} = 5V, I_{C} = 0$			1	mA
h _{FE1}	* DC Current Gain		$V_{CE} = 2V, I_{C} = 150 \text{mA}$	40		250	
h _{FE2}			$V_{CE} = 2V, I_{C} = 1A$	15			
V _{CE} (sat)	* Collector-Emitter Saturation	Voltage	$I_C = 1A, I_B = 0.1A$			0.8	V
V _{BE} (on)	* Base-Emitter On Voltage		$V_{CE} = 2V, I_{C} = 1A$			1.3	V
f _T	Current Gain Bandwidth Proc	luct	$V_{CE} = 10V, I_{C} = 250mA$	3			MHz

^{*} Pulse Test: PW=300μs, duty Cycle=1.5% Pulsed

h_{FE} Classificntion

Classification	6	10	16	
h _{FE1}	40 ~ 100	63 ~ 160	100 ~ 250	
* Classification 16: Only BD175				

Typical Characteristics

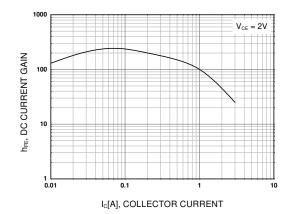


Figure 1. DC current Gain

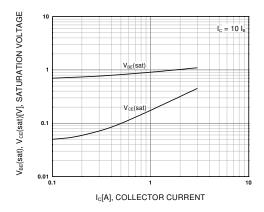


Figure 2. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

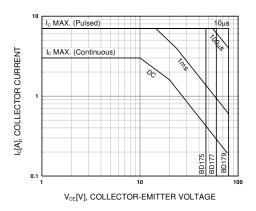


Figure 3. Safe Operating Area

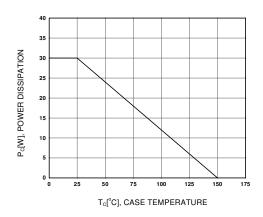
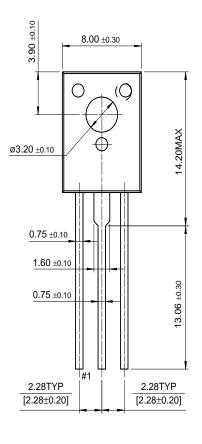


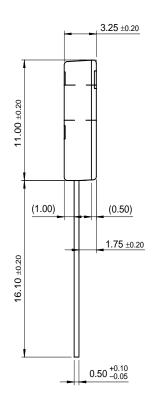
Figure 4. Power Derating

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Package Demensions

TO-126





Dimensions in Millimeters

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