



DUAL P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
-30V	5Ω @ VGS = -4.5V	-0.22A
	6Ω @ VGS = -2.5V	-0.20A
	7Ω @ VGS = -1.8V	-0.18A
	10Ω @ Vgs = -1.5V	-0.15A

Features and Benefits

- Low On-Resistance
- Very Low Gate Threshold Voltage
- Low Input Capacitance
- · Fast Switching Speed
- Ultra-Small Surface Mount Package 0.8mm x 0.6mm
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen- and Antimony-Free. "Green" Device (Note 3)
- The DMP32D9UDAQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

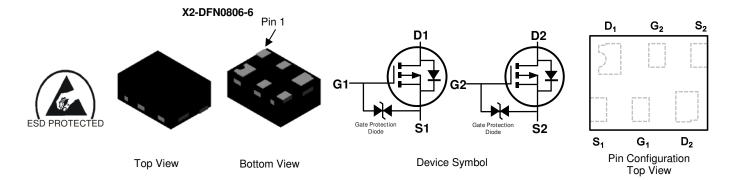
Description and Applications

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- General Purpose Interfacing Switch
- Power Management Functions
- Analog Switch

Mechanical Data

- Case: X2-DFN0806-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—Matte Tin Annealed over Copper Lead-Frame.
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.001 grams (Approximate)



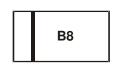
Ordering Information (Note 4)

Part Number	Case	Packaging
DMP32D9UDAQ-7B	X2-DFN0806-6	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



B8 = Product Type Marking Code Bar denotes Pin 1



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit			
Drain-Source Voltage			V _{DSS}	-30	V	
Gate-Source Voltage			V _{GSS}	±12	V	
0 11 0 141 514 451	Steady	T _A = +25°C	1	-0.22	Δ.	
Continuous Drain Current (Note 5) V _{GS} = -4.5V	State	T _A = +70°C	ID	-0.17		
Maximum Continuous Body Diode Forward Current (Note 5)			Is	-0.8	Α	
Pulsed Drain Current (Note 5)			I _{DM}	-0.8	Α	

Thermal Characteristics (@ $T_A = +25$ °C, unless otherwise specified.)

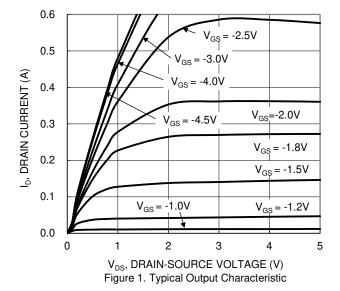
Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	P_D	0.36	W	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	345	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@ T_A = +25°C, unless otherwise specified.)

Characteristic		Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage		-30	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current @ $T_C = +25$ °C	I _{DSS}	_	_	-100	nA	$V_{DS} = -24V$, $V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 10V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	V _{GS(TH)}	-0.4	1	-1.0	V	$V_{DS}=V_{GS},\ I_D=-250\mu A$	
			1.8	5	Ω	$V_{GS} = -4.5V, I_D = -100mA$	
Static Drain-Source On-Resistance	D	_	2.3	6		$V_{GS} = -2.5V, I_D = -50mA$	
Static Drain-Source On-nesistance	R _{DS(ON)}	_	3	7		$V_{GS} = -1.8V, I_D = -20mA$	
		_	3.4	10		$V_{GS} = -1.5V, I_D = -10mA$	
Diode Forward Voltage		_	-0.6	-1.0	V	$V_{GS} = 0V, I_{S} = -10mA$	
DYNAMIC CHARACTERISTICS (Note 7)	•				•		
Input Capacitance		1	21.8		pF	V 45V V 0V	
Output Capacitance			2.82		pF	$V_{DS} = -15V, V_{GS} = 0V,$ f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	1.66	_	pF	1 = 1.000112	
Total Gate Charge	Qg	_	0.35	_	nC	V 45V V 45V	
Gate-Source Charge		_	0.05	_	nC	$V_{GS} = -4.5V, V_{DS} = -15V,$ $I_{D} = -200 \text{mA}$	
Gate-Drain Charge	Q _{gd}	_	0.10	_	nC	1ID = -20011IA	
Turn-On Delay Time	t _{D(ON)}		3.5	_	ns		
Turn-On Rise Time			5.2	_	ns	$V_{DD} = -15V, V_{GS} = -4.5V,$	
Turn-Off Delay Time			18.8	_	ns	$R_g = 2\Omega$, $I_D = -200mA$	
Turn-Off Fall Time		_	8.7	_	ns		

5. Device mounted on minimum recommended pad layout test board, 10µs pulse duty cycle = 1%.
6. Short duration pulse test used to minimize self-heating effect.
7. Guaranteed by design. Not subject to product testing





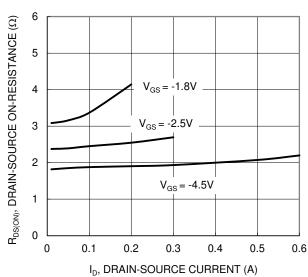


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

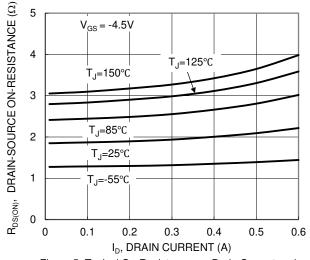
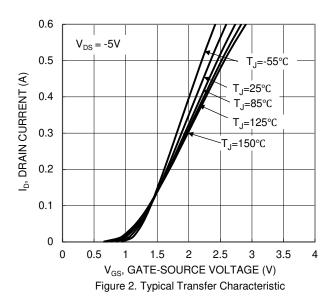
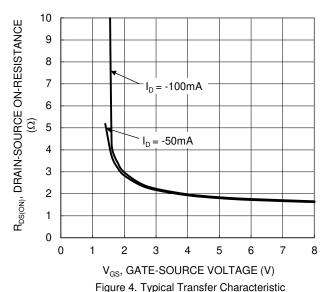


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature





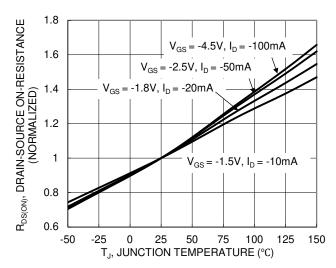


Figure 6. On-Resistance Variation with Junction Temperature



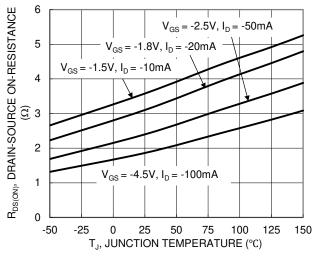
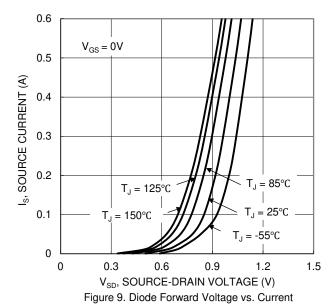
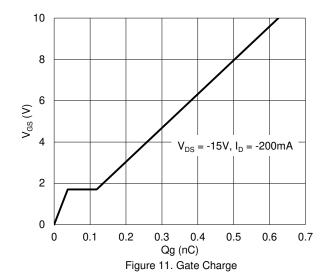


Figure 7. On-Resistance Variation with Junction Temperature





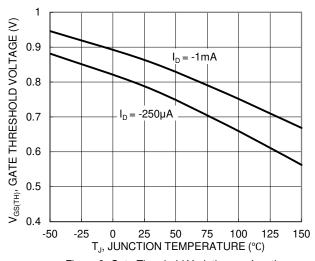
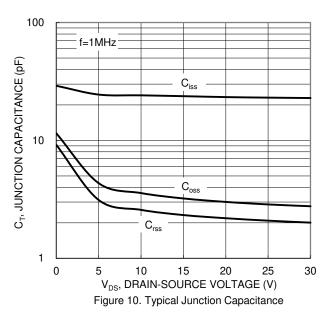


Figure 8. Gate Threshold Variation vs. Junction Temperature



R_{DS(ON)} Limited ID, DRAIN CURRENT (A) 0.1 P_W =100ms 0.01 $P_W = 10s$ $T_{J(Max)} = 150$ °C $T_C = 25^{\circ}C$ DC Single Pulse DUT on 1*MRP Board $V_{GS} = -4.5V$ 0.001 0.1 10 100 V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area



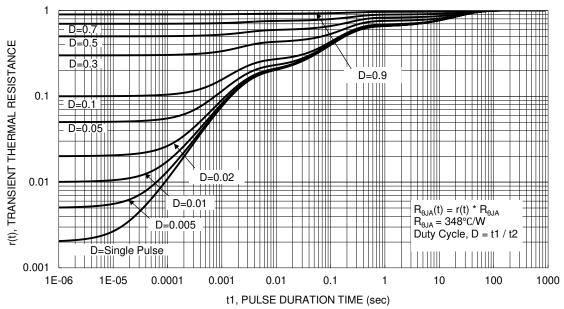
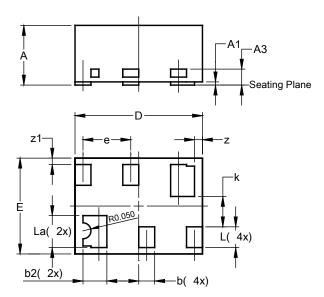


Figure 13. Transient Thermal Resistance



Package Outline Dimensions

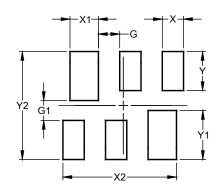
Please see http://www.diodes.com/package-outlines.html for the latest version.



	X2-DFN0806-6						
Dim	Min	Max	Тур				
Α		0.40	0.36				
A1	0.00	0.03	0.02				
A3	-		0.10				
b	0.07	0.15	0.10				
b2	0.10	0.20	0.15				
D	0.75	0.85	0.80				
E	0.55	0.65	0.60				
е			0.30				
k			0.19				
L	0.10	0.18	0.13				
La	0.17	0.25	0.20				
Z			0.05				
z1			0.04				
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)			
G	0.150			
G1	0.140			
Х	0.150			
X1	0.200			
X2	0.800			
Υ	0.275			
Y1	0.345			
Y2	0.760			



IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
 - 1. are intended to implant into the body, or
 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2020, Diodes Incorporated

www.diodes.com