# 2A, 100V - 200V Ultra Fast Surface Mount Rectifier

#### **FEATURES**

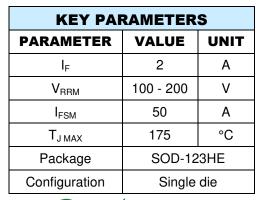
- AEC-Q101 gualified
- Planar technology
- Low power loss, high efficiency
- Ideal for automated placement
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

#### **APPLICATIONS**

- High frequency switching
- DC/DC
- Snubber

#### **MECHANICAL DATA**

- Case: SOD-123HE
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.013g (approximately)





HALOGEN FREE







ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise noted)					
PARAMETER		SYMBOL	PU2BLSH	PU2DLSH	UNIT
Marking code on the device			U2BLS	U2DLS	
Repetitive peak reverse voltage		V <sub>RRM</sub>	100	200	V
Reverse voltage, total rms value		V <sub>R(RMS)</sub>	70	140	V
Forward current		١ <sub>F</sub>	2		Α
Surge peak forward current single half sine-wave superimposed on rated load	t = 8.3ms	1	50 140		Δ
	t = 1.0ms	IFSM			A
Junction temperature		TJ	-55 to +175		°C
Storage temperature		T <sub>STG</sub>	-55 to +175		°C



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THERMAL PERFORMANCE				
PARAMETER	SYMBOL	ТҮР	UNIT	
Junction-to-lead thermal resistance	R <sub>eJL</sub>	14	°C/W	
Junction-to-ambient thermal resistance	R <sub>eja</sub>	79	°C/W	
Junction-to-case thermal resistance	R <sub>eJC</sub>	19	°C/W	

Thermal Performance Note: Units mounted on PCB (5mm x 5mm Cu pad test board)

ELECTRICAL SPECIFICATIONS (T <sub>A</sub> = 25°C unless otherwise noted)					
PARAMETER	CONDITIONS	SYMBOL	ТҮР	MAX	UNIT
<b>—</b> ———————————————————————————————————	$I_F = 1A, T_J = 25^{\circ}C$		0.81	-	V
	$I_F = 2A, T_J = 25^{\circ}C$	N	0.87	0.93	V
Forward voltage <sup>(1)</sup>	$I_F = 1A, T_J = 125^{\circ}C$	V <sub>F</sub>	0.66	-	V
	$I_F = 2A, T_J = 125^{\circ}C$	-	0.73	-	V
Reverse current @ rated $V_R^{(2)}$	$T_J = 25^{\circ}C$	- I <sub>R</sub>	-	2	μA
	T <sub>J</sub> = 125°C		-	10	μA
Junction capacitance	1MHz, V <sub>R</sub> = 4.0V	CJ	31	-	pF
Reverse recovery time	$I_F = 0.5A, I_R = 1.0A, I_{rr} = 0.25A$			25	
	$I_F = 1.0A, di/dt = 50A/\mu s, V_R = 30V$	t <sub>rr</sub>	30	-	ns
Reverse recovery current		I <sub>RM</sub>	3.6	-	А
Reverse recovery charge	$I_F = 2.0A$ , di/dt = 200A/µs, $V_R = 100V$	Q <sub>rr</sub>	31	-	μA pF ns
Reverse recovery time	]	t <sub>rr</sub>	19	-	ns

#### Notes:

1. Pulse test with PW = 0.3ms

2. Pulse test with PW = 30ms

ORDERING INFORMATION			
ORDERING CODE <sup>(1)</sup>	PACKAGE	PACKING	
PU2xLSH	SOD-123HE	10,000/ Tape & Reel	

Notes:

1. "x" defines voltage from 100V(PU2BLSH) to 200V(PU2DLSH)



## **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

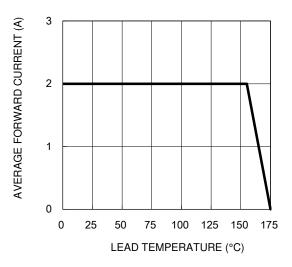
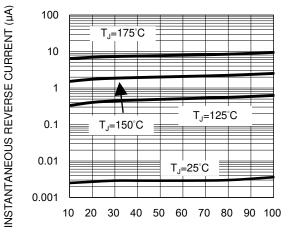
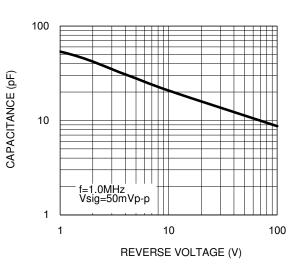


Fig.1 Forward Current Derating Curve

#### **Fig.3 Typical Reverse Characteristics**

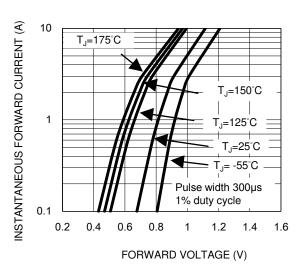


PERCENT OF RATED PEAK REVERSE VOLTAGE (%)

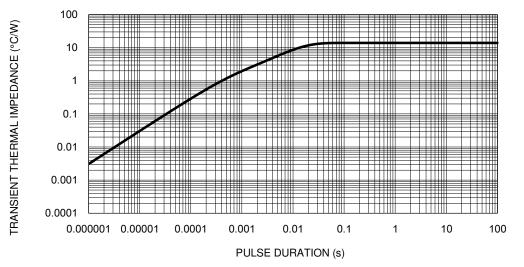


## Fig.2 Typical Junction Capacitance

**Fig.4 Typical Forward Characteristics** 





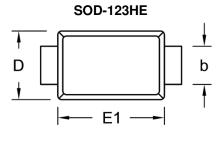


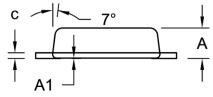


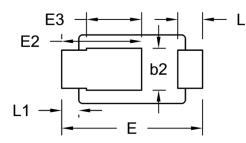
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## **PACKAGE OUTLINE DIMENSIONS**

**5** TAIWAN SEMICONDUCTOR

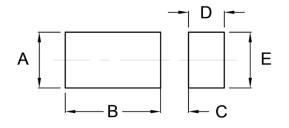






DIM.	Unit	(mm)	Unit	(inch)
	Min.	Max.	Min.	Max.
A	0.75	0.85	0.030	0.033
A1	0.00	0.02	0.000	0.001
b	0.85	1.15	0.033	0.045
b2	0.95	1.25	0.037	0.049
с	0.10	0.20	0.004	0.008
D	1.65	1.95	0.065	0.077
E	3.50	3.90	0.138	0.154
E1	2.60	3.00	0.102	0.118
E2	1.90	2.30	0.075	0.091
E3	1.35	1.55	0.053	0.061
L	0.55	0.75	0.022	0.030
L1	0.35	0.55	0.014	0.022

## SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
A	1.40	0.055
В	2.40	0.094
С	0.70	0.028
D	0.90	0.035
E	1.40	0.055

### **MARKING DIAGRAM**



P/N = Marking Co	de
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YW = Date Code

F = Factory Code



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