

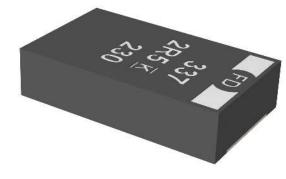
Overview

The KEMET Organic Capacitor (KO-CAP) is a tantalum capacitor with a Ta anode and Ta_2O_5 dielectric. A conductive organic polymer replaces the traditionally used MnO_2 as the cathode plate of the capacitor. This results in very low ESR and improved capacitance retention at high frequency. The KO-CAP also exhibits a benign failure mode which eliminates the ignition failures that can occur in standard MnO_2 tantalum types. KO-CAPs may also be operated at steady state voltages up to 90% of rated voltage for part types with rated voltages of \leq 10 volts and up to 80% of rated voltage for part types > 10 volts with equivalent or better reliability than traditional MnO_2 tantalum capacitors operated at 50% of rated voltage.

The T528 Series KO-CAP combines ultra-low ESR and high capacitance in a package design that offers the lowest ESL in the market for this type of product. This series offers exceptional performance for high-speed server and microprocessor decoupling – designs that are driving the demand for low inductance chips. The T528 uses a different termination design that allows for a reduction in the inductance loop area and comes in a low profile 1.7 mm case height. These product features offer the advantage of improved capacitance retention at frequencies of up to 1 MHz.

Benefits

- · Polymer cathode technology
- 100% accelerated steady state aging
- Low ESL <0.7 nH @ 20 MHz
- 100% surge current tested
- High frequency capacitance retention
- · Non-ignition failure mode
- · Improved volumetric efficiency
- · Self-healing mechanism
- Capacitance: 33 μF to 470 μF
- Use up to 90% of rated voltage (10% derating)
- Voltage: 2 V to 6.3 V
- · RoHS compliant and Halogen Free
- 105°C maximum temperature capability
- · Lead free 260°C reflow capable



Environmental Compliance

RoHS Compliant (6/6) according to Directive 2002/95/EC when ordered with 100% Sn Solder





Applications

Typical applications include high speed server, microprocessor decoupling and high ripple current applications.

SPICE

For a detailed analysis of specific part numbers, please visit www.kemet.com for a free download of KEMET's SPICE software. The KEMET SPICE program is freeware intended to aid design engineers in analyzing the performance of these capacitors over frequency, temperature, ripple, and DC bias conditions.

Ordering Information

т	528	Z	337	М	2R5	Α	т	E009	
Capacitor Class	Series	Case Size	Capacitance Code (pF)	Capacitance Tolerance	Voltage	Failure Rate/ Design	Lead Material	ESR Code	Packaging (C-Spec)
T = Tantalum	528 = Low ESL Facedown Terminal Polymer	B = 3528-21 K = 3528-10 W = 7343-15 Z = 7343-17	First two digits represent significant figures. Third digit specifies number of zeros.	M = ±20%	002 = 2 V 2R5 = 2.5 V 004 = 4 V 006 = 6.3 V	A = N/A	T = 100% Matte Tin (Sn) Plated P = Ni-Pd-Au Plated	E = ESR Last three digits specify ESR in m Ω (009 = 9 m Ω)	Blank = 7" Reel 7280 = 13" Reel

Performance Characteristics

Item	Performance Characteristics
Operating Temperature	-55°C to 105°C
Rated Capacitance Range	33 – 470 μF @ 120 Hz/25°C
Capacitance Tolerance	M Tolerance (20%)
Rated Voltage Range	2-6.3 V
DF (120 Hz)	≤ 10%
ESR (100 kHz)	Refer to Part Number Electrical Specification Table
Leakage Current	\leq 0.1 CV (µA) at rated voltage after 5 minutes



Qualification

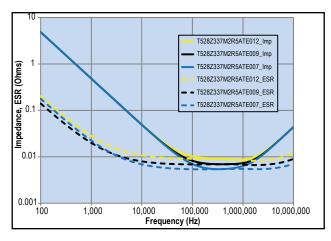
Test	Condition		Characteristics						
			ΔC/C	Within -20/+	10% of initial va	lue			
Endurance	105°C @ rated voltage, 2,000 hours		DF	≤ Initial Limit					
Endurance	105 C @ Taled Voltage, 2,000 hours		DCL	Within 1.25 x initial limit					
			ESR	Within 2.0 x	initial limit				
			ΔC/C	Within -20/+7	10% of initial va	lue			
Storage Life	105°C @ 0 volts, 2,000 hours		DF	Within initial	limits				
Storage Life			DCL	Within 1.25 >	cinitial limit				
			ESR	Within 2.0 x	initial limit				
			ΔC/C	Within -5/+35% of initial value					
Humidity	60°C, 90% RH, 500 hours		DF	≤ Initial Limit					
Turnuty	00 C, 90% KH, 500 Hours		DCL	Within 5.0 x initial limit					
			ESR	Within 2.0 x initial limit					
			+25°C	-55°C	+85°C	+105°C			
Temperature Stability	Extreme temperature exposure at a succession of continuous steps at +25°C,	ΔC/C	IL*	±20%	±20%	±30%			
Temperature Stability	-55°C, +25°C, +85°C, +105°C, +25°C	DF	IL	IL	1.2 x IL	1.5 x IL			
		DCL	IL	n/a	10 x IL	10 x IL			
			ΔC/C	Within -20/+2	10% of initial va	lue			
Surge Voltage	105°C, 1.32 x rated voltage 1,000 cycles		DF	Within initial	limits				
Surge voltage	103 C, 1.32 X Taleu Vollage 1,000 Cycles		DCL	Within initial limits					
			ESR	Within initial limits					
	MIL–STD–202, Method 213, Condition I, 100 G	peak	ΔC/C	Within ±10%	of initial value				
Mechanical Shock/Vibration	MIL-STD-202, Method 204, Condition D, 10 Hz	DF	Within initial limits						
	20 G peak		DCL	Within initial limits					

*IL = Initial limit

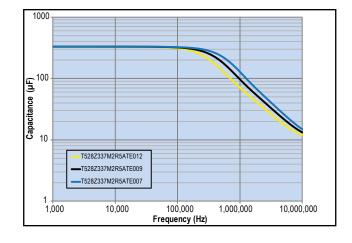


Electrical Characteristics

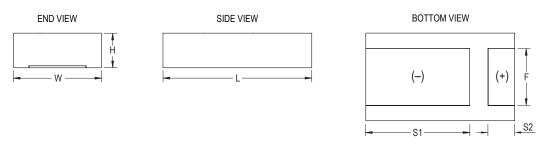
ESR vs. Frequency



Capacitance vs. Frequency



Dimensions – Millimeters



Case	Size	Component							
KEMET	EIA	L	W	Н	F ±0.2	S1 ±0.2	S2 ±0.2		
К	3528–10	3.5 ±0.3	2.8 ±0.3	1.0 Maximum	2	1.2	0.6		
В	3528–20	3.5 ±0.2	2.8 ±0.2	2.0 Maximum	2.2	0.8	0.8		
W	7343–15	7.3 ±0.4	4.3 ±0.3	1.5 Maximum	2.8	5.1	1.3		
Z	7343–17	7.3 ±0.4	4.3 ±0.3	1.7 Maximum	2.8	5.1	1.3		



Table 1 – Ratings & Part Number Reference

Rated Voltage	Rated Capacitance	Case Code/ Case Size	KEMET Part Number	DC Leakage	DF	ESR	Maximum Allowable Ripple Current	MSL	Maximum Operating Temp
VDC @ 105°C	μF	KEMET/EIA	(See below for part options)	µA @ +25°C Maximum/ 5 Minutes	% @ +25°C 120 Hz Maximum	mΩ @ +25°C 100 kHz Maximum	mA @ +45°C 100 kHz	Reflow Temp ≤ 260°C	°C
2	270	B/3528-21	T528B277M002APE006	54.0	8	6	3900	3	105
2	270	B/3528-21	T528B277M002APE009	54.0	8	9	3200	3	105
2.5	220	Z/7343-17	T528Z227M2R5ATE006	55.0	10	6	7400	3	105
2.5	330	W/7343-15	T528W337M2R5ATE009	82.5	10	9	6000	3	105
2.5	330	Z/7343-17	T528Z337M2R5ATE005	82.5	10	5	8100	3	105
2.5	330	Z/7343-17	T528Z337M2R5ATE006	82.5	10	6	7400	3	105
2.5	330	Z/7343-17	T528Z337M2R5ATE007	82.5	10	7	6800	3	105
2.5	330	Z/7343-17	T528Z337M2R5ATE008	82.5	10	8	6400	3	105
2.5	330	Z/7343-17	T528Z337M2R5ATE009	82.5	10	9	6000	3	105
2.5	330	Z/7343-17	T528Z337M2R5ATE012	82.5	10	12	5200	3	105
2.5	470	Z/7343-17	T528Z477M2R5ATE005	117.5	10	5	8100	3	105
2.5	470	Z/7343-17	T528Z477M2R5ATE006	117.5	10	6	7400	3	105
2.5	470	Z/7343-17	T528Z477M2R5ATE008	117.5	10	8	6400	3	105
2.5	470	Z/7343-17	T528Z477M2R5ATE009	117.5	10	9	6000	3	105
2.5	470	Z/7343-17	T528Z477M2R5ATE012	117.5	10	12	5200	3	105
4	220	K/3528-10	T528K227M004ATE100	88.0	10	100	1200	3	105
4	220	Z/7343-17	T528Z227M004ATE007	88.0	10	7	6800	3	105
4	220	Z/7343-17	T528Z227M004ATE008	88.0	10	8	6400	3	105
4	220	Z/7343-17	T528Z227M004ATE009	88.0	10	9	6000	3	105
4	220	Z/7343-17	T528Z227M004ATE012	88.0	10	12	5200	3	105
4	330	Z/7343-17	T528Z337M004ATE009	132.0	10	9	6000	3	105
4	330	Z/7343-17	T528Z337M004ATE012	132.0	10	12	5200	3	105
6.3	150	K/3528-10	T528K157M006ATE100	94.5	10	100	1200	3	105
6.3	150	K/3528-10	T528K157M006ATE200	94.5	10	200	900	3	105
6.3	150	Z/7343-17	T528Z157M006ATE007	94.5	10	7	6800	3	105
6.3	150	Z/7343-17	T528Z157M006ATE008	94.5	10	8	6400	3	105
6.3	150	Z/7343-17	T528Z157M006ATE009	94.5	10	9	6000	3	105
6.3	150	Z/7343-17	T528Z157M006ATE012	94.5	10	12	5200	3	105
6.3	220	Z/7343-17	T528Z227M006ATE009	138.6	10	9	6000	3	105
6.3	220	Z/7343-17	T528Z227M006ATE012	138.6	10	12	5200	3	105
VDC @ 105°C	μF	KEMET/EIA	(See below for part options)	µA @ +25°C Maximum/ 5 Minutes	% @ +25°C 120 Hz Maximum	mΩ @ +25°C 100 kHz Maximum	mA +45°C 100 kHz	Reflow Temp ≤ 260°C	°C
Rated Voltage	Rated Capacitance	Case Code/ Case Size	KEMET Part Number	DC Leakage	DF	ESR	Maximum Allowable Ripple Current	MSL	Maximum Operating Temp

Other part number options:

1- Standard with tin terminations (14th character = T). Tin/lead terminations is also available (14th character = H).

Also available on large (13 inch) reels. Add 7280 to the end of the part number.

Higher voltage ratings and tighter tolerance product including ESR may be substituted within the same size at KEMET's option. Voltage substitutions will be marked with the higher voltage rating. Substitutions can include better than series.

Bold text denotes black epoxy product



Derating Guidelines

Voltage Rating	Maximum Recommended Steady State Voltage	Maximum Recommended Transient Voltage (1 ms – 1 µs)
	-55°C to 105°C	
$2 V \le V_{R} \le 6.3 V$	90% of $V_{_{\rm R}}$	V _R
V _e = Rated Voltage		

R

Ripple Current/Ripple Voltage

Permissible AC ripple voltage and current are related to equivalent series resistance (ESR) and the power dissipation capabilities of the device. Permissible AC ripple voltage which may be applied is limited by two criteria:

1. The positive peak AC voltage plus the DC bias voltage, if any, must not exceed the DC voltage rating of the capacitor.

2. The negative peak AC voltage in combination with bias voltage, if any, must not exceed the allowable limits specified for reverse voltage. See the Reverse Voltage section for allowable limits.

The maximum power dissipation by case size can be determined using the table at right. The maximum power dissipation rating stated in the table must be reduced with increasing environmental operating temperatures. Refer to the table below for temperature compensation requirements.

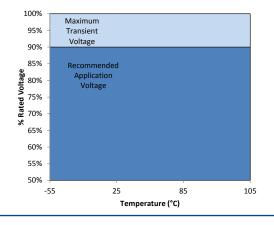
•	ure Compensation M Maximum Ripple Cur	•						
T ≤ 45°C	45° C < T ≤ 85°C	85°C < T ≤ 125°C						
1.00 0.70 0.25								

T= Environmental Temperature

Using the P max of the device, the maximum allowable rms ripple current or voltage may be determined.

 $I(max) = \sqrt{P max/R}$ $E(max) = Z \sqrt{P max/R}$

I = rms ripple current (amperes) E = rms ripple voltage (volts) P max = maximum power dissipation (watts) R = ESR at specified frequency (ohms) Z = Impedance at specified frequency (ohms)



Case Code	EIA Case Code	Maximum Power Dissipation (P max) mWatts @ 45°C with +30°C Rise
К	3528-10	162
В	3528-20	127
W	7343-15	325
Z	7343-17	325
D	7343-31	255
Y	7343-40	263
Х	7443-43	270

The maximum power dissipation rating must be reduced with increasing environmental operating temperatures. Refer to the Temperature Compensation Multiplier table for details.



Reverse Voltage

Polymer tantalum capacitors are polar devices and may be permanently damaged or destroyed if connected in the wrong polarity. These devices will withstand a small degree of transient voltage reversal for short periods as shown in the below table.

Temperature	Permissible Transient Reverse Voltage
25°C	15% of Rated Voltage
55°C	10% of Rated Voltage
85°C	5% of Rated Voltage
105°C	3% of Rated Voltage
125°C*	1% of Rated Voltage

*For Series Rated to 125°C

Table 2 – Land Dimensions/Courtyard

KEMET	Metric Size Code		Density Level A: Maximum (Most) Land Protrusion (mm)				Density Level B: Median (Nominal) Land Protrusion (mm)					Density Level C: Minimum (Least) Land Protrusion (mm)										
Case	EIA	L1	L2	W	S1	S2	V1	V2	L1	L2	W	S1	S2	V1	V2	L1	L2	W	S1	S2	V1	V2
В	3528-20	2.20	2.20	2.35	0.46	0.46	6.32	4.00	1.80	1.80	2.23	0.56	0.56	5.22	3.50	1.42	1.42	2.13	0.64	0.64	4.36	3.24
¹	3216-10	2.34	2.04	1.44	0.17	0.47	6.02	2.80	1.94	1.64	1.32	0.27	0.57	4.92	2.30	1.56	1.26	1.22	0.35	0.65	4.06	2.04
К	3258-10	2.62	2.02	2.24	0.09	0.69	6.42	4.10	2.22	1.62	2.12	0.19	0.79	5.32	3.60	1.84	1.24	2.02	0.27	0.87	4.46	3.34
W ²	7343-15	6.48	2.68	3.04	-1.82	1.98	10.32	5.60	6.18	2.38	2.92	-1.82	1.98	9.22	5.10	5.82	2.02	2.82	-1.76	2.04	8.36	4.84
Z ²	7343-17	6.48	2.68	3.04	-1.82	1.98	10.32	5.60	6.18	2.38	2.92	-1.82	1.98	9.22	5.10	5.82	2.02	2.82	-1.76	2.04	8.36	4.84

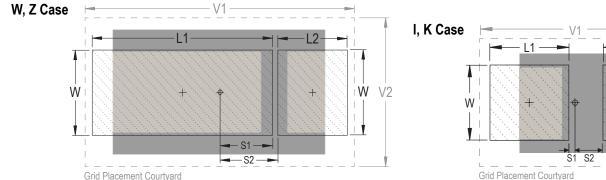
Density Level A: For low-density product applications. Recommended for wave solder applications and provides a wider process window for reflow solder processes. Density Level B: For products with a moderate level of component density. Provides a

robust solder attachment condition for reflow solder processes.

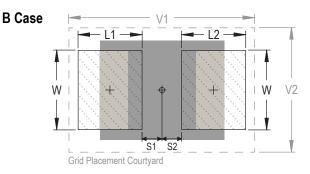
Density Level C: For high component density product applications. Before adapting the minimum land pattern variations the user should perform qualification testing based on the conditions outlined in IPC standard 7351 (IPC-7351).

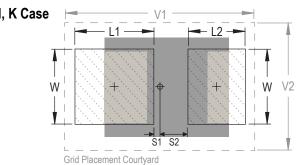
¹ Land pattern geometry is too small for silkscreen outline.

² Negative values of S1 mean that pad lies at the center's right side.











Soldering Process

KEMET's families of surface mount capacitors are compatible with wave (single or dual), convection, IR, or vapor phase reflow techniques. Preheating of these components is recommended to avoid extreme thermal stress. KEMET's recommended profile conditions for convection and IR reflow reflect the profile conditions of the IPC/J–STD–020D standard for moisture sensitivity testing. The devices can safely withstand a maximum of three reflow passes at these conditions.

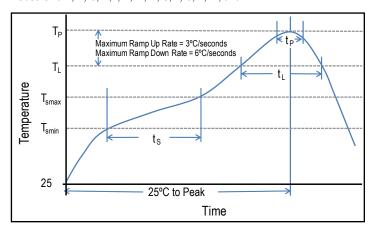
Please note that although the X/7343–43 case size can withstand wave soldering, the tall profile (4.3 mm maximum) dictates care in wave process development.

Hand soldering should be performed with care due to the difficulty in process control. If performed, care should be taken to avoid contact of the soldering iron to the molded case. The iron should be used to heat the solder pad, applying solder between the pad and the termination, until reflow occurs. Once reflow occurs, the iron should be removed immediately. "Wiping" the edges of a chip and heating the top surface is not recommended.

During typical reflow operations, a slight darkening of the goldcolored epoxy may be observed. This slight darkening is normal and not harmful to the product. Marking permanency is not affected by this change.

Profile Feature	SnPb Assembly	Pb-Free Assembly
Preheat/Soak		
Temperature Minimum (T _{smin})	100°C	150°C
Temperature Maximum (T _{Smax})	150°C	200°C
Time (t_s) from T_{min} to T_{max})	60 – 120 seconds	60 – 120 seconds
Ramp-up Rate $(T_L to T_P)$	3°C/seconds maximum	3°C/seconds maximum
Liquidous Temperature (T_L)	183°C	217°C
Time Above Liquidous (t_L)	60 – 150 seconds	60 – 150 seconds
Peak Temperature (T _P)	220°C* 235°C**	250°C* 260°C**
Time within 5°C of Maximum Peak Temperature (t _P)	20 seconds maximum	30 seconds maximum
Ramp-down Rate $(T_p \text{ to } T_L)$	6°C/seconds maximum	6°C/seconds maximum
Time 25°C to Peak Temperature	6 minutes maximum	8 minutes maximum

Note: All temperatures refer to the center of the package, measured on the package body surface that is facing up during assembly reflow. *Case Size D, E, P, Y, and X **Case Size A, B, C, H, I, K, M, R, S, T, U, V, W, and Z

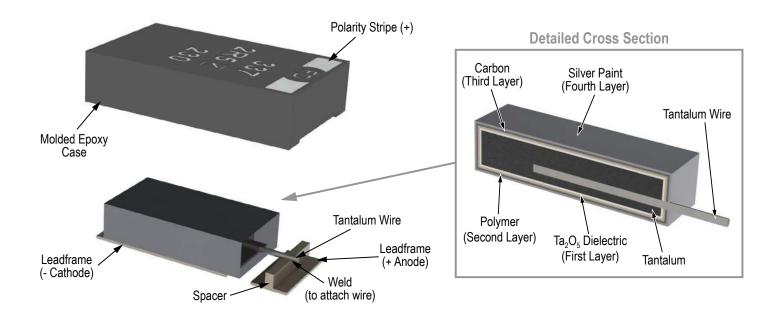


Storage

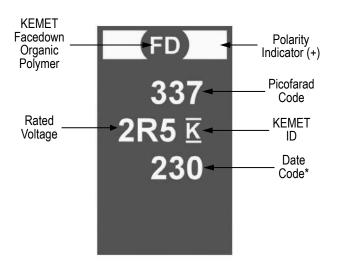
All KO-CAP series are shipped in moisture barrier bags with a desiccant and moisture indicator card. These series are classified as MSL3 (Moisture Sensitivity Level 3). Product contained within the moisture barrier bags should be stored in normal working environments with temperatures not to exceed 40°C and humidity not in excess of 90% RH.



Construction



Capacitor Marking



* 230 = 30th week of 2012

Date Code *								
1⁵t digit = Last number of Year	9 = 2009 0 = 2010 1 = 2011 2 = 2012 3 = 2013 4 = 2014							
2 nd and 3 rd digit = Week of the Year	$01 = 1^{st}$ week of the Year to $52 = 52^{nd}$ week of the Year							



Tape & Reel Packaging Information

KEMET's molded tantalum and aluminum chip capacitor families are packaged in 8 and 12 mm plastic tape on 7" and 13" reels in accordance with *EIA Standard 481*: Embossed Carrier Taping of Surface Mount Components for Automatic Handling. This packaging system is compatible with all tape-fed automatic pick-and-place systems.

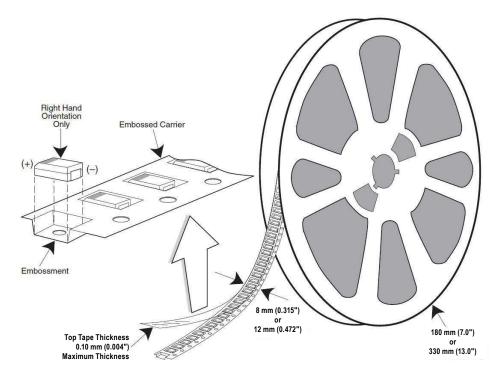


Table 3 – Packaging Quantity

Case Code		Tape Width (mm)	7" Reel*	13" Reel*	
KEMET	EIA				
S	3216-12	8	2,500	10,000	
Т	3528-12	8	2,500	10,000	
М	3528-15	8	2,000	8,000	
U	6032-15	12	1,000	5,000	
L	6032-19	12	1,000	5,000	
W	7343-15	12	1,000	3,000	
Z	7343-17	12	1,000	3,000	
V	7343-20	12	1,000	3,000	
A	3216-18	8	2,000	9,000	
В	3528-21	8	2,000	8,000	
С	6032-28	12	500	3,000	
D	7343-31	12	500	2,500	
Q	7343-12	12	1,000	3,000	
Y	7343-40	12 500		2,000	
Х	7343-43	12	500	2,000	
E/T428P	7360-38	12	500	2,000	
Н	7360-20	12	1,000	2,500	

* No C-Spec required for 7" reel packaging. C-7280 required for 13" reel packaging.



Figure 1 – Embossed (Plastic) Carrier Tape Dimensions

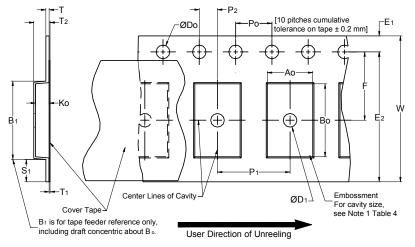


Table 4 – Embossed (Plastic) Carrier Tape Dimensions

Metric will govern

Constant Dimensions — Millimeters (Inches)									
Tape Size	D ₀	D ₁ Minimun Note 1	n E ₁	P ₀	P ₂	R Reference Note 2	S ₁ Minimum Note 3	T Maximum	T ₁ Maximum
8 mm		1.0 (0.039)			2.0 ±0.05	25.0 (0.984)			
12 mm	1.5 +0.10/-0.0 (0.059 +0.004/-0		1.75 ±0.10 (0.069 ±0.0)		,	30	0.600 (0.024)	0.600 (0.024)	0.100 (0.004)
16 mm		(0.059)		, ,	2.0 ±0.1 (0.079 ±0.059)	(1.181)			
Variable Dimensions — Millimeters (Inches)									
Tape Size	Pitch	B ₁ Maximum Note 4	E ₂ Minimum	F	P ₁		T ₂ Maximum	W Maximum	A ₀ , B ₀ & K ₀
8 mm	Single (4 mm)	4.35 (0.171)	6.25 (0.246)	3.5 ±0.05 (0.138 ±0.002)	2.0 ±0.05 or 4. (0.079 ±0.002 or 0.		2.5 (0.098)	8.3 (0.327)	
12 mm	Single (4 mm) & Double (8 mm)	8.2 (0.323)	10.25 (0.404)	5.5 ±0.05 (0.217 ±0.002)	2.0 ±0.05 (0.079 ±0 ±0.10 (0.157 ±0.004 (0.315 ±0.0) or 8.0 ±0.10	4.6 (0.181)	12.3 (0.484)	Note 5
16 mm	Triple (12 mm)	12.1	14.25 (0.561)	7.5±0.10 (0.295 ±0.004)	4.0 ±0.10 (0.157 ±0 +0.10 (0.472 ±	,	8.0 (0.315)	16.3 (0.642)	

1. The embossment hole location shall be measured from the sprocket hole controlling the location of the embossment. Dimensions of embossment location and hole location shall be applied independent of each other.

±0.10 (0.472 ±0.004)

(0.295 ±0.004)

2. The tape, with or without components, shall pass around R without damage (see Figure 4).

3. If S₁ < 1.0 mm, there may not be enough area for cover tape to be properly applied (see EIA Standard 481–D, paragraph 4.3, section b).

4. B, dimension is a reference dimension for tape feeder clearance only.

(0.476)

5. The cavity defined by A_{α} , B_{α} and K_{α} shall surround the component with sufficient clearance that:

(a) the component does not protrude above the top surface of the carrier tape.

(b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.

(c) rotation of the component is limited to 20° maximum for 8 and 12 mm tapes and 10° maximum for 16 mm tapes (see Figure 2).

(d) lateral movement of the component is restricted to 0.5 mm maximum for 8 mm and 12 mm wide tape and to 1.0 mm maximum for 16 mm tape (see Figure 3).

(e) see Addendum in EIA Standard 481–D for standards relating to more precise taping requirements.

(0.561)

(0.642)



Packaging Information Performance Notes

- 1. Cover Tape Break Force: 1.0 Kg minimum.
- 2. Cover Tape Peel Strength: The total peel strength of the cover tape from the carrier tape shall be:

Tape Width	Peel Strength	
8 mm	0.1 to 1.0 Newton (10 to 100 gf)	
12 and 16 mm	0.1 to 1.3 Newton (10 to 130 gf)	

The direction of the pull shall be opposite the direction of the carrier tape travel. The pull angle of the carrier tape shall be 165° to 180° from the plane of the carrier tape. During peeling, the carrier and/or cover tape shall be pulled at a velocity of 300 ± 10 mm/minute. **3. Labeling:** Bar code labeling (standard or custom) shall be on the side of the reel opposite the sprocket holes. *Refer to EIA Standards* 556 and 624.

Figure 2 – Maximum Component Rotation

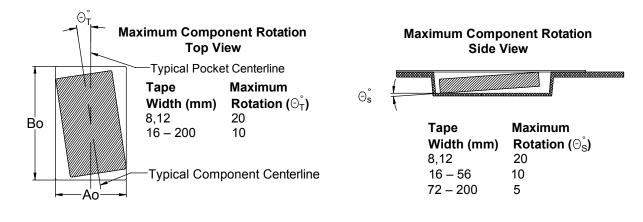


Figure 3 – Maximum Lateral Movement

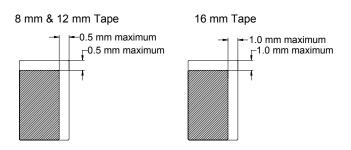


Figure 4 – Bending Radius

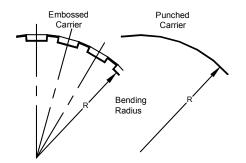
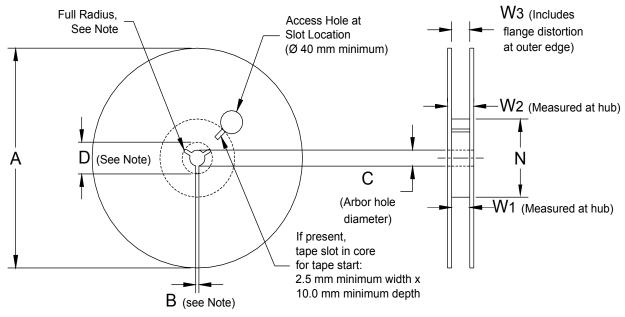




Figure 5 – Reel Dimensions



Note: Drive spokes optional; if used, dimensions B and D shall apply.

Table 5 – Reel Dimensions

Metric will govern

Constant Dimensions — Millimeters (Inches)							
Tape Size	А	B Minimum	С	D Minimum			
8 mm	178 ±0.20 (7.008 ±0.008)	1.5 (0.059)	13.0 +0.5/-0.2 (0.521 +0.02/-0.008)	20.2 (0.795)			
12 mm	or						
16 mm	330 ±0.20 (13.000 ±0.008)			(0.100)			
	Variable Dimensions — Millimeters (Inches)						
Tape Size	N Minimum	W ₁	W ₂ Maximum	W ₃			
8 mm		8.4 +1.5/-0.0 (0.331 +0.059/-0.0)	14.4 (0.567)				
12 mm	50 (1.969)	12.4 +2.0/-0.0 (0.488 +0.078/-0.0)	18.4 (0.724)	Shall accommodate tape width without interference			
16 mm	, , , , , , , , , , , , , , , , , , ,	16.4 +2.0/-0.0 (0.646 +0.078/-0.0)	22.4 (0.882)				



Figure 6 – Tape Leader & Trailer Dimensions

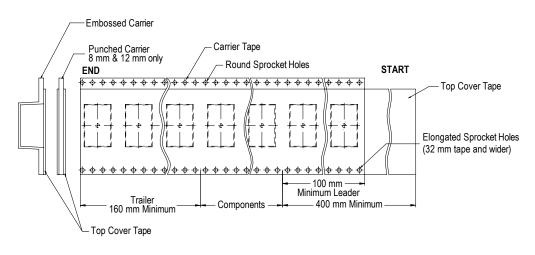
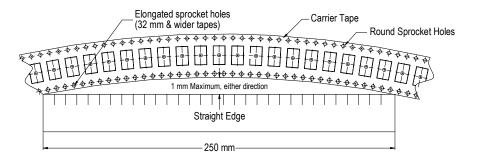


Figure 7 – Maximum Camber





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