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October 2008



## FJA4310 NPN Epitaxial Silicon Transistor

- Audio Power Amplifier
- High Current Capability : I<sub>C</sub>=10A
- High Power Dissipation
- Wide S.O.A
- Complement to FJA4210



1.Base 2.Collector 3.Emitter

## Absolute Maximum Ratings\* T<sub>a</sub> = 25°C unless otherwise noted

Symbol	Parameter	Ratings	Units	
V <sub>CBO</sub>	Collector-Base Voltage	200	V	
V <sub>CEO</sub>	Collector-Emitter Voltage	140	V	
V <sub>EBO</sub>	Emitter-Base Voltage	6	V	
IC	Collector Current (DC)	10	А	
I <sub>B</sub>	Base Current (DC)	1.5	А	
P <sub>C</sub>	Collector Dissipation (T <sub>C</sub> =25°C)	100	W	
TJ	Junction Temperature	150	°C	
T <sub>STG</sub>	Storage Temperature	- 55 ~ 150	°C	

\* These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

## **Electrical Characteristics\*** $T_a$ =25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> =5mA, I <sub>E</sub> =0	200			V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> =50mA, R <sub>BE</sub> =∞	140			V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> =5mA, I <sub>C</sub> =0	6			V
I <sub>CBO</sub>	Collector Cut-off Current	V <sub>CB</sub> =200V, I <sub>E</sub> =0			10	μA
I <sub>EBO</sub>	Emitter Cut-off Current	V <sub>EB</sub> =6V, I <sub>C</sub> =0			10	μA
h <sub>FE</sub>	* DC Current Gain	V <sub>CE</sub> =4V, I <sub>C</sub> =3A	50		180	
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> =5A, I <sub>B</sub> =0.5A			0.5	V
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> =10V, f=1MHz		250		pF
f <sub>T</sub>	Current Gain Bandwidth Product	V <sub>CE</sub> =5V, I <sub>C</sub> =1A		30		MHz

\* Pulse Test: Pulse Width $\leq$ 300 $\mu$ s, Duty Cycle $\leq$ 2%

### h<sub>FE</sub> Classification

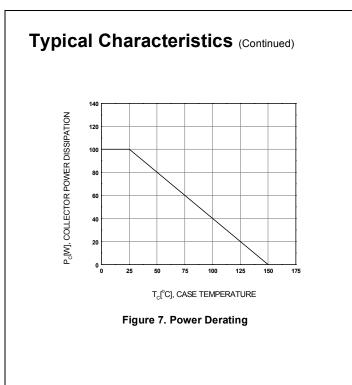
Classification	R	0	Y
h <sub>FE</sub>	50 ~ 100	70 ~ 140	90 ~ 180

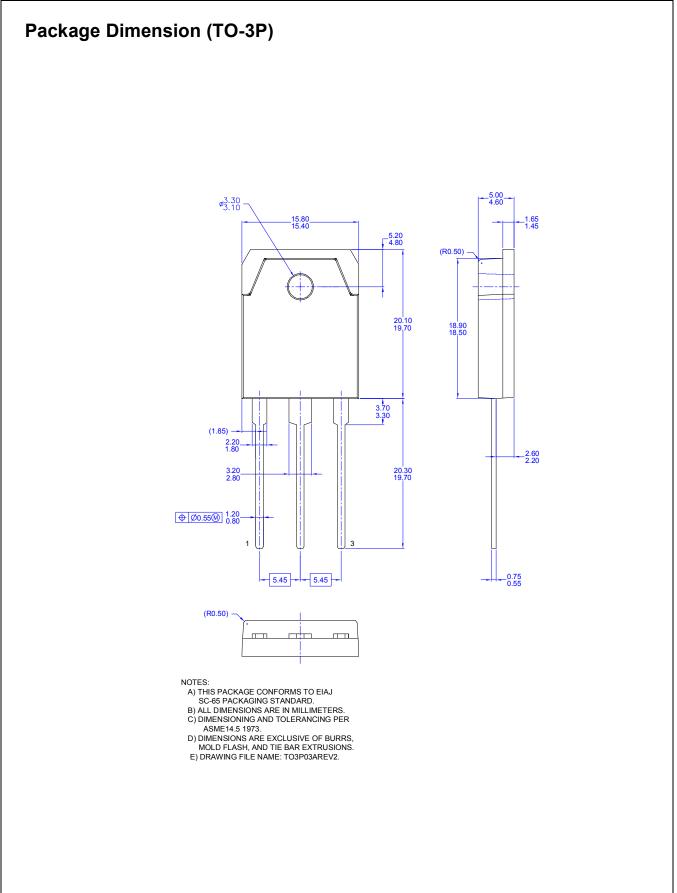
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#### **Typical Characteristics** 250mA 1000 I<sub>B</sub> = 400m/ = 200mA V\_CE = 4 V 150mA Ic [A], COLLECTOR CURRENT heed DC CURRENT GAIN I<sub>B</sub> = 100mA Ta = 25 °C Ta = 125 °C 5 $I_{B} = 50 \text{mA}$ Ta = - 25 °C 2 I<sub>B</sub> = 20mA 10 0.1 10 2 3 $\rm I_{\rm c}$ [A], COLLECTOR CURRENT V<sub>CE</sub> [V], COLLECTOR-EMITTER VOLTAGE Figure 1. Static Characterstic Figure 2. DC current Gain 3. I\_ = 10 I\_ V<sub>cE</sub>(sat) [V], SATURATION VOLTAGE $V_{CE}(sat)$ [V], SATURATION VOLTAGE 2.5 2.0 1.5 0.1 Ta = 25 1.0 - 25 °C Та 0.5 17= 10A - 5A 0.01 └─ 0.01 0.0 0.4 1.2 1.6 2.0 0.8 0.1 I<sub>c</sub> [A], COLLECTOR CURRENT I<sub>B</sub> [A], BASE CURRENT Figure 3. V<sub>CE</sub>(sat) vs. I<sub>B</sub> Characteristics Figure 4. Collector-Emitter Saturation Voltage V<sub>CE</sub> = 4 V t=10ms I\_ (Pulse) I<sub>c</sub> [A], COLLECTOR CURRENT Ic [A], COLLECTOR CURRENT I (DC t=100 Ta = 25 °C 2 Ta = 125 T<sub>c</sub> = 25°C Single Pulse 25 °C 0.1 0.0 10 100 0.5 1.0 1.5 V<sub>CE</sub> [V], COLLECTOR-EMITTER VOLTAGE V<sub>BE</sub> [V], Base-Emitter On VOLTAGE Figure 6. Forward Bias Safe Operating Area Figure 5. Base-Emitter On Voltage

FJA4310 — NPN Epitaxial Silicon Transistor

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