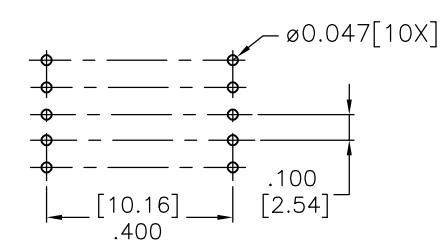
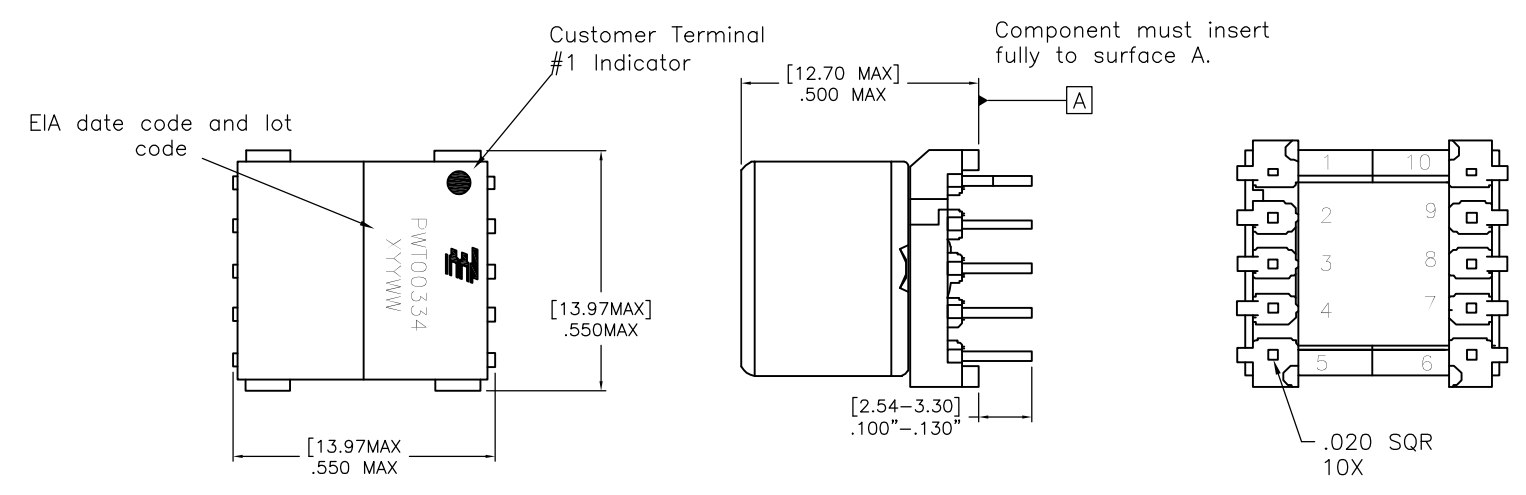


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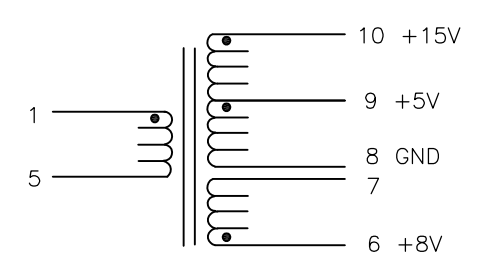
LOC	DIST	REVISIONS					
		P	LTR	DESCRIPTION	DATE	DWN	APVD
E	B		BA	NEW DRAWING	15MAR2011	PSK	JC

Electrical Specifications at +20°C unless noted otherwise

Test Description	Limit	Units	Tol	Notes
Inductance	51.00	μH	±5%	1-5, 100kHz, 100mVAC, Ls.
DC Resistance	0.41	Ω	Max	1-5
DC Resistance	0.18	Ω	Max.	10-9
DC Resistance	0.04	Ω	Max.	9-8
DC Resistance	0.19	Ω	Max.	6-7
Turns Ratio	2.64 : 1.00	--	±1%	( 1-5 ) : ( 10-9 ), 100kHz, 100mVAC.
Turns Ratio	4.83 : 1.00	--	±1%	( 1-5 ) : ( 9-8 ), 100kHz, 100mVAC.
Turns Ratio	3.22 : 1.00	--	±1%	( 1-5 ) : ( 6-7 ), 100kHz, 100mVAC.
Dielectric	1500	VAC	500μA	1-10, 1 second test, 500μA maximum leakage current.
Dielectric	1500	VAC	500μA	1-6, 1 second test, 500μA maximum leakage current.
Dielectric	2500	VAC	500μA	6-core, 1 second test, 500μA maximum leakage current.



Terminal Dimensions  
 Customer to determine PCB pad dimensions.



Schematic

NOTES:

- Operating Temperature Range: -40°C to +85°C
- Terminal Plating is Hot-dipped SnAgCu over Ni
- 265°C Maximum wave solder temperature per J-STD-020
- All dimensions are in inches [millimeters].

**RoHS Compliant**

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN PRASAD K 15MAR2011	<b>STE</b> TE Connectivity		
		CHK ALLAN P 15MAR2011			
DIMENSIONS: INCHES [MM]		APVD J CATCHPOLE 15MAR2011	NAME MGPWT-00334-P		
		PRODUCT SPEC		RESTRICTED TO	
		APPLICATION SPEC			
MATERIAL		WEIGHT	SIZE A3	CAGE CODE 00779	DRAWING NO C-2-1879391-5
		CUSTOMER DRAWING		SCALE NTS	SHEET 1 OF 1