

## **N-channel SiC power MOSFET**

V <sub>DSS</sub>	1200V
R <sub>DS(on)</sub> (Typ.)	18mΩ
I <sub>D</sub> <sup>*1</sup>	81A
$P_{D}$	312W

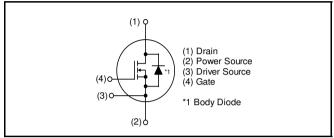
#### Outline



#### Features

- 1) Low on-resistance
- 2) Fast switching speed
- 3) Fast reverse recovery
- 4) Easy to parallel
- 5) Simple to drive
- 6) Pb-free lead plating; RoHS compliant

#### •Inner circuit



Please note Driver Source and Power Source are not exchangeable. Their exchange might lead to malfunction.

# Application

- Solar inverters
- DC/DC converters
- · Switch mode power supplies
- · Induction heating
- Motor drives

## Packaging specifications

	Packing	Tube
	Reel size (mm)	-
Typo	Tape width (mm)	-
Туре	Basic ordering unit (pcs)	30
	Taping code	C15
	Marking	SCT4018KR

# ● **Absolute maximum ratings** (T<sub>vj</sub> = 25°C unless otherwise specified.)

Parameter		Symbol	Value	Unit	
Drain - source voltage		$V_{DSS}$	1200	V	
Continuous drain	V V	$T_c = 25^{\circ}C$	, , *1	81	Α
and source current	$V_{GS} = V_{GS\_on}$	T <sub>c</sub> = 100°C	l <sub>D</sub> , l <sub>S</sub> *1	57	А
Pulsed drain current	$V_{GS} = V_{GS\_on}$	$T_c = 25^{\circ}C$	I <sub>D,pulse</sub> *2	179	Α
Body diode pulsed forward	ard current	T <sub>c</sub> = 25°C	I <sub>S,pulse</sub> *1,*3	81	Α
Body diode surge forward current		$V_{GS} = 0 V$	I <sub>S,pulse</sub> *1,*4	179	Α
Gate - source voltage (DC)		$V_{GSS\_DC}$	-4 to +21	V	
Gate - source surge voltage (t <sub>surge</sub> < 300ns)		$V_{\rm GSS\_surge}^{*5}$	-4 to +23	V	
Recommended turn-on gate - source drive voltage		ive voltage	${\sf V_{GS\_on}}^{*6}$	+15 to +18	V
Recommended turn-off gate - source drive voltage		$V_{GS\_off}$	0	V	
Virtual junction temperature		$T_{vj}$	175	°C	
Range of storage temperature		$T_{stg}$	-40 to +175	°C	

# ullet Electrical characteristics (T<sub>vj</sub> = 25°C unless otherwise specified)

Doromotor	Cymbol	Conditions		Values		
Parameter	Symbol Conditions —		Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	V	$V_{GS} = 0 \text{ V}, I_D = 18.6 \text{mA}$				V
	(BR)DSS	$T_{vj} = 25^{\circ}C$	1200	-	-	V
		$V_{GS} = 0 \text{ V}, V_{DS} = 1200 \text{V}$				
Zero Gate voltage Drain current	I <sub>DSS</sub>	$T_{vj} = 25^{\circ}C$	-	1	80	μΑ
Drain carrent		T <sub>vj</sub> = 150°C	-	10	-	
Gate - Source leakage current	I <sub>GSS+</sub>	$V_{GS} = +21V$ , $V_{DS} = 0V$	-	-	100	nA
Gate - Source leakage current	I <sub>GSS-</sub>	$V_{GS} = -4V$ , $V_{DS} = 0V$	-	-	-100	nA
Gate threshold voltage	$V_{GS(th)}^{*7}$	$V_{DS} = 10V, I_D = 22.2mA$	2.8	ı	4.8	V
		$V_{GS} = 18V, I_{D} = 42A$				
Static Drain - Source on - state resistance	R <sub>DS(on)</sub> *8	$T_{vj} = 25^{\circ}C$	-	18.0	23.4	mΩ
on oldio robotano		T <sub>vj</sub> = 150°C	-	36.0	-	
Gate input resistance	$R_{G}$	f = 1MHz, open drain	-	1	-	Ω

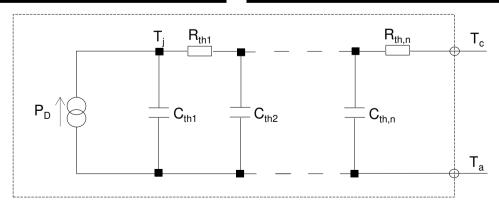
## ●Thermal resistance

Parameter	Symbol	Values			Unit
Farameter		Min.	Тур.	Max.	UTIIL
Thermal resistance, junction - case	$R_{thJC}^{}^{\star9}}$	-	0.37	0.48	K/W

●Typical Transient Thermal Characteristics

Symbol	Value	Unit
R <sub>th1</sub>	4.0 ×10 <sup>-2</sup>	
R <sub>th2</sub>	1.6 ×10 <sup>-1</sup>	K/W
R <sub>th3</sub>	1.7 ×10 <sup>-1</sup>	

Symbol	Value	Unit
C <sub>th1</sub>	1.2 ×10 <sup>-3</sup>	
$C_{th2}$	4.6 ×10 <sup>-3</sup>	Ws/K
C <sub>th3</sub>	2.6 ×10 <sup>-2</sup>	



# ullet Electrical characteristics (T<sub>vj</sub> = 25°C unless otherwise specified)

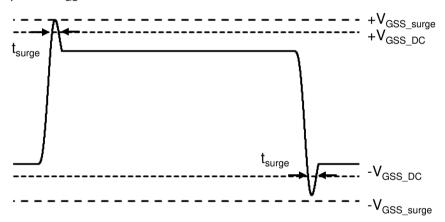
Doromotor	Cumbal	ol Conditions -	Values			Lloit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Transconductance	$g_{fs}^{*8}$	$V_{DS} = 10V, I_{D} = 42A$	-	22	-	S
Input capacitance	$C_{iss}$	$V_{GS} = 0V$	-	4532	-	
Output capacitance	$C_{oss}$	V <sub>DS</sub> = 800V	ı	129	-	pF
Reverse transfer capacitance	$C_{rss}$	f = 1MHz	ı	9	-	
Effective output capacitance, energy related	C <sub>o(er)</sub>	$V_{GS} = 0V$ $V_{DS} = 0V \text{ to } 800V$	ı	156	-	pF
Total Gate charge	Q <sub>g</sub> *8	$V_{DS} = 800V$ $I_{D} = 42A$	-	170	-	
Gate - Source charge	Q <sub>gs</sub> *8	V <sub>GS</sub> = 18V	-	32	-	nC
Gate - Drain charge	Q <sub>gd</sub> *8	See Fig. 1-1, 1-2.	-	52	-	
Turn - on delay time	t <sub>d(on)</sub> *8	$V_{DS} = 800V$ $I_{D} = 42A$	-	13	-	
Rise time	t <sub>r</sub> *8	$V_{GS} = +18V / 0V$	-	21	-	ns
Turn - off delay time	$t_{d(off)}$ *8	$R_G = 3.3\Omega$ , L = 250µH $E_{on}$ includes diode	ı	50	-	113
Fall time	t <sub>f</sub> *8	reverse recovery $L_{\sigma} = 50$ nH, $C_{\sigma} = 10$ pF	ı	11	ı	
Turn - on switching loss	E <sub>on</sub> *8	See Fig. 2-1, 2-2, 2-3.	-	520	-	1
Turn - off switching loss	E <sub>off</sub> *8		-	142	-	μJ

# ●Body diode electrical characteristics (Source-Drain) (T<sub>vi</sub> = 25°C unless otherwise specified)

Parameter	Cymbol	Conditions	Values			Unit
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Forward voltage	V <sub>SD</sub> *8	$V_{GS} = 0V, I_{S} = 42A$	ı	3.3	ī	V
Reverse recovery time	t <sub>rr</sub> *8	$I_F = 42A$ $V_B = 800V$	ı	12	ı	ns
Reverse recovery charge	Q <sub>rr</sub> *8	di/dt = 4700A/µs	-	252	-	nC
Peak reverse recovery current	I <sub>rrm</sub> *8	$L_{\sigma} = 50$ nH, $C_{\sigma} = 10$ pF See Fig. 3-1, 3-2.	-	44	-	Α

<sup>\*1</sup> Limited by maximum T<sub>vj</sub> and for Max. R<sub>thJC</sub>.

## \*5 Example of acceptable V<sub>GS</sub> waveform



Please note especially when using driver source that  $V_{\text{GSS\_surge}}$  must be in the range of absolute maximum rating.

- \*6 Please be advised not to use SiC-MOSFETs with V<sub>GS</sub> below 10V as doing so may cause thermal runaway.
- \*7 Tested after applying  $V_{GS} = 21V$  for 100ms.
- \*8 Pulsed
- \*9 Measured conformable to JESD51-14.

See the application note "rthjc\_measurement\_and\_usage\_an-e.pdf". Link

URL: https://fscdn.rohm.com/en/products/databook/applinote/discrete/common/rthjc\_measurement\_and\_usage\_an-e.pdf

<sup>\*2</sup> Pulse width and duty cycle are limited by  $T_{v_j,max}$ .

<sup>\*3</sup> Only for body-diode, Repititive pulse, PW ≤ 1.5µs, Duty cycle ≤ 5%

<sup>\*4</sup> When used as a protective function, PW  $\leq$  10 $\mu$ s

Fig.1 Power Dissipation Derating Curve

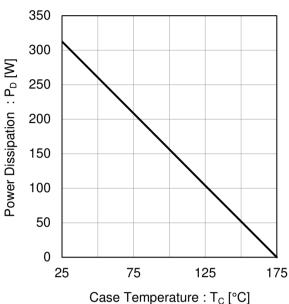


Fig.2 Maximum Safe Operating Area

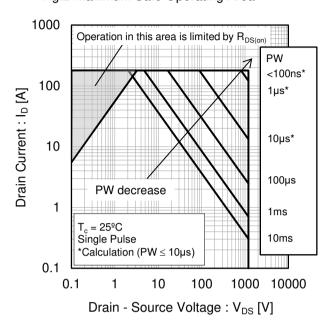
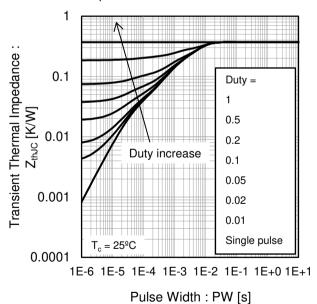
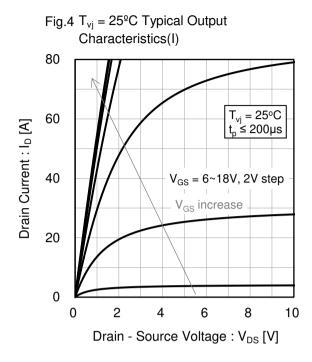


Fig.3 Typical Transient Thermal Impedance vs. Pulse Width





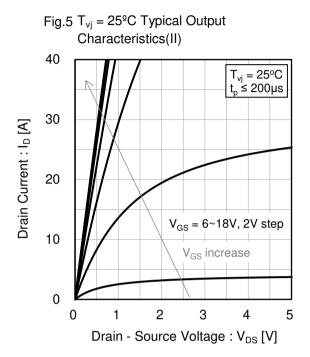
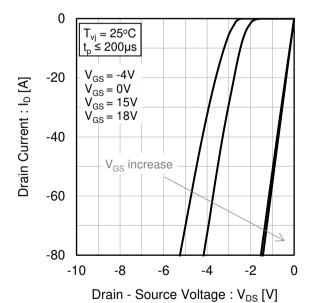
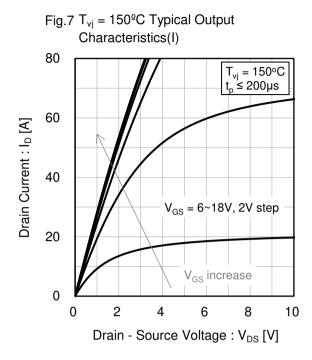
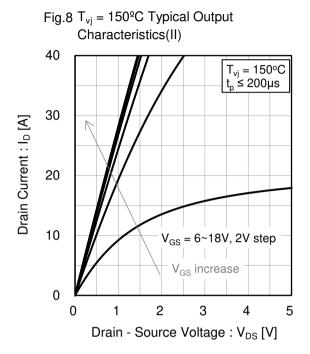
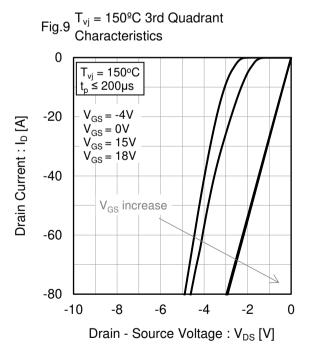


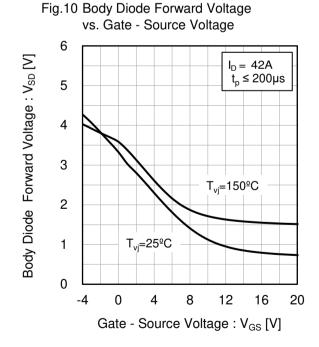
Fig.6 T<sub>vi</sub> = 25°C 3rd Quadrant Characteristics











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Fig.11 Typical Transfer Characteristics (I)

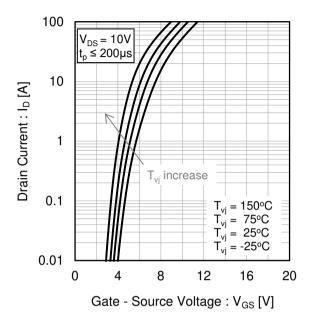


Fig.12 Typical Transfer Characteristics (II)

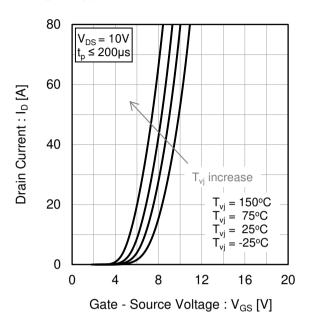


Fig.13 Gate Threshold Voltage vs. Virtual Junction Temperature

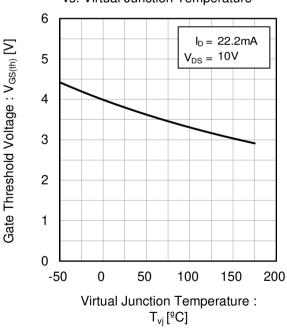


Fig.14 Transconductance vs. Drain Current

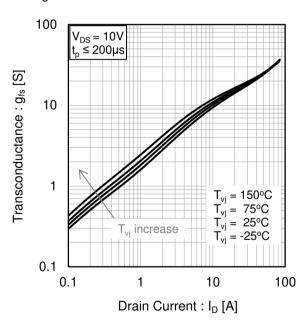


Fig.15 Static Drain - Source On - State
Resistance vs. Gate - Source Voltage

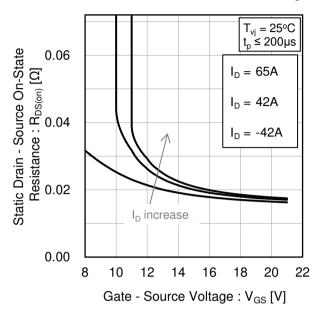


Fig.16 Static Drain - Source On - State Resistance vs. Virtual Junction Temperature

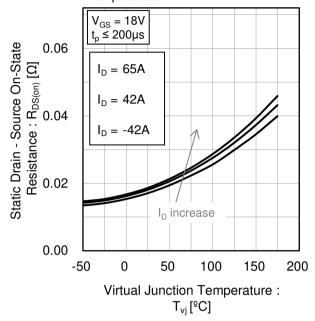


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current

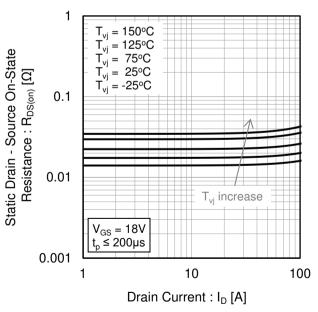
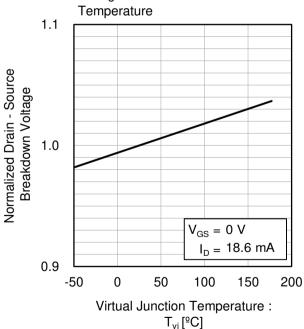
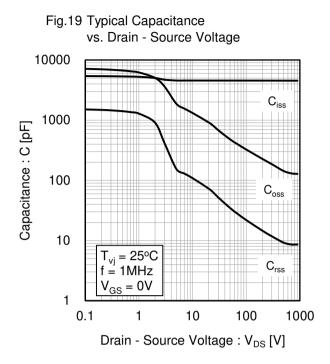


Fig.18 Normalized Drain - Source Breakdown Voltage vs. Virtual Junction





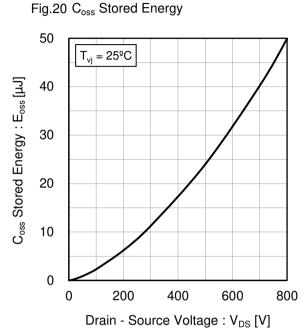


Fig.21 Dynamic Input Characteristics

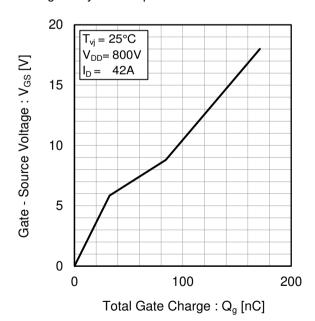


Fig.22 Typical Switching Time

vs. External Gate Resistance 250  $T_{vi} = 25^{\circ}C$  $I_D =$ 42A  $V_{DD} = 800V$ 200  $V_{GS} = +18V/0V$  $t_{d(off)}$ Switching Time:t[ns]  $L = 250 \mu H$ 150 100  $t_{d(on)}$ 50 0 0 5 10 15 20

External Gate Resistance :  $R_G[\Omega]$ 

vs. Drain - Source Voltage 2000  $T_{vi} = 25^{\circ}C$  $I_D =$ 42A  $V_{GS} = +18V/0V$  $R_G = 3.3\Omega$ 1500 Switching Loss: E [µJ]  $L = 250 \mu H$ 1000  $E_{on}$ 500 0 200 600 400 800 Drain - Source Voltage: V<sub>DS</sub> [V]

Fig.23 Typical Switching Loss

Fig.24 Typical Switching Loss vs. Drain Current

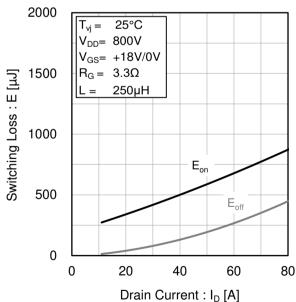
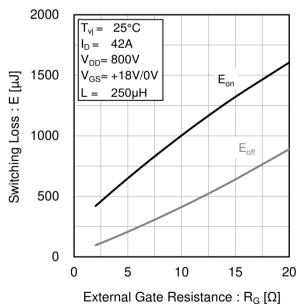


Fig.25 Typical Switching Loss vs. External Gate Resistance



## • Measurement circuits and waveforms

Fig.1-1 Gate Charge Measurement Circuit

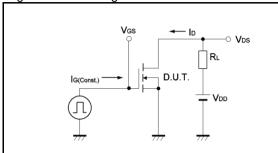


Fig.2-1 Switching Characteristics Measurement Circuit

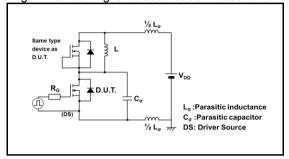


Fig.2-3 Waveforms for Switching Energy Loss

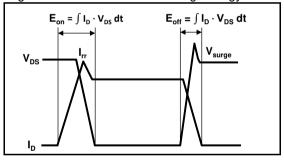


Fig.3-1 Reverse Recovery Time Measurement Circuit

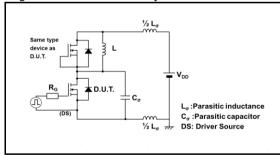


Fig.1-2 Gate Charge Waveform

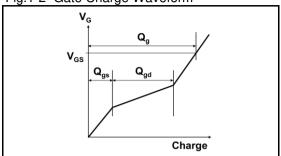


Fig.2-2 Waveforms for Switching Time

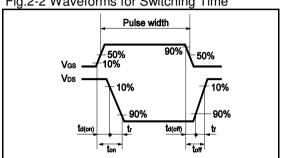
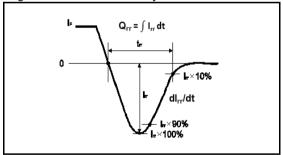
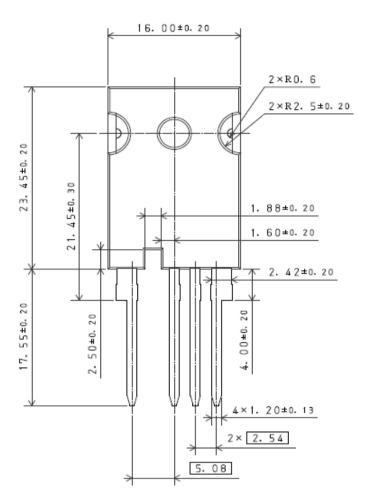
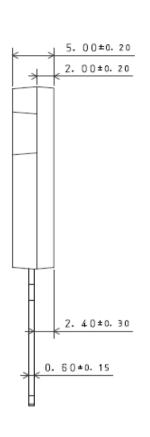


Fig.3-2 Reverse Recovery Waveform

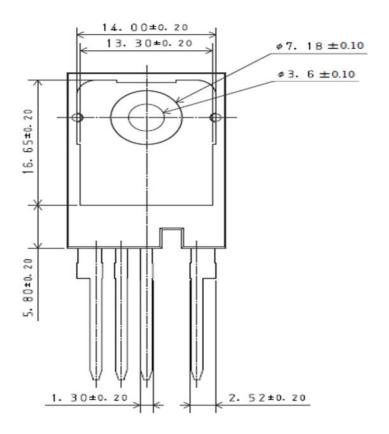


## ●Package Dimensions



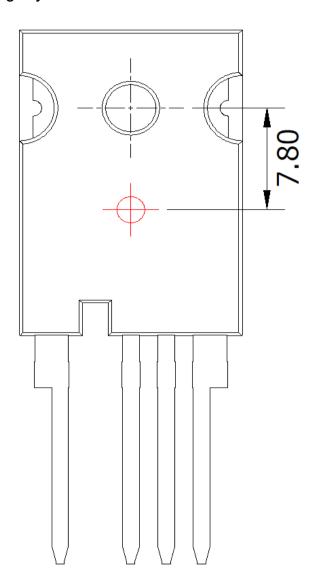


Unit: mm



Unit: mm

# **●**Die Bonding Layout





- •Front view of the packaging.
- •Dimensions are design values.
- ·If the heat sink is to be installed, it should be in contact with the die bonding point.

Unit: mm

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