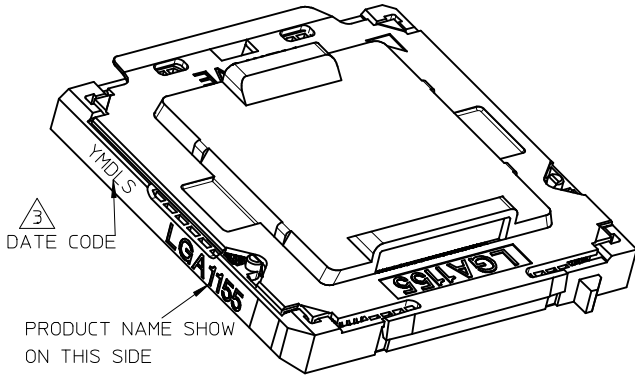
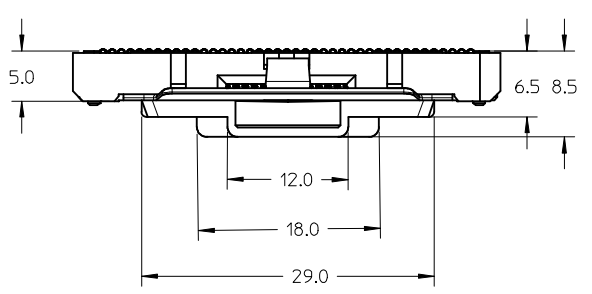
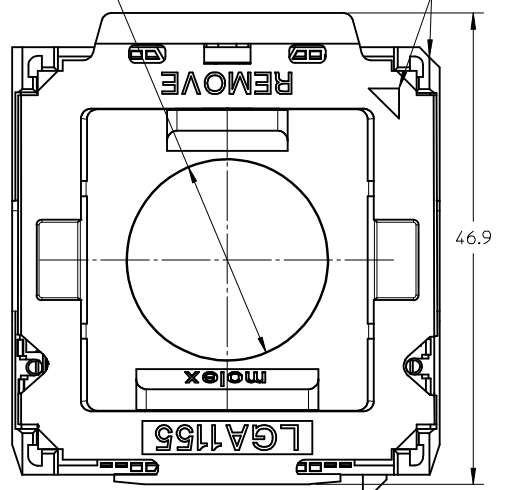


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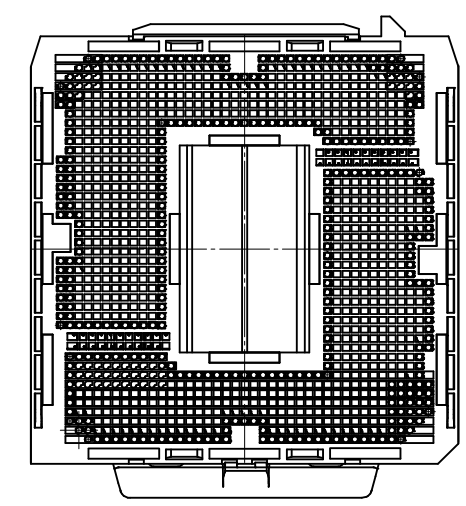
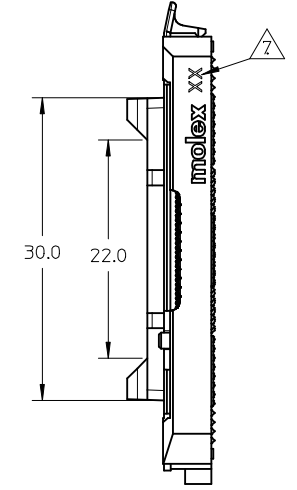


- NOTES:
- MATERIAL:  
HOUSING AND CAP: HIGH TEMPERATURE THERMOPLASTIC, UL94V-0  
CONTACT: HIGH STRENGTH COPPER ALLOY  
SOLDER BALL: LEAD FREE SnAg3.0Cu0.5
  - PLATING:  
50u" MIN. NICKEL AT PADDLE AND CONTACT AREA.  
GOLD PLATING THICKNESS AT CONTACT AREA REFER TO TABLE.
  - DATE CODE "YMDLS" IS DEFINED AS:  
"YEAR/MONTH/DAY/\*\*".("\*\*" IS MOLEX INTERNAL CONTROLLED).
  - 47596\*\*\*\* COMPLIANT TO RoHS DIRECTIVE 2002/95/EC AND ELV DIRECTIVE 2000/53/EC.
  - PRODUCT SPECIFICATION PS-47596-001 APPLIES.
  - PACKAGE DRAWING PK-47596-001 APPLIES.
  - "XX" IS FOR CAVITY # (MOLEX INTERNAL USE)

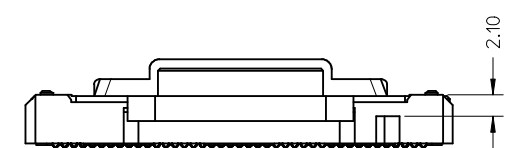
Ø20  
PICK AND PLACE  
IN THIS ZONE



TOP VIEW



BOTTOM VIEW



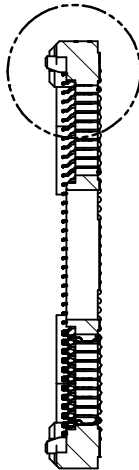
ENTER DESCRIPTION EC NO: SH2013-0312 DRWN: YXZHENG 2013/03/28 CHKD: APPR: AY IN 2013/04/16	QUALITY SYMBOLS $F_A=0$ $F_G=0$ $F_P=0$	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE	SCALE	DESIGN UNITS	THIRD ANGLE PROJECTION	
				IN/MM	2:1	INCH		
			mm	INCH	DRAWN BY	DATE	TITLE	
			4 PLACES	± ---	± ---	VLV	2008/12/11	LGA1155 SOCKET
		3 PLACES	± ---	± ---	CHECKED BY	DATE		
		2 PLACES	± 0.25	± ---	JKACHL IC	2008/12/11		
		1 PLACE	± 0.25	± ---	APPROVED BY	DATE		
		ANGULAR ± 1 °		MATERIAL NO.		DOCUMENT NO.		
		DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		SEE TABLE		SD-47596-200		
				SIZE	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION			
				A3				
					MOLEX INCORPORATED		SHEET NO.	
							1 OF 3	

9 8 7 6 5 4 3 2 1

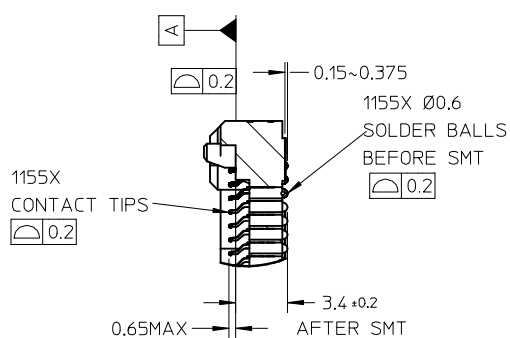
1155x CONTACT POINTS  
FOR PACKAGE LANDS

$\varnothing 0.25$  A|B|C

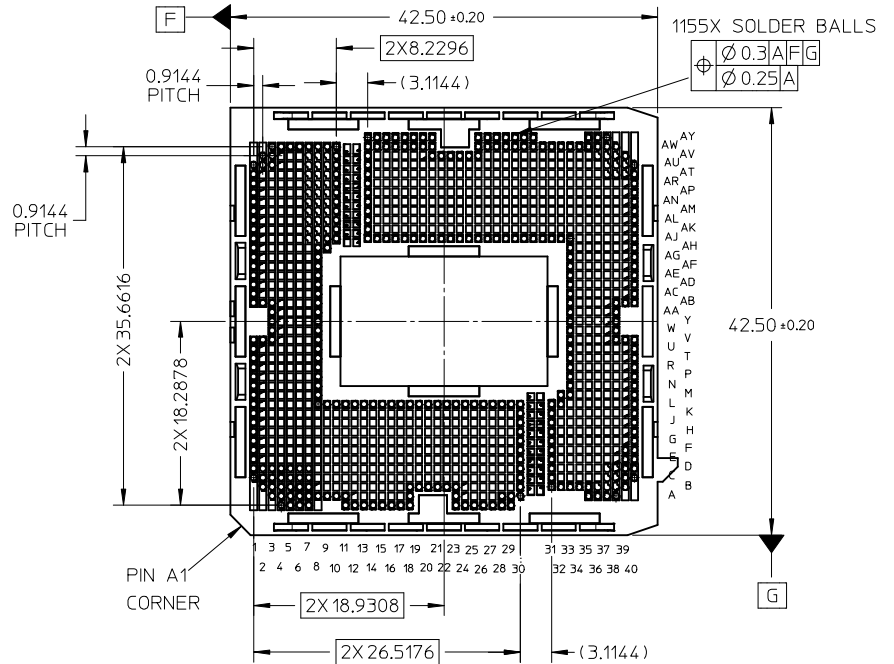
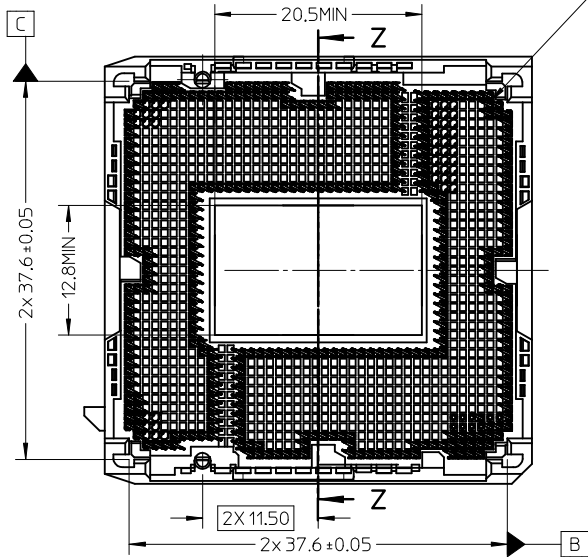
DETAIL1



SECTION Z-Z

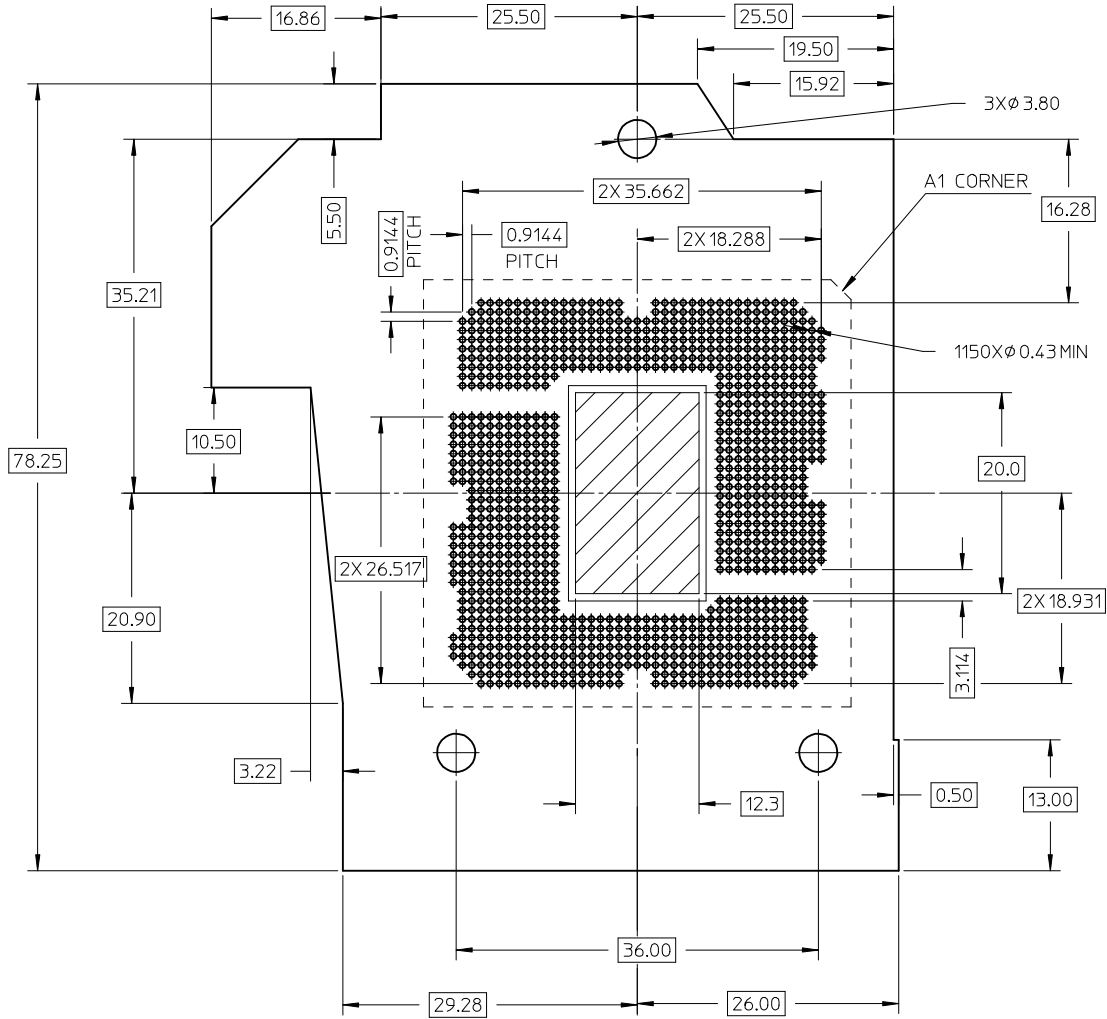


DETAIL1



PART NUMBER	PLATING THICKNESS ON CONTACT AREA
475960232	15u*(0.38um)MIN. GOLD
475960233	30u*(0.76um)MIN. GOLD

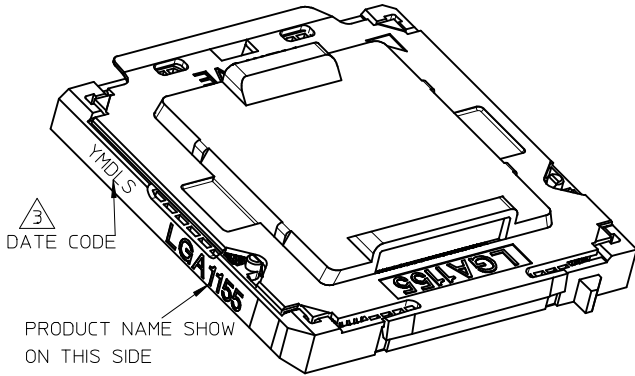
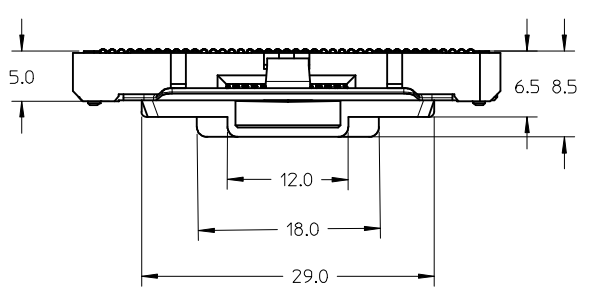
ENTER DESCRIPTION IEC NO: SH2013-0312 DRWN: YXZHENG CHKD: APPR: AYIN 2013/03/28 2013/04/16	QUALITY SYMBOLS $F_A=0$ $F_B=0$ $F_C=0$	GENERAL TOLERANCES (UNLESS SPECIFIED)	DIMENSION STYLE IN/MM	SCALE 2:1	DESIGN UNITS INCH	THIRD ANGLE PROJECTION																																
	<table border="1"> <thead> <tr> <th></th> <th>mm</th> <th>INCH</th> </tr> </thead> <tbody> <tr> <td>4 PLACES</td> <td>±---</td> <td>±---</td> </tr> <tr> <td>3 PLACES</td> <td>±---</td> <td>±---</td> </tr> <tr> <td>2 PLACES</td> <td>±0.25</td> <td>±---</td> </tr> <tr> <td>1 PLACE</td> <td>±0.25</td> <td>±---</td> </tr> </tbody> </table>		mm	INCH	4 PLACES	±---	±---	3 PLACES	±---	±---	2 PLACES	±0.25	±---	1 PLACE	±0.25	±---	<table border="1"> <thead> <tr> <th>DRAWN BY</th> <th>DATE</th> <th>TITLE</th> </tr> </thead> <tbody> <tr> <td>VLV</td> <td>2008/12/11</td> <td>LGA1155 SOCKET</td> </tr> <tr> <th>CHECKED BY</th> <th>DATE</th> <td></td> </tr> <tr> <td>JKACHLIC</td> <td>2008/12/11</td> <td></td> </tr> <tr> <th>APPROVED BY</th> <th>DATE</th> <td></td> </tr> <tr> <td></td> <td></td> <td></td> </tr> </tbody> </table>	DRAWN BY	DATE	TITLE	VLV	2008/12/11	LGA1155 SOCKET	CHECKED BY	DATE		JKACHLIC	2008/12/11		APPROVED BY	DATE					MATERIAL NO. SEE TABLE	DOCUMENT NO. SD-47596-200	SHEET NO. 2 OF 3
		mm	INCH																																			
	4 PLACES	±---	±---																																			
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PCB TOP SIDE KEEP OUT ZONE LAYOUT  
FOR REFERENCE

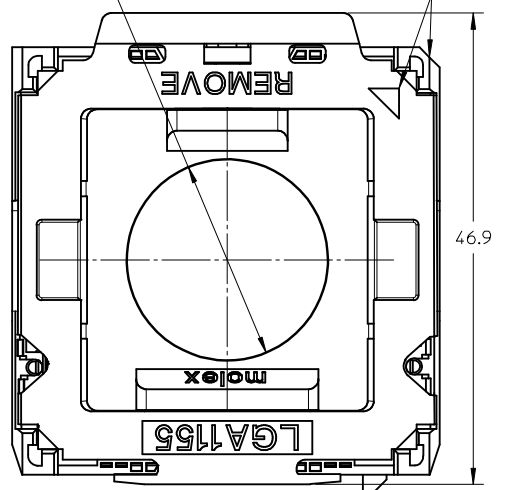
<b>ENTER DESCRIPTION</b> EC NO: SH2013-0312 DRWN: YXZHENG 2013/03/28 CHKD: CHYD APPR: AYIN 2013/04/16 REV	<b>QUALITY SYMBOLS</b> $F_A = 0$ $F_C = 0$ $F_P = 0$	<b>GENERAL TOLERANCES (UNLESS SPECIFIED)</b> <table border="1"> <thead> <tr> <th></th> <th>mm</th> <th>INCH</th> </tr> </thead> <tbody> <tr> <td>4 PLACES</td> <td>± ---</td> <td>± ---</td> </tr> <tr> <td>3 PLACES</td> <td>± ---</td> <td>± ---</td> </tr> <tr> <td>2 PLACES</td> <td>± 0.25</td> <td>± ---</td> </tr> <tr> <td>1 PLACE</td> <td>± 0.25</td> <td>± ---</td> </tr> </tbody> </table>		mm	INCH	4 PLACES	± ---	± ---	3 PLACES	± ---	± ---	2 PLACES	± 0.25	± ---	1 PLACE	± 0.25	± ---	<b>DIMENSION STYLE</b> IN/MM DRAWN BY: VLV DATE: 2008/12/11 CHECKED BY: JKACHLIC DATE: 2008/12/11 APPROVED BY: _____ DATE: _____	<b>SCALE</b> 2:1 <b>DESIGN UNITS</b> INCH <b>THIRD ANGLE PROJECTION</b>	<b>TITLE</b> LGA155 SOCKET
		mm	INCH																	
	4 PLACES	± ---	± ---																	
	3 PLACES	± ---	± ---																	
2 PLACES	± 0.25	± ---																		
1 PLACE	± 0.25	± ---																		
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THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION																				

10 9 8 7 6 5 4 3 2 1

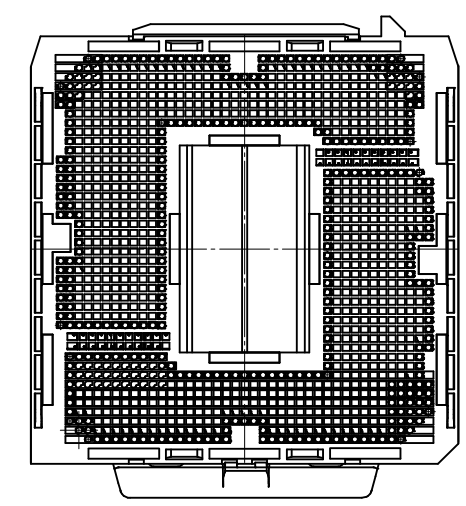
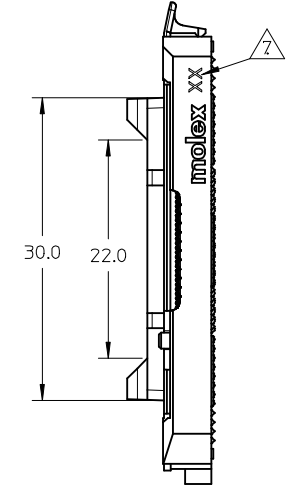


- NOTES:
- MATERIAL:  
HOUSING AND CAP: HIGH TEMPERATURE THERMOPLASTIC, UL94V-0  
CONTACT: HIGH STRENGTH COPPER ALLOY  
SOLDER BALL: LEAD FREE SnAg3.0Cu0.5
  - PLATING:  
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GOLD PLATING THICKNESS AT CONTACT AREA REFER TO TABLE.
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  - 47596\*\*\*\* COMPLIANT TO RoHS DIRECTIVE 2002/95/EC AND ELV DIRECTIVE 2000/53/EC.
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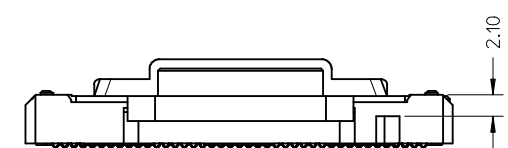
Ø 20  
PICK AND PLACE  
IN THIS ZONE



TOP VIEW



BOTTOM VIEW



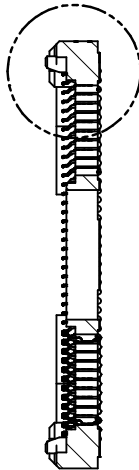
ENTER DESCRIPTION EC NO: SH2013-0312 DRWN: YXZHENG 2013/03/28 CHKD: APPR: AY IN 2013/04/16	QUALITY SYMBOLS F <sub>A</sub> =0 F <sub>G</sub> =0 F <sub>P</sub> =0	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE IN/MM		SCALE 2:1	DESIGN UNITS INCH	THIRD ANGLE PROJECTION		
			mm	INCH	DRAWN BY VLV	DATE 2008/12/11	TITLE LGA1155 SOCKET			
		4 PLACES	± ---	± ---	CHECKED BY JKACHL IC	DATE 2008/12/11	MATERIAL NO. SEE TABLE			
		3 PLACES	± ---	± ---	APPROVED BY	DATE				
	2 PLACES	± 0.25	± ---	DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS						
	1 PLACE	± 0.25	± ---	SIZE A3						
		ANGULAR ± 1 °		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION						

9 8 7 6 5 4 3 2 1

1155x CONTACT POINTS  
FOR PACKAGE LANDS

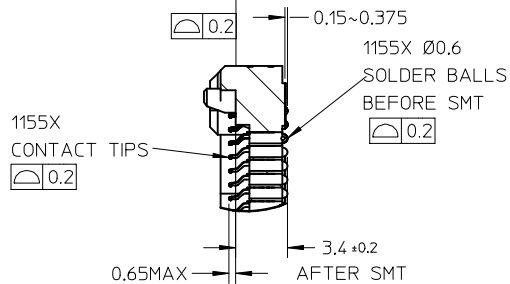
$\varnothing 0.25$  A|B|C

DETAIL1

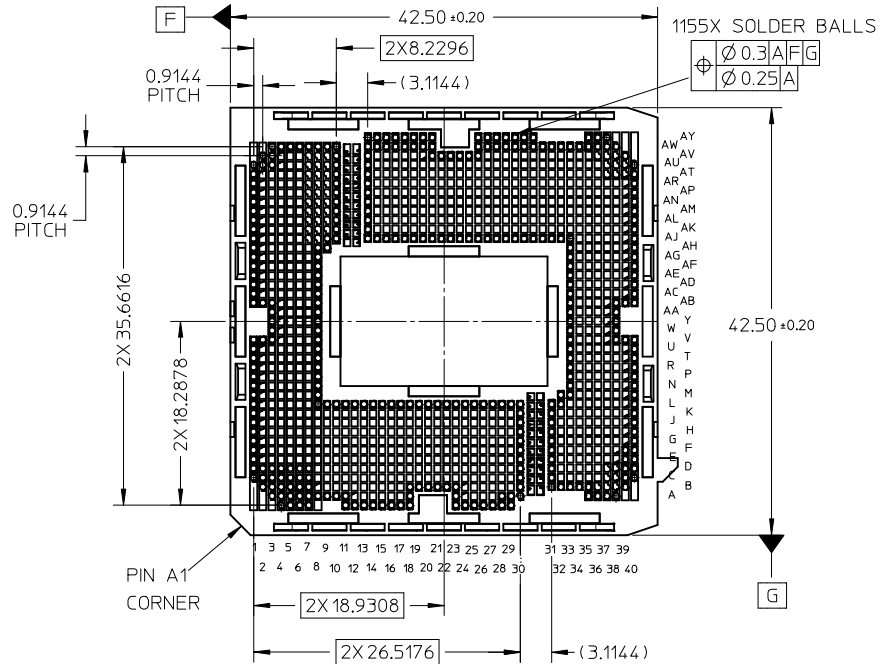
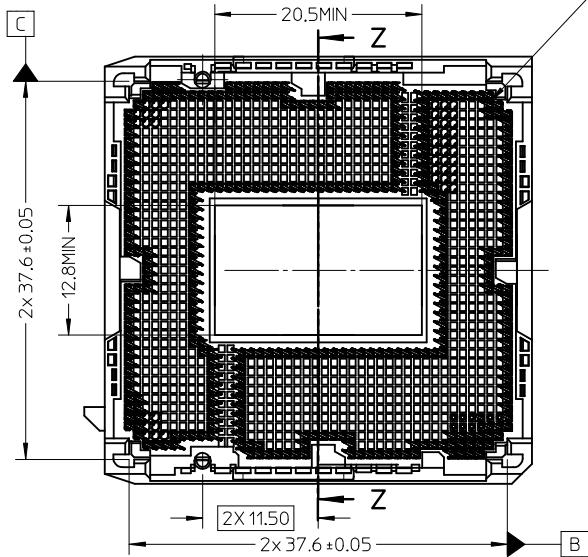


SECTION Z-Z

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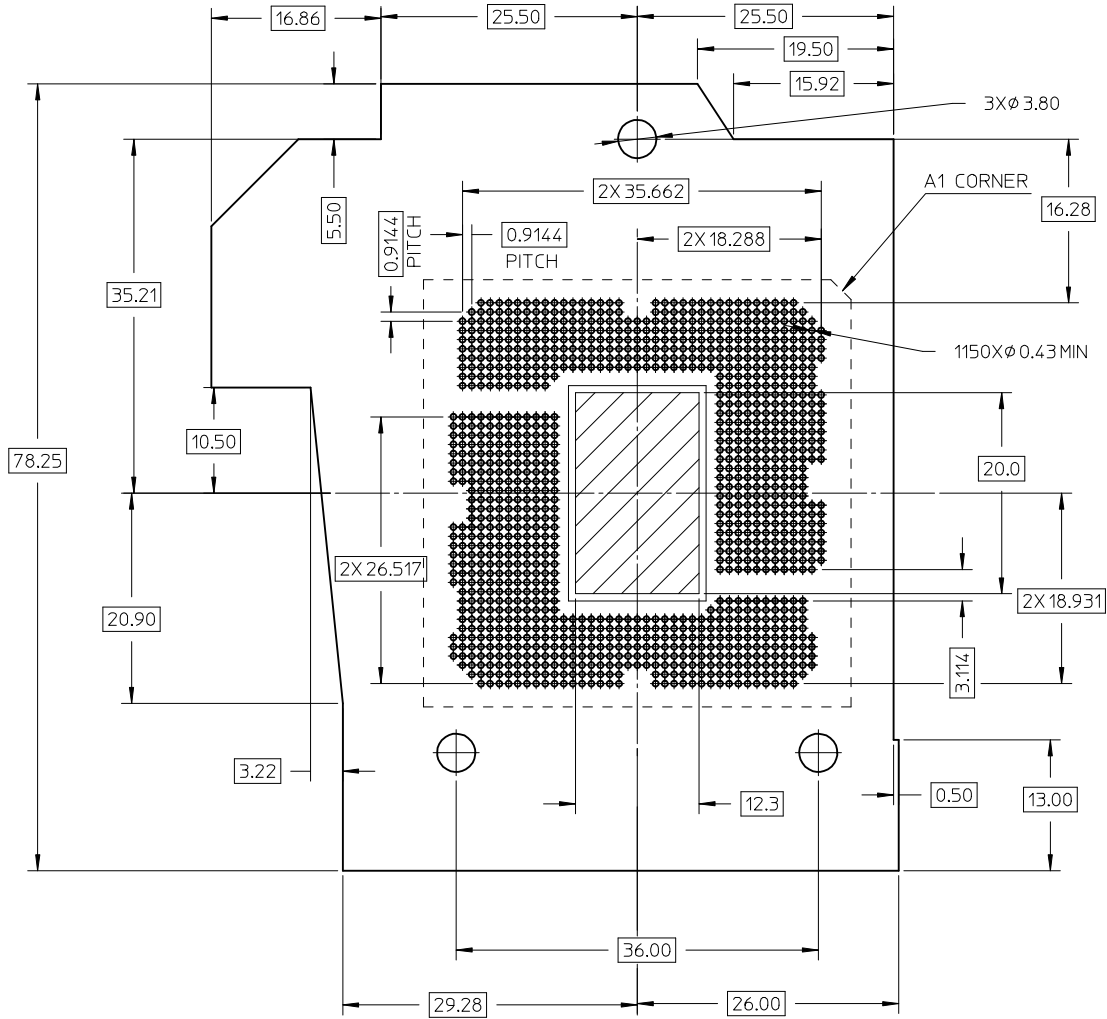


DETAIL1



PART NUMBER	PLATING THICKNESS ON CONTACT AREA
475960232	15u*(0.38um)MIN. GOLD
475960233	30u*(0.76um)MIN. GOLD

ENTER DESCRIPTION EC NO: SH2013-0312 DRWN:YYZHENG 2013/03/28 CHKD: APPR:AYIN 2013/04/16	QUALITY SYMBOLS F <sub>A</sub> =0 F <sub>E</sub> =0 F <sub>B</sub> =0	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE	SCALE	DESIGN UNITS	THIRD ANGLE PROJECTION
		mm	INCH	IN/MM	2:1	INCH	
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	4 PLACES	± ---	± ---	DRAWN BY	DATE	TITLE	
	3 PLACES	± ---	± ---	VLV	2008/12/11	LGA1155 SOCKET	
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	1 PLACE	± 0.25	± ---	JKACHLIC	2008/12/11	DOCUMENT NO. SD-47596-200	
ANGULAR ± 1°				APPROVED BY	DATE	SHEET NO. 2 OF 3	
				MATERIAL NO.	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION		
				SIZE	SEE TABLE		



PCB TOP SIDE KEEP OUT ZONE LAYOUT  
FOR REFERENCE

<b>ENTER DESCRIPTION</b> EC NO: SH2013-0312 DRWN: YXZHENG 2013/03/28 CHKD: CHKD: APPR: AYIN 2013/04/16 REV	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)	DIMENSION STYLE	SCALE	DESIGN UNITS	THIRD ANGLE PROJECTION															
	$F_A = 0$ $F_C = 0$ $F_P = 0$	<table border="1"> <thead> <tr> <th></th> <th>mm</th> <th>INCH</th> </tr> </thead> <tbody> <tr> <td>4 PLACES</td> <td>± ---</td> <td>± ---</td> </tr> <tr> <td>3 PLACES</td> <td>± ---</td> <td>± ---</td> </tr> <tr> <td>2 PLACES</td> <td>± 0.25</td> <td>± ---</td> </tr> <tr> <td>1 PLACE</td> <td>± 0.25</td> <td>± ---</td> </tr> </tbody> </table>		mm	INCH	4 PLACES	± ---	± ---	3 PLACES	± ---	± ---	2 PLACES	± 0.25	± ---	1 PLACE	± 0.25	± ---	IN/MM	2:1	INCH	
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			MATERIAL NO.	DOCUMENT NO.	SHEET NO.																
			SEE TABLE	SD-47596-200	3 OF 3																
			SIZE	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION																	
			A3																		