

Off-Line Quasi-Resonant Switching Regulators

Features and Benefits

- Quasi-resonant topology IC ⇒ Low EMI noise and soft switching
- Bottom-skip mode ⇒ Improved system efficiency over the entire output load by avoiding increase of switching frequency
- Auto-Standby mode ⇒ Lowers input power at very light output load condition
- Avalanche-guaranteed MOSFET \Rightarrow Improves system-level reliability and does not require V_{DSS} derating
- 500 V_{DSS} / 0.36 Ω $R_{DS(on)}$
- Various protections ⇒ Improved system-level reliability
 - Pulse-by-pulse drain overcurrent limiting
 - Overvoltage Protection (bias winding voltage sensing), with latch
 - Overload Protection with latch
 - Maximum on-time limit

Package: 7-Pin TO-3P



Description

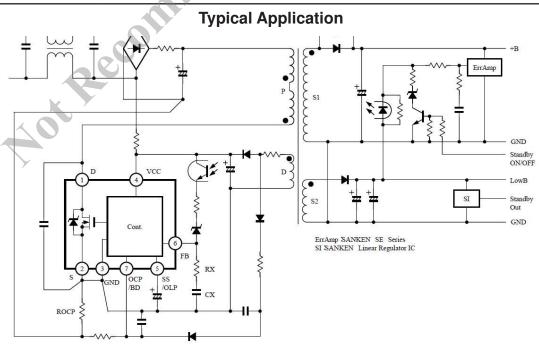
The STR-X6737 is a quasi-resonant topology IC designed for SMPS applications. It shows lower EMI noise characteristics than conventional PWM solutions, especially at greater than 2 MHz. It also provides a soft-switching mode to turn on the internal MOSFET at close to zero voltage ($V_{\rm DS}$ bottom point) by use of the resonant characteristic of primary inductance and a resonant capacitor.

The package is a fully molded TO-3P, which contains the controller chip (MIC) and MOSFET, enabling output power up to 290 W at 120 VAC input. The bottom-skip mode skips the first bottom of $V_{\rm DS}$ and turns on the MOSFET at the second bottom point, to minimize an increase of operating frequency at light output load, improving system-level efficiency over the entire load range.

There are two standby modes available to reduce the input power under very light load conditions. The first is Auto-Standby mode, which is internally triggered by periodic sensing, and the other is a manual standby mode, which is executed by clamping the secondary output. In general applications, the manual standby mode reduces the input power further compared to Auto-Standby mode.

Continued on the next page...

Not to scale



STR-X6737

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Description (continued)

The soft-start mode minimizes surge voltage and reduces power stress to the MOSFET and to the secondary rectifying diodes during the start-up sequence. Various protections such as overvoltage, overload, overcurrent, maximum on-time protections and avalanche-energyguaranteed MOSFET secure good system-level reliability.

- Set Top Box
- LCD PC monitor, LCD TV
- Printer, Scanner
- SMPS power supplies

Selection Guide

| Part Number | Package |
|-------------|---------|
| STR-X6737 | TO-3P |

| guaranteed MOSFET secure good | l system-level re | eliability. | | |
|--|--|---|--|---------------------------------------|
| Applications include the following: • Set Top Box • LCD PC monitor, LCD TV • Printer, Scanner • SMPS power supplies Selection Guide Part Number Package STR-X6737 TO-3P | | | | |
| Selection Guide | | | | |
| Part Number | Packag | ie | | |
| STR-X6737 | TO-3P | 4 0 | | |
| Absolute Maximum Ratings Parameter | at T _A = 25°C | Conditions | Rating | Unit |
| Drain Current ¹ I _D peak | | Single pulse | 22 | |
| Maximum Switching Current ² I _{Dmax} | | T _A = -20°C to 125°C | | Α |
| | | | 22 | A A |
| | E _{AS} | | | |
| Input Voltage for Controller (MIC) | E _{AS} | Single pulse, V _{DD} = 30 V, L = 50 mH, I _{Lpeak} = 3.0 A | 22 | Α |
| | E _{AS} | | 22 239 | A mJ |
| Input Voltage for Controller (MIC) | E _{AS} | Single pulse, V _{DD} = 30 V, L = 50 mH, I _{Lpeak} = 3.0 A | 22 239 35 | A mJ V |
| Input Voltage for Controller (MIC) SS/OLP Terminal Voltage FB Terminal Inflow Current FB Terminal Voltage | E _{AS} V _{CC} V _{SSOLP} | | 22 239 35 -0.5 to 6.0 | A mJ V V mA V |
| Input Voltage for Controller (MIC) SS/OLP Terminal Voltage FB Terminal Inflow Current | E _{AS} V _{CC} V _{SSOLP} I _{FB} | Single pulse, V_{DD} = 30 V, L = 50 mH, I_{Lpeak} = 3.0 A I_{FB} within the limits of I_{FB} | 22 239 35 -0.5 to 6.0 10 -0.5 to 9.0 -1.5 to 5.0 | A mJ V V mA V |
| Input Voltage for Controller (MIC) SS/OLP Terminal Voltage FB Terminal Inflow Current FB Terminal Voltage OCP/BD Terminal Voltage | EAS VCC VSSOLP IFB VFB VOCPBD | Single pulse, V_{DD} = 30 V, L = 50 mH, I_{Lpeak} = 3.0 A I_{FB} within the limits of I_{FB} With infinite heatsink | 22 239 35 -0.5 to 6.0 10 -0.5 to 9.0 -1.5 to 5.0 | A mJ V V MA V V W |
| Input Voltage for Controller (MIC) SS/OLP Terminal Voltage FB Terminal Inflow Current FB Terminal Voltage OCP/BD Terminal Voltage MOSFET Power Dissipation ⁴ | EAS VCC VSSOLP IFB VFB VOCPBD PD1 | Single pulse, V_{DD} = 30 V, L = 50 mH, I_{Lpeak} = 3.0 A I_{FB} within the limits of I_{FB} With infinite heatsink Without heatsink | 22 239 35 -0.5 to 6.0 10 -0.5 to 9.0 -1.5 to 5.0 44 2.8 | A mJ V V MA V V W W |
| Input Voltage for Controller (MIC) SS/OLP Terminal Voltage FB Terminal Inflow Current FB Terminal Voltage OCP/BD Terminal Voltage MOSFET Power Dissipation ⁴ Controller (MIC) Power Dissipation | EAS | Single pulse, V_{DD} = 30 V, L = 50 mH, I_{Lpeak} = 3.0 A I_{FB} within the limits of I_{FB} With infinite heatsink Without heatsink $V_{CC} \times I_{CC}$ | 22 239 35 -0.5 to 6.0 10 -0.5 to 9.0 -1.5 to 5.0 44 2.8 0.8 | M M M M M M M M M M M M M M M M M M M |
| Input Voltage for Controller (MIC) SS/OLP Terminal Voltage FB Terminal Inflow Current FB Terminal Voltage OCP/BD Terminal Voltage MOSFET Power Dissipation ⁴ Controller (MIC) Power Dissipation Operating Internal Leadframe Temper | E _{AS} V _{CC} V _{SSOLP} I _{FB} V _{FB} V _{OCPBD} P _{D1} P _{D2} ature F | Single pulse, V_{DD} = 30 V, L = 50 mH, I_{Lpeak} = 3.0 A I_{FB} within the limits of I_{FB} With infinite heatsink Without heatsink | 22 239 35 -0.5 to 6.0 10 -0.5 to 9.0 -1.5 to 5.0 44 2.8 0.8 -20 to 125 | M M V V W W W W °C |
| Input Voltage for Controller (MIC) SS/OLP Terminal Voltage FB Terminal Inflow Current FB Terminal Voltage OCP/BD Terminal Voltage MOSFET Power Dissipation ⁴ Controller (MIC) Power Dissipation Operating Internal Leadframe Temper Operating Ambient Temperature | EAS | Single pulse, V_{DD} = 30 V, L = 50 mH, I_{Lpeak} = 3.0 A I_{FB} within the limits of I_{FB} With infinite heatsink Without heatsink $V_{CC} \times I_{CC}$ | 22 239 35 -0.5 to 6.0 10 -0.5 to 9.0 -1.5 to 5.0 44 2.8 0.8 -20 to 125 -20 to 125 | A mJ V V W W W W C °C °C |
| Input Voltage for Controller (MIC) SS/OLP Terminal Voltage FB Terminal Inflow Current FB Terminal Voltage OCP/BD Terminal Voltage MOSFET Power Dissipation ⁴ Controller (MIC) Power Dissipation Operating Internal Leadframe Temper | E _{AS} V _{CC} V _{SSOLP} I _{FB} V _{FB} V _{OCPBD} P _{D1} P _{D2} ature F | Single pulse, V_{DD} = 30 V, L = 50 mH, I_{Lpeak} = 3.0 A I_{FB} within the limits of I_{FB} With infinite heatsink Without heatsink $V_{CC} \times I_{CC}$ | 22 239 35 -0.5 to 6.0 10 -0.5 to 9.0 -1.5 to 5.0 44 2.8 0.8 -20 to 125 | M M V V W W W W °C |

¹Refer to MOSFET ASO curve

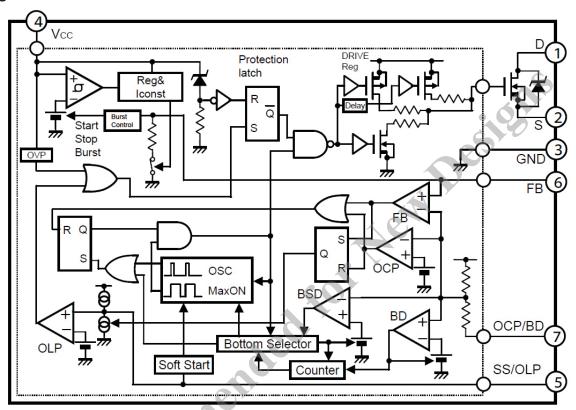
All performance characteristics given are typical values for circuit or system baseline design only and are at the nominal operating voltage and an ambient temperature, TA, of 25°C, unless otherwise stated.

²I_{DMAX} is the drain current determined by the drive voltage of the IC and the threshold voltage, V_{th}, of the MOSFET

³Refer to Avalanche Energy Derating curve

⁴Refer to MOSFET Ta-PD1 curve

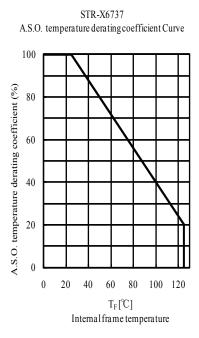
Functional Block Diagram

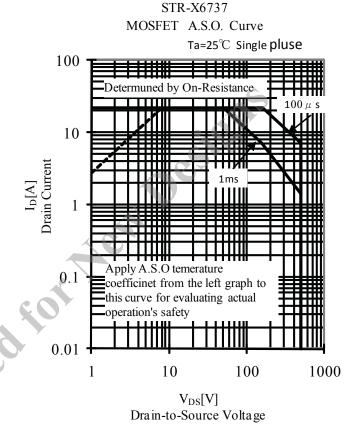


Terminal List Table

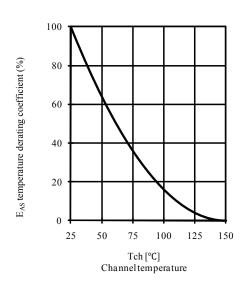
| Number | Name | Description | Functions |
|--------|--------|---|--|
| 1 | D | Drain | MOSFET drain |
| 2 | S | Source | MOSFET source |
| 3 | GND | Ground terminal | Ground |
| 4 | VCC | Power supply terminal | Input of power supply for control circuit |
| 5 | SS/OLP | Soft Start/Overload Protection terminal | Input to set delay for Overload Protection and Soft Start operation |
| 6 | FB | Feedback terminal | Input for Constant Voltage Control and Burst (intermittent) Mode oscillation control signals |
| 7 | OCP/BD | Overcurrent Protection/Bottom Detection | Input for Overcurrent Detection and Bottom Detection signals |

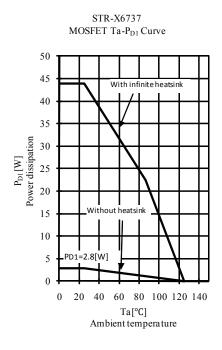
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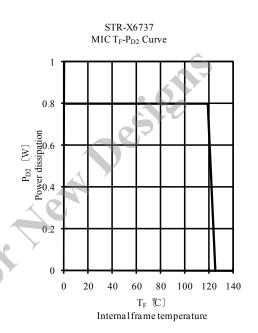




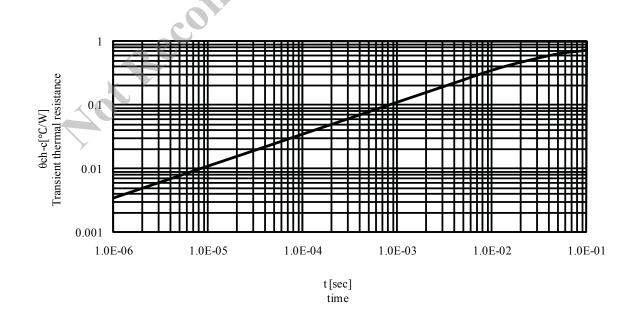
Avalanche energy derating curve







STR-X6737 Transient thermal resistance curve



ELECTRICAL CHARACTERISTICS

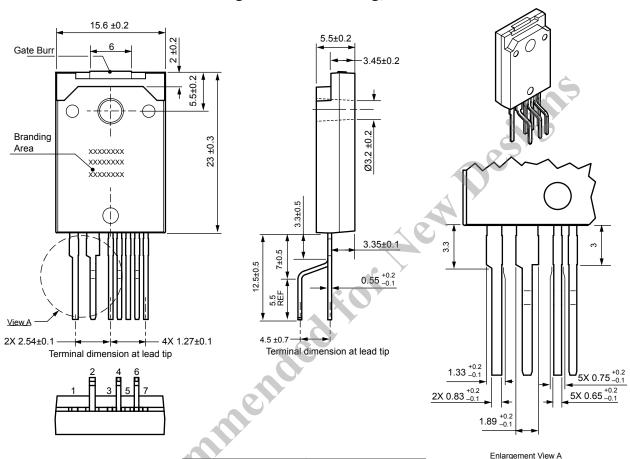
| Characteristic | Symbol | Test Conditions | Min. | Тур. | Max. | Units |
|--|-----------------------------|---|------------|--------|--------|-------|
| ELECTRICAL CHARACTERISTICS for Controller (| MIC) ¹ , valid a | It $T_A = 25$ °C, $V_{CC} = 20$ V, unless othe | rwise spec | cified | | |
| Power Supply Start-up Operation | | | | | | |
| Operation Start Voltage | V _{CC(ON)} | V _{CC} = 0→20 V | 16.3 | 18.2 | 19.9 | V |
| Operation Stop Voltage | V _{CC(OFF)} | V _{CC} = 20→8.8 V | 8.8 | 9.7 | 10.6 | V |
| Circuit Current In Operation | I _{CC(ON)} | | _ | | 6 | mA |
| Circuit Current In Non-Operation | I _{CC(OFF)} | V _{CC} = 15 V | _ | - (| 100 | μA |
| Oscillation Frequency | f _{osc} | | 19 | 22 | 25 | kHz |
| Soft Start Operation Stop Voltage | V _{SSOLP(SS)} | V _{SS/OLP} increasing | 1.1 | 1.2 | 1.4 | V |
| Soft Start Operation Charging Current | I _{SSOLP(SS)} | V _{SS/OLP} = 0 V | -710 | -550 | -390 | μΑ |
| Normal Operation | | | | | | |
| Bottom-Skip Operation Threshold Voltage 1 | V _{OCPBD(BS1)} | | -0.720 | -0.665 | -0.605 | V |
| Bottom-Skip Operation Threshold Voltage 2 | V _{OCPBD(BS2)} | | -0.485 | -0.435 | -0.385 | V |
| Overcurrent Detection Threshold Voltage | V _{OCPBD(LIM)} | V _{OCP/BD} falling | -0.995 | -0.940 | -0.895 | V |
| OCP/BD Terminal Outflow Current | I _{OCPBD} | V _{OCP/BD} = -0.95 V | -250 | -100 | -40 | μA |
| Quasi-Resonant Operation Threshold Voltage 1 | V _{OCPBD(TH1)} | V _{OCP/BD} falling | 0.28 | 0.40 | 0.52 | V |
| Quasi-Resonant Operation Threshold Voltage 2 | V _{OCPBD(TH2)} | V _{OCP/BD} rising | 0.67 | 0.80 | 0.93 | V |
| FB Terminal Threshold Voltage | V _{FB(OFF)} | V _{FB} rising | 1.32 | 1.45 | 1.58 | V |
| FB Terminal Inflow Current (Normal Operation) | I _{FB(ON)} | V _{FB} = 1.6 V | 600 | 1000 | 1400 | μA |
| Standby Operation | | | | | | |
| Standby Operation Start Voltage | V _{CC(S)} | V _{CC} = 0→15 V, V _{FB} = 1.6 V | 10.3 | 11.2 | 12.1 | V |
| Standby Operation Start Voltage Interval | V _{CC(SK)} | $V_{CC(SK)} = V_{CC(S)} - V_{CC(OFF)}$ | 1.10 | 1.35 | 1.65 | V |
| Standby Non-Operation Circuit Current | Jcc(s) | V _{CC} = 10.2 V, V _{FB} = 1.6 V | _ | 20 | 56 | μA |
| FB Terminal Inflow Current, Standby Operation | I _{FB(S)} | V _{CC} = 10.2 V, V _{FB} = 1.6 V | _ | 4 | 14 | μA |
| FB Terminal Threshold Voltage, Standby Operation | $V_{FB(S)}$ | V _{CC} = 15 V, V _{FB} rising | 0.55 | 1.10 | 1.50 | V |
| Minimum On Time | t _{ON(MIN)} | | 0.75 | 1.10 | 1.50 | μs |
| Protection Operation | | | , | | | |
| Maximum On Time | t _{ON(MAX)} | | 27.5 | 32.5 | 39.0 | μs |
| Overload Protection Operation Threshold Voltage | V _{SSOLP(OLP)} | | 4.0 | 4.9 | 5.8 | V |
| Overload Protection Operation Charging Current | I _{SSOLP(OLP)} | V _{SS/OLP} = 2.5 V | -16 | -11 | -6 | μA |
| Overvoltage Protection Operation Voltage | V _{CC(OVP)} | V _{CC} = 0→30 V | 25.5 | 27.7 | 29.9 | V |
| Latch Circuit Holding Current ² | I _{CC(H)} | V _{CC(OFF)} - 0.3 V | _ | 45 | 140 | μA |
| Latch Circuit Release Voltage ² | V _{CC(La.OFF)} | V _{CC} = 30→6 V, OVP operating | 6.0 | 7.2 | 8.5 | V |
| ELECTRICAL CHARACTERISTICS for MOSFET, va | alid at T _A = 25 | °C, unless otherwise specified | | | | |
| Drain-to-Source Breakdown Voltage | V _{DSS} | I _{DSS} = 300 μA | 500 | _ | _ | V |
| Drain Leakage Current | I _{DSS} | V _{DSSS} = 500 V | _ | _ | 300 | μΑ |
| On Resistance | R _{DS(on)} | I _{DS} = 4.0 A | _ | _ | 0.36 | Ω |
| Switching Time | t _f | | _ | _ | 500 | ns |
| Thermal Resistance | R _{0ch-F} | Channel to internal frame | _ | _ | 1.09 | °C/W |

¹Current polarity with respect to the IC: positive current indicates current sink at the terminal named, negative current indicates source at the terminal named.

²The latch circuit means a circuit operated OVP and OLP.

Off-Line Quasi-Resonant Switching Regulators

Package Outline Drawing, TO-3P



70.7 O.7 O.7 Side View

Gate burr: 0.3 mm (max.) Terminal core material: Cu

Terminal treatment: Ni plating and Pb-free solder dip

Leadform: 1901

Approximate weight: 6 g

Dimensions in millimeters

Drawing for reference only

Branding codes (exact appearance at manufacturer discretion):

1st line, type: STR
2nd line, subtype: X6737
3rd line, lot: YM DD

Where: Y is the last digit of the year of manufacture

M is the month (1 to 9, O, N, D)

DD is the 2-digit date



Leadframe plating Pb-free. Device composition includes high-temperature solder (Pb >85%), which is exempted from the RoHS directive.

Because reliability can be affected adversely by improper storage environments and handling methods, please observe the following cautions.

Cautions for Storage

- Ensure that storage conditions comply with the standard temperature (5°C to 35°C) and the standard relative humidity (around 40% to 75%); avoid storage locations that experience extreme changes in temperature or humidity.
- Avoid locations where dust or harmful gases are present and avoid direct sunlight.
- Reinspect for rust on leads and solderability of the products that have been stored for a long time.

Cautions for Testing and Handling

When tests are carried out during inspection testing and other standard test periods, protect the products from power surges from the testing device, shorts between the product pins, and wrong connections. Ensure all test parameters are within the ratings specified by Sanken for the products.

Remarks About Using Silicone Grease with a Heatsink

- When silicone grease is used in mounting the products on a heatsink, it shall be applied evenly and thinly. If more silicone grease than required is applied, it may produce excess stress.
- Volatile-type silicone greases may crack after long periods
 of time, resulting in reduced heat radiation effect. Silicone
 greases with low consistency (hard grease) may cause
 cracks in the mold resin when screwing the products to a
 heatsink.

Our recommended silicone greases for heat radiation purposes, which will not cause any adverse effect on the product life, are indicated below:

| Туре | Suppliers |
|--------|--------------------------------------|
| G746 | Shin-Etsu Chemical Co., Ltd. |
| YG6260 | Momentive Performance Materials Inc. |
| SC102 | Dow Corning Toray Co., Ltd. |

Cautions for Mounting to a Heatsink

When the flatness around the screw hole is insufficient, such
as when mounting the products to a heatsink that has an
extruded (burred) screw hole, the products can be damaged,
even with a lower than recommended screw torque. For
mounting the products, the mounting surface flatness should
be 0.05 mm or less.

Recommended operation temperature

• Inner frame temperature in operation T_F = 115°C

- Please select suitable screws for the product shape. Do not use a flat-head machine screw because of the stress to the products. Self-tapping screws are not recommended. When using self-tapping screws, the screw may enter the hole diagonally, not vertically, depending on the conditions of hole before threading or the work situation. That may stress the products and may cause failures.
- Recommended screw torque: 0.588 to 0.785 N

 m (6 to 8 kgf

 cm).
- For tightening screws, if a tightening tool (such as a driver) hits the products, the package may crack, and internal stress fractures may occur, which shorten the lifetime of the electrical elements and can cause catastrophic failure. Tightening with an air driver makes a substantial impact. In addition, a screw torque higher than the set torque can be applied and the package may be damaged. Therefore, an electric driver is recommended.

When the package is tightened at two or more places, first pre-tighten with a lower torque at all places, then tighten with the specified torque. When using a power driver, torque control is mandatory.

Soldering

- When soldering the products, please be sure to minimize the working time, within the following limits:
 - 260±5°C 10±1 s (Flow, 2 times) 380±10°C 3.5±0.5 s (Soldering iron, 1 time)
- Soldering should be at a distance of at least 2.0 mm from the body of the products.

Electrostatic Discharge

- When handling the products, the operator must be grounded. Grounded wrist straps worn should have at least 1 M Ω of resistance from the operator to ground to prevent shock hazard, and it should be placed near the operator.
- Workbenches where the products are handled should be grounded and be provided with conductive table and floor mats.
- When using measuring equipment such as a curve tracer, the equipment should be grounded.
- When soldering the products, the head of soldering irons
 or the solder bath must be grounded in order to prevent
 leak voltages generated by them from being applied to the
 products.
- The products should always be stored and transported in Sanken shipping containers or conductive containers, or be wrapped in aluminum foil.

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