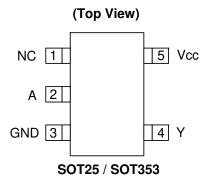


#### **Description**

The 74AHCT1G14 is a single 1-input Schmitt-trigger inverter gate with a standard push-pull output. The device is designed for operation with a power supply range of 4.5V to 5.5V. The gate performs the positive Boolean function:

$$Y = \overline{A}$$

#### **Pin Assignments**



#### **Features**

- Supply Voltage Range from 4.5V to 5.5V
- ± 8 mA Output Drive at 5.0V
- CMOS low power consumption
- Schmitt Trigger Action at All Inputs Make the Circuit Tolerant for Slower Input Rise and Fall Time
- ESD Protection per JESD 22
  - o Exceeds 200-V Machine Model (A115-A)
  - Exceeds 2000-V Human Body Model (A114-A)
  - Exceeds 1000-V Charged Device Model (C101C)
- Latch-Up Exceeds 100mA per JESD 78, Class II
- SOT25 and SOT353: Assembled with "Green" Molding Compound (no Br, Sb)
- Lead Free Finish / RoHS Compliant (Note 1)

#### **Applications**

- General Purpose Logic
- Wide array of products such as
  - PCs, networking, notebooks, netbooks, PDAs
  - o Computer peripherals, hard drives, CD/DVD ROM
  - o TV, DVD, DVR, set top box
  - o Cell Phones, Personal Navigation / GPS
  - o MP3 players ,Cameras, Video Recorders

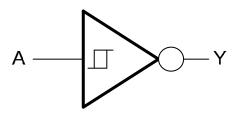
Notes: 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied. Please visit our website at http://www.diodes.com/products/lead\_free.html.



# **Pin Descriptions**

Pin Name	Pin NO.	Description	
NC	1	No Connection	
Α	2	Data Input	
GND	3	Ground	
Y	4	Data Output	
V <sub>CC</sub>	5	Supply Voltage	

# Logic Diagram



# **Function Table**

Inputs	Output
Α	Υ
Н	L
L	Н



## **Absolute Maximum Ratings (Note 2)**

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD CDM	Charged Device Model ESD Protection	1	KV
ESD MM	Machine Model ESD Protection	200	V
$V_{CC}$	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
V <sub>O</sub>	Voltage applied to output in high or low state	-0.5 to V <sub>CC</sub> +0.5	V
l <sub>IK</sub>	Input Clamp Current V <sub>I</sub> <0	-20	mA
I <sub>OK</sub>	Output Clamp Current (V <sub>O</sub> < 0 or V <sub>O</sub> > V <sub>CC</sub> )	±20	mA
Io	Continuous output current (V <sub>O</sub> = 0 to V <sub>CC</sub> )	±25	mA
I <sub>CC</sub>	Continuous current through V <sub>CC</sub>	50	mA
I <sub>GND</sub>	Continuous current through GND	-50	mA
TJ	Operating Junction Temperature	-40 to 150	°C
T <sub>STG</sub>	Storage Temperature	-65 to 150	°C

Notes: 2. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

# **Recommended Operating Conditions (Note 3)**

Symbol	Parameter	Min	Max	Unit
$V_{CC}$	Operating Voltage	4.5	5.5	V
VI	Input Voltage	0	5.5	V
V <sub>O</sub>	Output Voltage	0	$V_{CC}$	V
I <sub>OH</sub>	High-level output current		-8	mA
I <sub>OL</sub>	Low-level output current		8	mA
Δt/ΔV	Input transition rise or fall rate		20	ns/V
T <sub>A</sub>	Operating free-air temperature	-40	125	ºC

Notes: 3. Unused inputs should be held at  $V_{CC}$  or Ground.



## **Electrical Characteristics**

0	D	Took Conditions	Parameter Test Conditions V T <sub>A</sub> =25.		-40ºC to 85ºC		-40ºC to 125ºC		Unit		
Symbol	Parameter	Test Conditions	V <sub>CC</sub>	Min	Тур.	Max	Min	Max	Min	Max	Unit
	Positive-going		4.5V			1.9		1.9		1.9	
V <sub>T+</sub>	input threshold voltage		5.5V			2.1		2.1		2.1	
	Negative-going input threshold		4.5V	0.5			0.5		0.5		
$V_{T-}$	voltage		5.5V	0.6			0.6		0.6		
ΔV <sub>T</sub>	Hysteresis		4.5V	0.4		1.4	0.4	1.4	.35	1.4	
77.4	$(V_{T+} - V_{T-})$		5.5V	0.4		1.6	0.4	1.6	.35	1.5	
V <sub>OH</sub>	High Level	$I_{OH} = -50\mu A$	4.5V	4.4	4.5		4.4		4.4		V
VOH	Output Voltage	$I_{OH} = -8mA$	4.5V	3.94			3.80		3.70		V
V	V <sub>OL</sub> High-level Input Voltage	$I_{OL} = 50\mu A$	4.5V			0.1		0.1		0.1	- V
VOL		$I_{OL} = 8mA$	4.5V			0.36		0.44		0.55	
II	Input Current	$V_I = 5.5 \text{ V or GND}$	0 to 5.5V			± 0.1		± 1		± 2	μΑ
Icc	Supply Current	$V_I = 5.5V$ or GND $I_O=0$	5.5V			2		20		40	μA
ΔI <sub>CC</sub>	Additional Supply Current	One input at 3.4 V Other inputs at V <sub>CC</sub> or GND	5.5V			1.35		1.5		1.5	mA
Cı	Input Capacitance	V <sub>I</sub> = V <sub>CC</sub> or GND	5.5V		3	10		10		10	pF
0	Thermal Resistance	SOT25	(Note 4)		195						°C/W
θ <sub>JA</sub>	Junction-to- Ambient	SOT353	(Note 4)		430						°C/W
Alo	Thermal Resistance	SOT25	(Note 4)		58						°C/W
θ <sub>JC</sub>	Junction-to- Case	SOT353	(Note 4)		155						°C/W

Note: 4. Test conditions for SOT25, and SOT353: Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout

# **Switching Characteristics**

 $V_{CC} = 5V \pm 0.5V$  (see Figure 1)

		<u> </u>									
Doromotor	From	ТО			25ºC		-40ºC t	o 85ºC	-40ºC to	125ºC	Unit
Parameter	(Input)	(OUTPUT)		Min	Тур.	Max	Min	Max	Min	Max	Unit
	^	V	C <sub>L</sub> =15pF	0.6	4.0	7.0	0.6	8.0	0.6	9.0	ns
<sup>l</sup> pd	A	Y	C <sub>L</sub> =50pF	0.6	5.4	8.0	0.6	9.0	0.6	10.0	ns

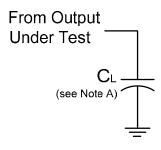


## **Operating Characteristics**

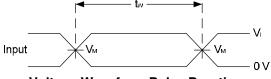
T<sub>A</sub> = 25 <sup>o</sup>C

Parameter		Test Conditions	V <sub>CC</sub> = 5V Typ.	Unit
C <sub>pd</sub>	Power dissipation capacitance	f = 1 MHz No Load	10	pF

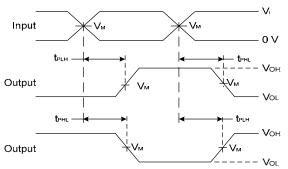
#### **Parameter Measurement Information**



V	Inputs		V	•
V <sub>CC</sub>	VI	t <sub>r</sub> /t <sub>f</sub>	V <sub>M</sub>	CL
3.3V±0.3V	V <sub>CC</sub>	≤3ns	V <sub>CC</sub> /2	15pF
5V±0.5V	V <sub>CC</sub>	≤3ns	V <sub>CC</sub> /2	15pF
3.3V±0.3V	V <sub>CC</sub>	≤3ns	V <sub>CC</sub> /2	50pF
5V±0.5V	V <sub>CC</sub>	≤3ns	V <sub>CC</sub> /2	50pF



**Voltage Waveform Pulse Duration** 



Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Figure 1. Load Circuit and Voltage Waveforms

Notes: A. Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 1 MHz.
- C. Inputs are measured separately one transition per measurement.
- D. t<sub>PLH</sub> and t<sub>PHL</sub> are the same as t<sub>PD</sub>.



## **Ordering Information**

74 AHCT 1G 14 XX - 7 Logic Device **Function** Package **Packing** 74: Logic Prefix 7: Tape & Reel 14:1-Input W5: SOT25

AHCT: 2 to 5.5V

Family with TTL input level

Schmitt-Trigger Inverter

**SE: SOT353** 

1G: One gate

	Device	Package	Packaging	7" Tape and Reel		
	Device	Code	(Note 5)	Quantity	Part Number Suffix	
<b>6</b>	74AHCT1G14W5-7	W5	SOT25	3000/Tape & Reel	-7	
<b>Pb</b> ,	74AHCT1G14SE-7	SE	SOT353	3000/Tape & Reel	-7	

Notes: 5. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

#### **Marking Information**

## (Top View)

5 4 XX Y WX

2

3

XX: Identification code

Y: Year 0~9

<u>W</u>: Week: A~Z: 1~26 week;

a~z: 27~52 week; z represents 52 and 53 week

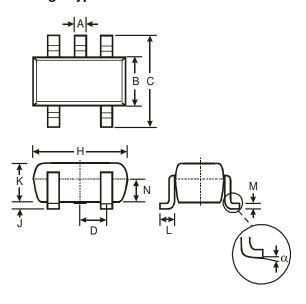
 $\underline{X}$ :  $A^{\sim}Z$ : Internal code

Part Number	Package	Identification Code
74AHCT1G14W5	SOT25	ZV
74AHCT1G14SE	SOT353	ZV



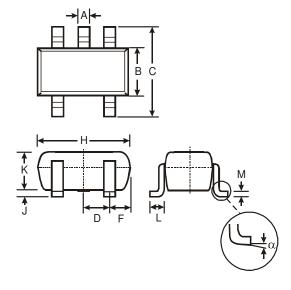
## Package Outline Dimensions (All Dimensions in mm)

## (1) Package Type: SOT25



SOT25						
Dim	Min	Max	Тур			
A	0.35	0.50	0.38			
В	1.50	1.70	1.60			
C	2.70	3.00	2.80			
D			0.95			
Н	2.90	3.10	3.00			
7	0.013	0.10	0.05			
K	1.00	1.30	1.10			
L	0.35	0.55	0.40			
М	0.10	0.20	0.15			
N	0.70	0.80	0.75			
α	0°	8°				
All Dimensions in mm						

## (2) Package Type: SOT353



SOT353					
Dim	Min	Max			
Α	0.10	0.30			
В	1.15	1.35			
C	2.00	2.20			
D	0.65 Typ				
F	0.40	0.45			
Н	1.80	2.20			
7	0	0.10			
K	0.90	1.00			
L	0.25	0.40			
М	0.10	0.22			
α	0°	8°			
All Dimensions in mm					



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