CMLT3820G	Cent	FCI		
SURFACE MOUNT SILICON				
VERY LOW V _{CE(SAT)}	DESCRIPTION	DESCRIPTION:		
NPN TRANSISTOR	The CENTRAL	SEMICONDUC		
	a very low V _{CE}	(SAT) NPN Tra		
		ère small size a		
		ents. Packaged ce mount packa		
Central	provides perform	mance characte g size constrain		
SOT-563 CASE	MARKING COI	DE: 38G		
APPLICATIONS:	FEATURES:			
DC/DC Converters	 Device is Hal 	• Device is <i>Halogen Free</i> by d		
Voltage Clamping	 High Current (IC=1.0A) 			
Protection Circuits	• V _{CE(SAT)} =0.28V MAX @ I _C			
 Battery powered Cell Phones, Pagers, 	 SOT563 surface mount packa 			
Digital Cameras, PDAs, Laptops, etc.	 Complementary PNP device 			
MAXIMUM RATINGS: (T _A =25°C)	SYMBOL			
Collector-Base Voltage	V _{CBO}	80		
Collector-Emitter Voltage	V _{CEO} 60 V _{CEO} 50			

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CTOR CMLT3820G is ansistor, designed for and efficiency are the d in a space saving age, this component eristics suitable for the ned applications.

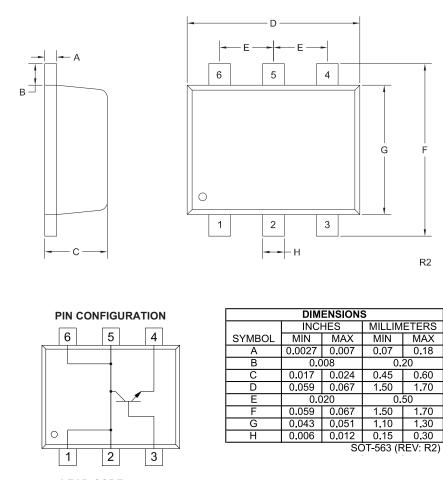
APPLICATIONS:		FEATURES.			
• DC/DC C	onverters	 Device is Halogen Free by design 			
 Voltage C 	Clamping	 High Current (IC=1.0A) 			
 Protection 	• V _{CE(SAT)} =0.28V MAX @ I _C =1.0A			.0A	
 Battery powered Cell Phones, Pagers, 		SOT563 surface mount package			
• •	meras, PDAs, Laptops, etc.	Complementary PNP device CMLT7820G			
MAXIMUM F	MAXIMUM RATINGS: (T _A =25°C)			UNITS	
Collector-Base Voltage		V _{CBO}	80	V	
Collector-Em	Collector-Emitter Voltage		60	V	
Emitter-Base	Emitter-Base Voltage		5.0	V	
Continuous (Continuous Collector Current		1.0	А	
Peak Collect	Peak Collector Current		2.0	A	
Continuous E	Continuous Base Current		300	mA	
Power Dissip	Power Dissipation		250	mW	
Operating ar	Operating and Storage Junction Temperature		-65 to +150	°C	
Thermal Res	istance	Τ _J , T _{stg} Θ _{JA}	500	°C/W	
ELECTRICA	L CHARACTERISTICS : (T _A =25°C u	unless otherwise no	oted)		
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS	
ICBO	V _{CB} =60V		100	nA	
IEBO	V _{EB} =5.0V		100	nA	
BVCBO	Ι _C =100μΑ	80		V	
BVCEO	I _C =10mA	60		V	
BVEBO	Ι _Ε =100μΑ	5.0		V	
VCE(SAT)	I _C =100mA, I _B =1.0mA		0.115	V	
V _{CE} (SAT)	I _C =500mA, I _B =50mA		0.15	V	
V _{CE} (SAT)	I _C =1.0A, I _B =100mA		0.28	V	
V _{BE} (SAT)	I _C =1.0A, I _B =50mA		1.1	V	
V _{BE(ON)}	V _{CE} =5.0V, I _C =1.0A		0.9	V	
h _{FE} `´	V _{CE} =5.0V, I _C =1.0mA	200			
hFE	V _{CE} =5.0V, I _C =500mA	200			
h _{FE}	V _{CE} =5.0V, I _C =1.0A	100			
fт	V _{CE} =10V, I _C =50mA	150		MHz	
Cob	V _{CB} =10V, I _E =0, f=1.0MHz		10	pF	



CMLT3820G

SURFACE MOUNT SILICON VERY LOW V_{CE(SAT)} NPN TRANSISTOR

SOT-563 CASE - MECHANICAL OUTLINE



LEAD CODE: 1) Collector 2) Collector 3) Base 4) Emitter 5) Collector 6) Collector Pins 1, 2, 5 and 6 are common.

MARKING CODE: 38G

R4 (29-June 2015)

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Central's operations team provides the highest level of support to insure product is delivered on-time.

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- Inventory bonding
- · Consolidated shipping options

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Central's applications engineering team is ready to discuss your design challenges. Just ask.

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- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities

Special wafer diffusions

· Custom product packing

- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits

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Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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