

40 V, 10 A NPN high power bipolar transistor

27 January 2015

Product data sheet

1. General description

NPN high power bipolar transistor in a SOT669 (LFPAK56) Surface-Mounted Device (SMD) power plastic package.

PNP complement: PHPT60410PY

2. Features and benefits

- High thermal power dissipation capability
- High temperature applications up to 175 °C
- Reduced Printed Circuit Board (PCB) requirements comparing to transistors in DPAK
- High energy efficiency due to less heat generation
- AEC-Q101 qualified.

3. Applications

- Power management
- Load switch
- Linear mode voltage regulator
- Backlighting applications
- Motor drive
- Relay replacement

4. Quick reference data

Table 1. Quick reference data							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	40	V
I _C	collector current			-	-	10	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-	20	А
R _{CEsat}	collector-emitter saturation resistance	$\begin{split} I_C &= 10 \text{ A}; I_B = 1 \text{ A}; \text{ pulsed}; t_p \leq 300 \mu\text{s}; \\ \delta &\leq 0.02; T_{amb} = 25 ^\circ\text{C} \end{split}$		-	28	40	mΩ



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5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter	mb	C
2	Е	emitter		в
3	E	emitter	q	- M
4	В	base	ប្រួបួប	E sym123
mb	С	collector	1 2 3 4 LFPAK56; Power- SO8 (SOT669)	5911125

6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PHPT60410NY	LFPAK56; Power-SO8	Plastic single-ended surface-mounted package (LFPAK56; Power-SO8); 4 leads	SOT669			

7. Marking

Table 4. Marking codes	
Type number	Marking code
PHPT60410NY	0410NAB

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8. Limiting values

Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	40	V
V _{CEO}	collector-emitter voltage	open base		-	40	V
V _{EBO}	emitter-base voltage	open collector		-	7	V
I _C	collector current			-	10	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	20	А
I _B	base current			-	1	А
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	2	А
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	1.3	W
			[2]	-	3.3	W
			[3]	-	5	W
			[4]	-	25	W
Tj	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

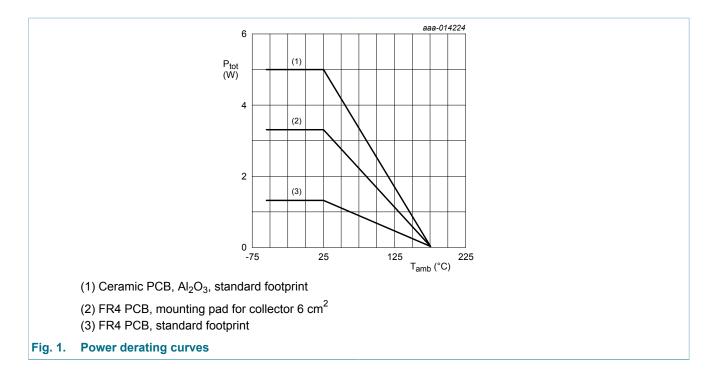
[1] Device mounted on an FR4 Printed-Circuit Board (PCB); single-sided copper; tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB; single-sided copper; tin-plated and mounting pad for collector 6 cm².

[3] Device mounted on an ceramic PCB; Al₂O₃, standard footprint.

[4] Power dissipation from junction to mounting base.

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9. Thermal characteristics

Table 6. Thermal characteristics							
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R _{th(j-a)} thermal resistance from junction to ambient		in free air	[1]	-	-	115	K/W
		[2]	-	-	45	K/W	
	ambient		[3]	-	-	30	K/W
R _{th(j-mb)}	thermal resistance from junction to mounting base			-	-	6	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

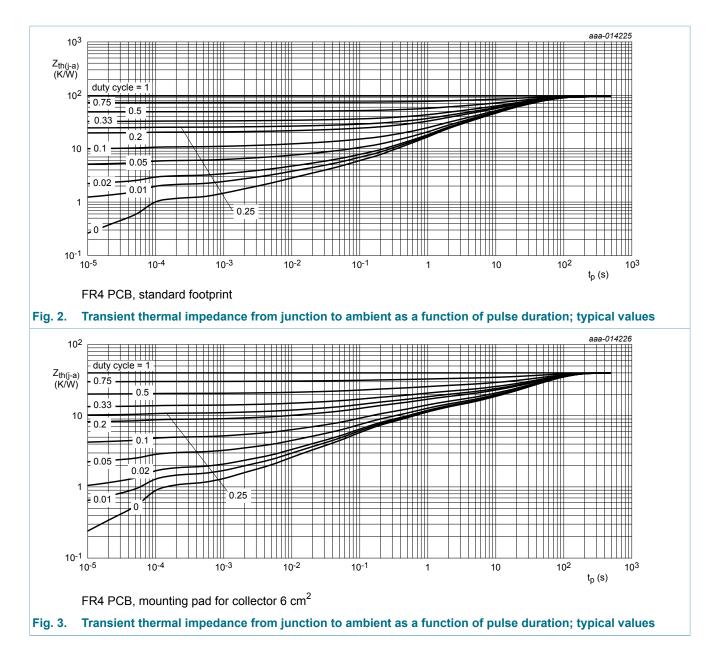
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated mounting pad for collector 6 cm².

[3] Device mounted on an ceramic Printed-Circuit Board (PCB), Al₂O₃, standard footprint.

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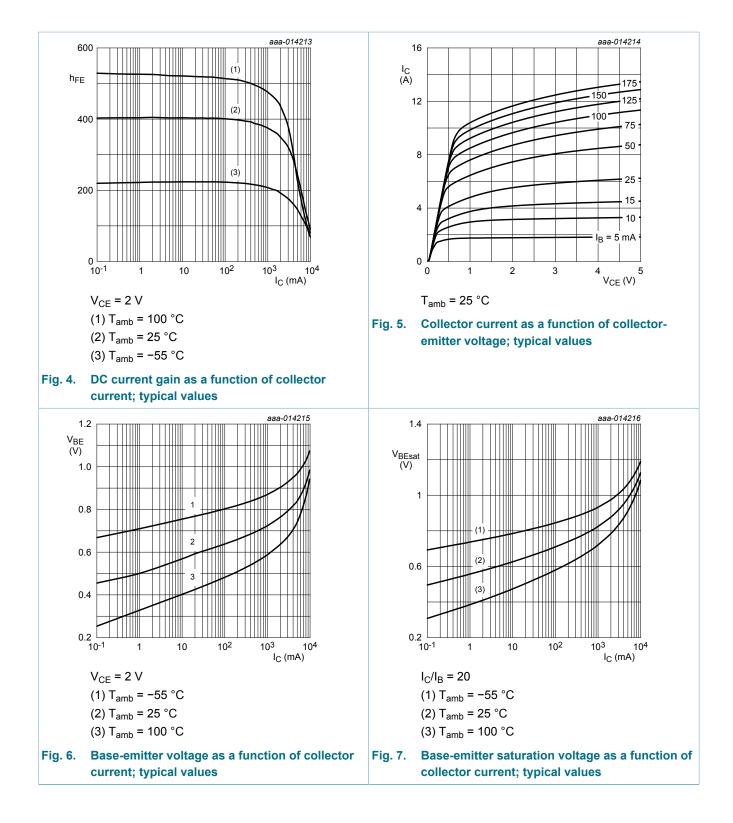
10. Characteristics

Table 7. Characteristics							
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	V_{CB} = 32 V; I _E = 0 A; T _{amb} = 25 °C		-	-	100	nA
current		V_{CB} = 32 V; I _E = 0 A; T _j = 150 °C		-	-	50	μA
I _{CES}	collector-emitter cut-off current	V_{CE} = 32 V; V_{BE} = 0 V; T_{amb} = 25 °C		-	-	100	nA
I _{EBO}	emitter-base cut-off current	V_{EB} = 7 V; I _C = 0 A; T _{amb} = 25 °C		-	-	100	nA
h _{FE}	DC current gain	V_{CE} = 2 V; I _C = 500 mA; T _{amb} = 25 °C		230	370	-	
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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		V_{CE} = 2 V; I_C = 1 A; $t_p \le 300 \ \mu s$; $\delta \le 0.02$; T_{amb} = 25 °C	220	360	-	
		$V_{CE} = 2 \text{ V; } I_C = 5 \text{ A; } t_p \le 300 \mu\text{s;}$ $\delta \le 0.02; \text{ T}_{amb} = 25 ^\circ\text{C}$	140	230	-	
		$V_{CE} = 2 \text{ V; } I_{C} = 10 \text{ A; } t_{p} \le 300 \mu\text{s;}$ $\delta \le 0.02; \text{ T}_{amb} = 25 \text{ °C; pulsed}$	55	90	-	
V _{CEsat}	collector-emitter saturation voltage	I_C = 1 A; I_B = 50 mA; $t_p \le 300 \ \mu$ s; δ ≤ 0.02; T_{amb} = 25 °C; pulsed	-	35	50	mV
		I_{C} = 5 A; I_{B} = 500 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	140	200	mV
		I_{C} = 10 A; I_{B} = 500 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	330	460	mV
R _{CEsat}	collector-emitter saturation resistance	I_C = 10 A; I_B = 1 A; pulsed; $t_p \le 300 \ \mu$ s; δ ≤ 0.02; T_{amb} = 25 °C	-	28	40	mΩ
V _{BEsat}	base-emitter saturation voltage	I_C = 1 A; I_B = 50 mA; pulsed; $t_p \le 300 \ \mu$ s; δ ≤ 0.02; T_{amb} = 25 °C	-	-	0.95	V
		I_C = 5 A; I_B = 500 mA; pulsed; $t_p \le 300 \ \mu$ s; δ ≤ 0.02; T_{amb} = 25 °C	-	-	1.2	V
		I_{C} = 10 A; I_{B} = 500 mA; pulsed; $t_{p} \le 300 \ \mu$ s; δ ≤ 0.02; T_{amb} = 25 °C	-	-	1.3	V
V _{BEon}	base-emitter turn-on voltage	V_{CE} = 2 V; I _C = 500 mA; T _{amb} = 25 °C	-	-	0.8	V
t _d	delay time	V_{CC} = 12.5 V; I _C = 5 A; I _{Bon} = 250 mA;	-	15	-	ns
t _r	rise time	I_{Boff} = -250 mA; T_{amb} = 25 °C	-	130	-	ns
t _{on}	turn-on time		-	145	-	ns
ts	storage time	-	-	310	-	ns
t _f	fall time	-	-	95	-	ns
t _{off}	turn-off time		-	405	-	ns
f _T	transition frequency	V_{CE} = 10 V; I _C = 500 mA; f = 100 MHz; T _{amb} = 25 °C	-	128	-	MHz
C _c	collector capacitance	V _{CB} = 10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	57	-	pF

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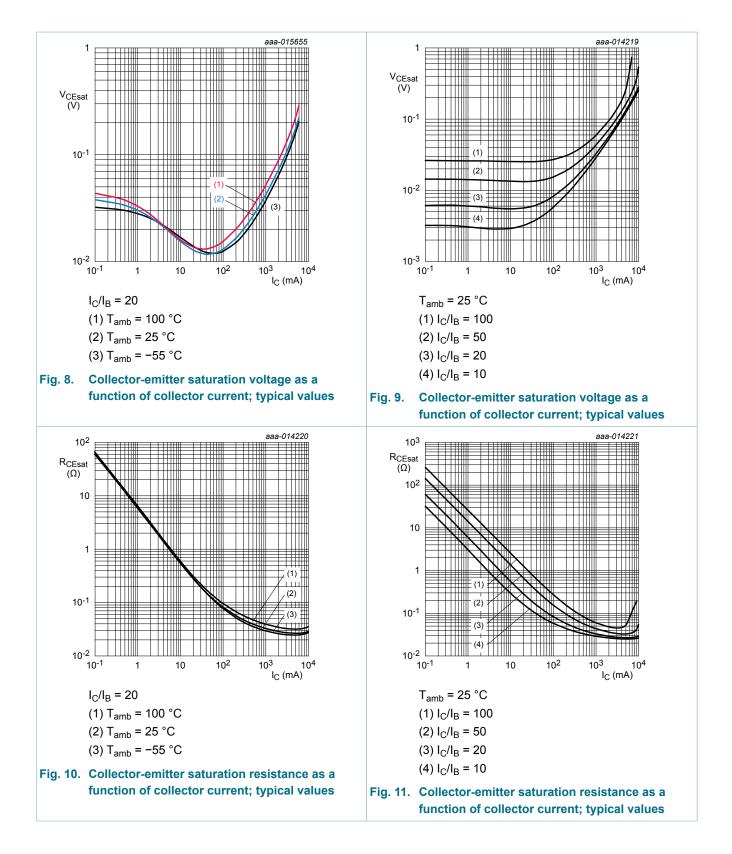


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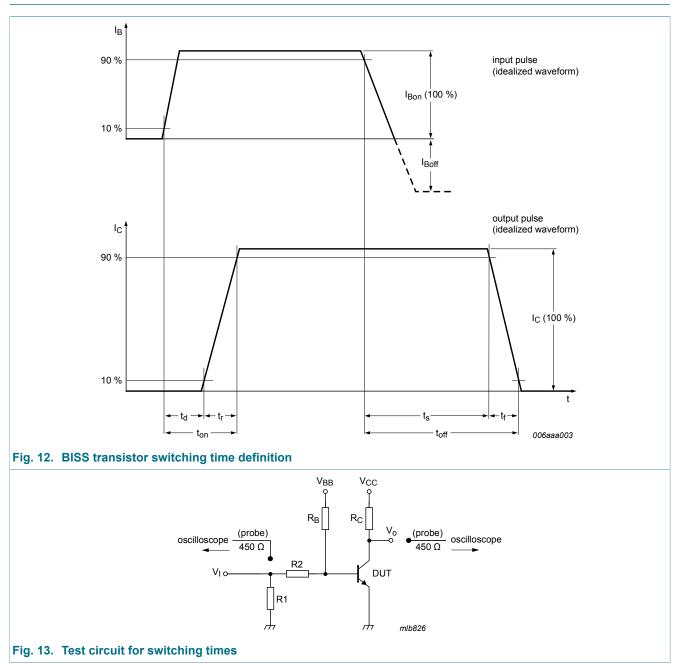
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11. Test information

11.1 Quality information

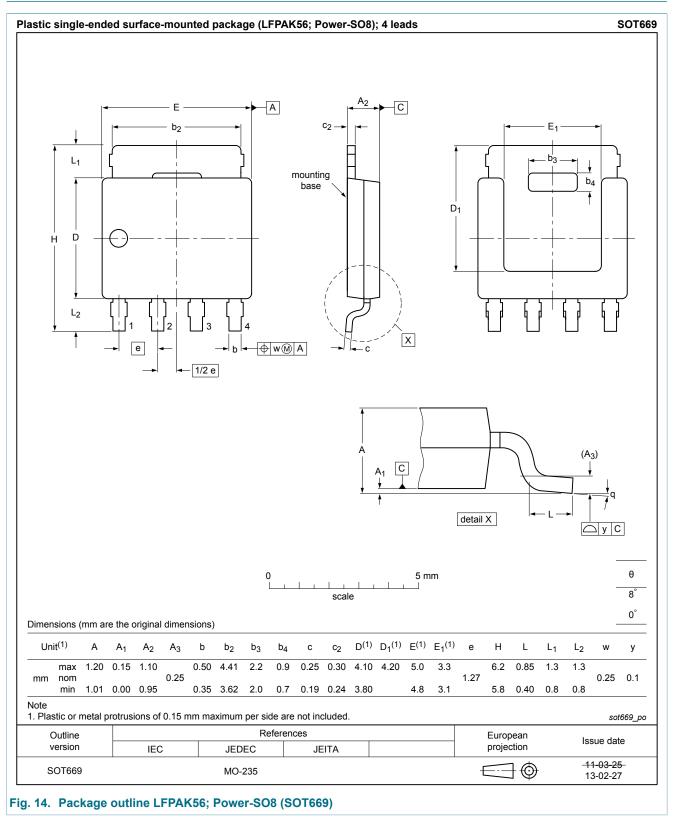
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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12. Package outline



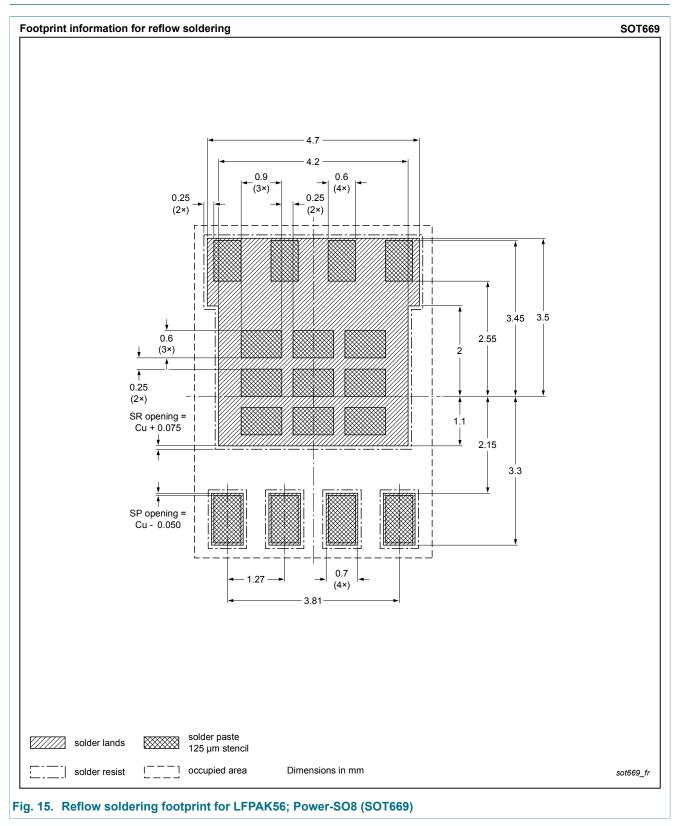
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13. Soldering



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14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PHPT60410NY v.1	20150127	Product data sheet	-	-		

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15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [<u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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