

**Not Recommended  
For New Designs  
See 82C82/82C83H**

### Features

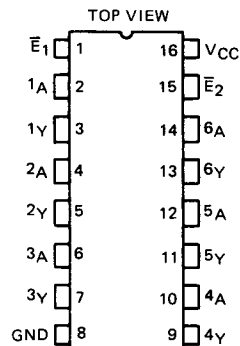
- SINGLE POWER SUPPLY
- HIGH NOISE IMMUNITY
- INDUSTRIAL AND MILITARY GRADES
- DRIVE CAPACITY 300pF
- SOURCE CURRENT 4mA
- SINK CURRENT 6mA
- PROPAGATION DELAY 45nsec MAX.

### Description

The HD-6495 is a self aligned silicon gate CMOS Three-State buffer driver. The circuit consists of 6 non-inverting buffers with separate inputs and outputs which permit this driver to be used for bi-directional or uni-directional busing. A high on either Three-State control line  $\bar{E}_1$  or  $\bar{E}_2$  will force the drivers to the high impedance mode.

Outputs guaranteed valid at VCC 2.0V for Battery Backup Applications.

### Pinout



### Truth Table

CONTROL INPUTS		INPUT	OUTPUT
$\bar{E}_1$	$\bar{E}_2$	A	Y
L	L	L	L
L	L	H	H
L	H	X	HI-Z
H	L	X	HI-Z
H	H	X	HI-Z

X = DON'T CARE  
HI-Z = HIGH IMPEDANCE

### Functional Diagram

