MOTOROLA

# 4-BIT BIDIRECTIONAL UNIVERSAL SHIFT REGISTER

The MC74F194 is a high-speed 4-bit bidirectional universal shift register. As a high-speed multifunctional, sequential building block, it is useful in a wide variety of applications. It may be used in serial-serial, shift left, shift right, serial-parallel, parallel-serial, and parallel-parallel data register transfers. The F194 is similar in operation to the S195 universal shift register, with added features of shift left without external connections and hold (do nothing) modes of operation.

- Typical Shift Frequency of 150 MHz
- Asynchronous Master Reset
- Hold (Do Nothing) Mode
- Fully Synchronous Serial or Parallel Data Transfers

FUNCTIONAL DESCRIPTION

The F194 contains four edge-triggered D flip-flops and the necessary interstage logic to synchronously perform shift right, shift left, parallel load and hold operations. Signals applied to the Select (S_0, S_1) inputs determine the type of operation, as shown in the Function Table. Signals on the Select, Parallel data ($P_0 - P_3$) and Serial data (D_{SR}, D_{SL}) inputs can change when the clock is in either state, provided only that the recommended setup and hold times, with respect to the clock rising edge, are observed. A LOW signal on Master Reset (MR) overrides all other inputs and forces the outputs LOW.

CONNECTION DIAGRAM

V_{CC}	Q_0	Q_1	Q_2	Q_3	CP	S_1	S_0
16	15	14	13	12	11	10	9

1	2	3	4	5	6	7	8
MR	D_{SR}	P_0	P_1	P_2	P_3	D_{SL}	GND
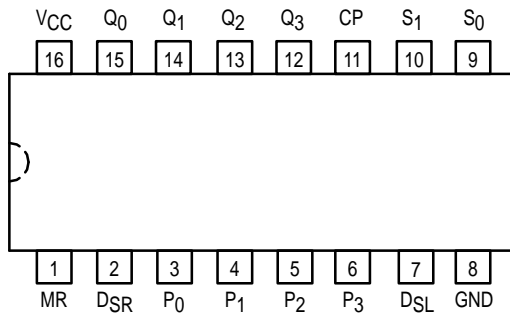
FUNCTION TABLE

| Operating Mode	Inputs						Outputs			
	MR	S_1	S_0	D_{SR}	D_{SL}	P_n	Q_0	Q_1	Q_2	Q_3
Reset	L	X	X	X	X	X	L	L	L	L
Hold	H	l	l	X	X	X	q_0	q_1	q_2	q_3
Shift Left	H	h	l	X	l	X	q_1	q_2	q_3	L
	H	h	l	X	h	X	q_1	q_2	q_3	H
Shift Right	H	l	h	l	X	X	L	q_0	q_1	q_2
	H	l	h	h	X	X	H	q_0	q_1	q_2
Parallel Load	H	h	h	X	X	p_n	p_0	p_1	p_2	p_3

l = LOW voltage level one setup time prior to the LOW-to-HIGH clock transition.
h = HIGH voltage level one setup time prior to the LOW-to-HIGH clock transition.
p_n, q_n = Lower case letters indicate the state of the referenced input or output one setup time prior to the LOW-to-HIGH clock transition.
H = HIGH Voltage Level
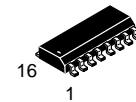L = LOW Voltage Level
X = Immaterial

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4-BIT BIDIRECTIONAL UNIVERSAL SHIFT REGISTER

FAST™ SCHOTTKY TTL
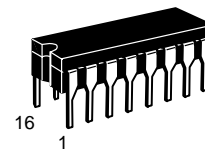
J SUFFIX
CERAMIC
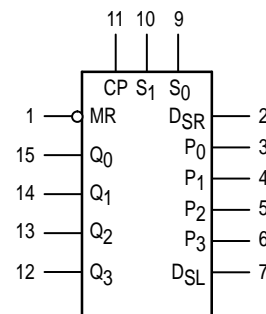CASE 620-09

16   1
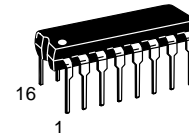
N SUFFIX
PLASTIC
CASE 648-08

16   1

D SUFFIX
SOIC
CASE 751B-03

16   1

ORDERING INFORMATION

MC74FXXXJ	Ceramic
MC74FXXXN	Plastic
MC74FXXXD	SOIC

LOGIC SYMBOL

11	10	9
CP	S_1	S_0

1 — MR	D_{SR} — 2
15 — Q_0	P_0 — 3
14 — Q_1	P_1 — 4
13 — Q_2	P_2 — 5
12 — Q_3	P_3 — 6
	D_{SL} — 7

V_{CC} = PIN 16
GND = PIN 8

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LOGIC DIAGRAM
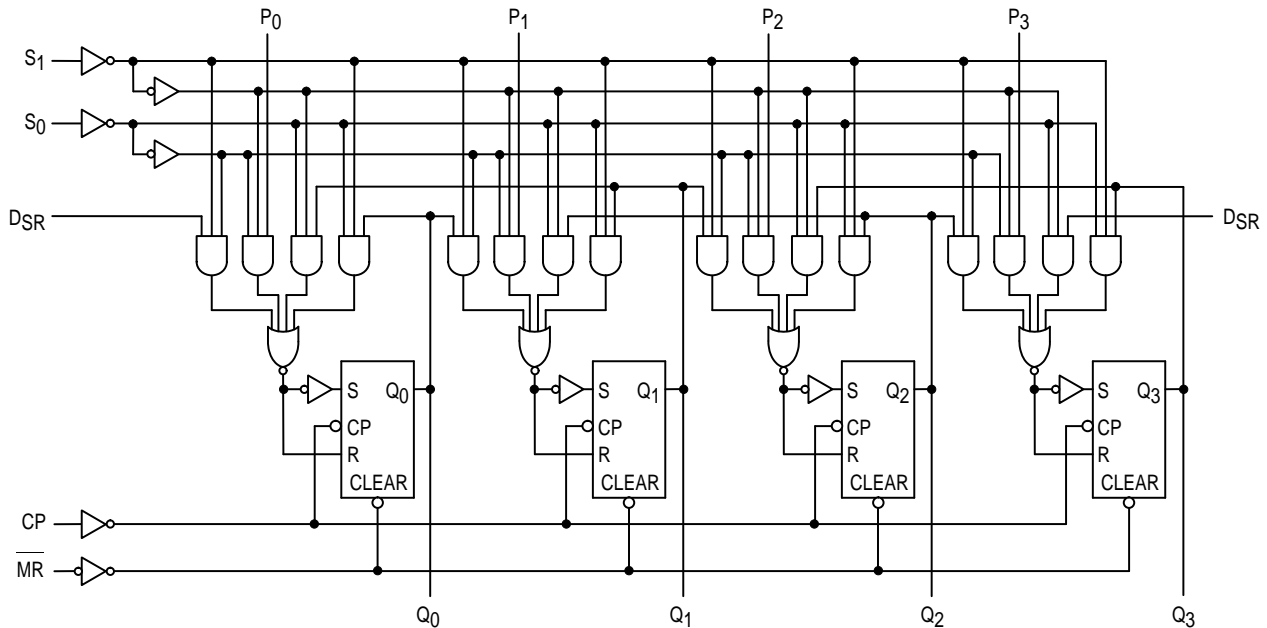
Please note that this diagram is provided only for the understanding of logic operations and should not be used to estimate propagation delays.

GUARANTEED OPERATING RANGES

Symbol	Parameter		Min	Typ	Max	Unit
V_{CC}	Supply Voltage	74	4.5	5.0	5.5	V
T_A	Operating Ambient Temperature Range	74	0	25	70	°C
I_{OH}	Output Current — High	74			−1.0	mA
I_{OL}	Output Current — Low	74			20	mA

DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

Symbol	Parameter		Limits			Unit	Test Conditions	
			Min	Typ	Max			
V_{IH}	Input HIGH Voltage		2.0			V	Guaranteed Input HIGH Voltage	
V_{IL}	Input LOW Voltage				0.8	V	Guaranteed Input LOW Voltage	
V_{IK}	Input Clamp Diode Voltage				−1.2	V	$I_{IN} = -18$ mA	V_{CC} = MIN
V_{OH}	Output HIGH Voltage	74	2.5	3.4		V	$I_{OH} = -1.0$ mA	V_{CC} = 4.5 V
		74	2.7	3.4		V	$I_{OH} = -1.0$ mA	V_{CC} = 4.75 V
V_{OL}	Output LOW Voltage			0.35	0.5	V	$I_{OL} = 20$ mA	V_{CC} = MIN
I_{IH}	Input HIGH Current				20	µA	$V_{IN} = 2.7$ V	V_{CC} = MAX
					100	µA	$V_{IN} = 7.0$ V	
I_{IL}	Input LOW Current				−0.6	mA	$V_{IN} = 0.5$ V	V_{CC} = MAX
I_{OS}	Output Short Circuit Current (Note 2)		−60		−150	mA	$V_{OUT} = 0$ V	V_{CC} = MAX
I_{CC}	Power Supply Current			33	46	mA	S_n, $\overline{MR}$, D_{SR}, D_{SL} = 4.5 V P_n = Gnd, CP = ⌐	V_{CC} = MAX

NOTES:

1. For conditions such as MIN or MAX, use the appropriate value specified under guaranteed operating ranges.

2. Not more than one output should be shorted at a time, nor for more than 1 second.

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AC CHARACTERISTICS

Symbol	Parameter	74F T$_A$ = +25°C V$_{CC}$ = +5.0 V C$_L$ = 50 pF		74F T$_A$ = 0 to +70°C V$_{CC}$ = 5.0 V ±10% C$_L$ = 50 pF		Unit
		Min	Max	Min	Max	
f$_{max}$	Maximum Shift Frequency	105		90		MHz
t$_{PLH}$ t$_{PHL}$	Propagation Delay CP to Q$_n$	3.0 3.5	7.0 7.5	3.5 3.5	8.0 8.0	ns
t$_{PHL}$	Propagation Delay MR to Q$_n$	4.5	12	4.5	14	ns

AC OPERATING REQUIREMENTS

Symbol	Parameter	74F T$_A$ = +25°C V$_{CC}$ = +5.0 V		74F T$_A$ = 0 to +70°C V$_{CC}$ = 5.0 V ±10%		Unit
		Min	Max	Min	Max	
t$_s$(H) t$_s$(L)	Set up Time, HIGH or LOW P$_n$ or D$_{SR}$ or D$_{SL}$ to CP	4.0 4.0		4.0 4.0		ns
t$_h$(H) t$_h$(L)	Hold Time, HIGH or LOW P$_n$ or D$_{SR}$ or D$_{SL}$ to CP	0 0		1.0 1.0		
t$_s$(H) t$_s$(L)	Set up Time, HIGH or LOW S$_n$ to CP	8.0 8.0		9.0 8.0		ns
t$_h$(H) t$_h$(L)	Hold Time, HIGH or LOW S$_n$ to CP	0 0		0 0		
t$_w$(H)	CP Pulse Width HIGH	5.0		5.5		ns
t$_w$(L)	MR Pulse Width LOW	5.0		5.0		ns
t$_{rec}$	Recovery Time MR to CP	7.0		8.0		ns

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How to reach us:

USA/EUROPE/Locations Not Listed: Motorola Literature Distribution; P.O. Box 5405, Denver, Colorado 80217.
1–303–675–2140 or 1–800–441–2447

JAPAN: Motorola Japan Ltd.; SPS, Technical Information Center, 3–20–1, Minami–Azabu. Minato–ku, Tokyo 106–8573 Japan.
81–3–3440–3569

ASIA/PACIFIC: Motorola Semiconductors H.K. Ltd.; Silicon Harbour Centre, 2 Dai King Street, Tai Po Industrial Estate, Tai Po, N.T., Hong Kong. 852–26668334

Customer Focus Center: 1–800–521–6274
Mfax™: RMFAX0@email.sps.mot.com   – TOUCHTONE 1–602–244–6609
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