



20V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)}	I _D Max T _A = +25°C
20V	175mΩ @ V _{GS} = 4.5V	1.32A
	240mΩ @ V _{GS} = 2.5V	1.11A
	360mΩ @ V _{GS} = 1.8V	0.91A

Features and Benefits

- Footprint of Just 0.6mm² Thirteen Times Smaller Than SOT23
- 0.5mm Profile Ideal for Low Profile Applications
- On Resistance <200mΩ @ V_{GS} = 4.5V
- Low Gate Threshold Voltage
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- ESD Protected Gate

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Load Switch

Mechanical Data

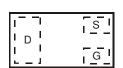
- Case: X1-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound;
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe; Solderable per MIL-STD-202, Method 208@4)
- Weight: 0.001 grams (Approximate)

X1-DFN1006-3

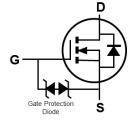




Bottom View



Top View



Equivalent Circuit

Ordering Information (Note 4)

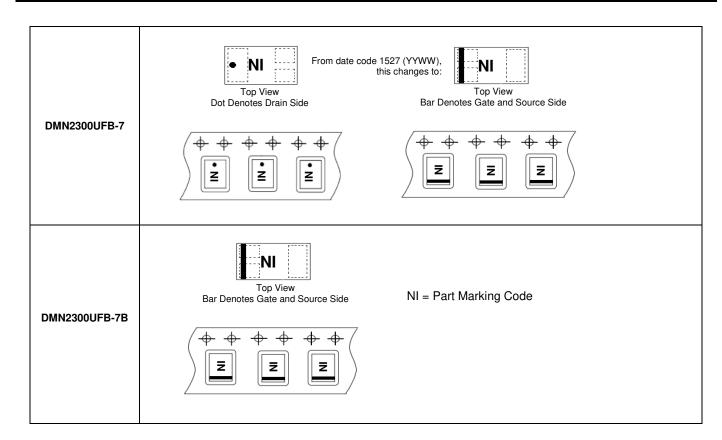
Part Number	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
DMN2300UFB-7	NI	7	8	3,000
DMN2300UFB-7B	NI	7	8	10,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/



Marking Information





Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage			V_{DSS}	20	V
Gate-Source Voltage			V_{GSS}	±8	V
Continuous Drain Current V _{GS} = 4.5V	Steady State	$T_A = +25$ °C (Note 5) $T_A = +85$ °C (Note 5) $T_A = +25$ °C (Note 6)	I _D	1.32 0.94 1.78	А
Pulsed Drain Current (Note 7)		I _{DM}	8	Α	

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P _D	0.468	W
Power Dissipation (Note 6)	P _D	1.2	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{ heta JA}$	267	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	R _{0JA}	104	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

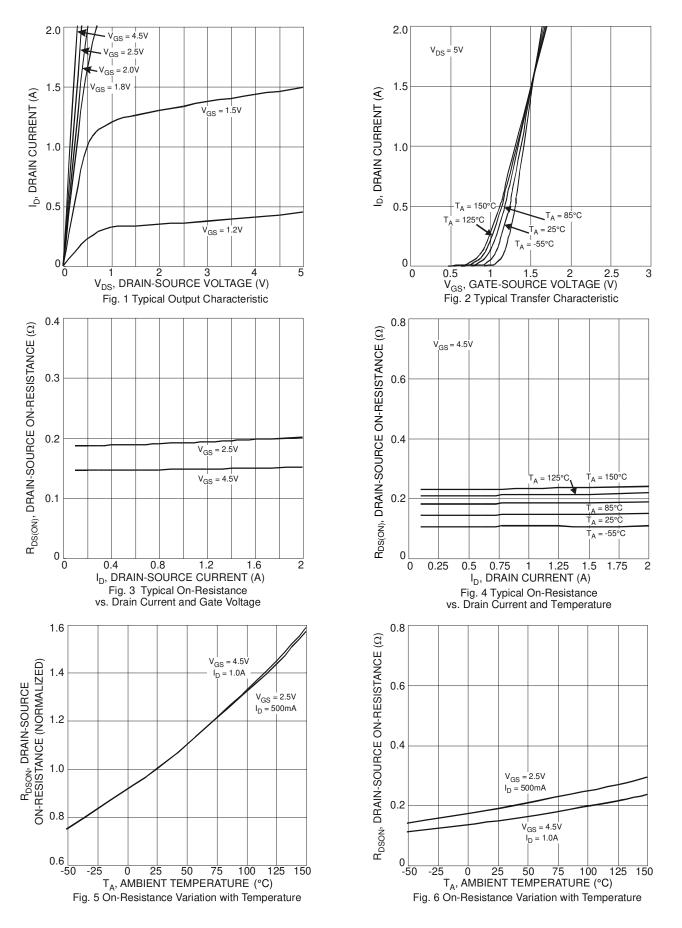
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	20	_	_	V	$V_{GS} = 0V, I_D = 10\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_	-	1	μΑ	$V_{DS} = 20V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	1	10	μΑ	$V_{GS} = \pm 8V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)	ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	0.45	_	0.95	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
		_	_	175		$V_{GS} = 4.5V, I_D = 300mA$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_		240	mΩ	$V_{GS} = 2.5V, I_D = 250mA$	
		_	1	360		$V_{GS} = 1.8V, I_D = 100mA$	
Forward Transfer Admittance	Y _{fs}	40	1	_	mS	$V_{DS} = 3V, I_{D} = 30mA$	
Diode Forward Voltage	V_{SD}	_	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 300mA$	
DYNAMIC CHARACTERISTICS							
Input Capacitance	C _{iss}		67.6	_	рF	V _{DS} = 20V, V _{GS} = 0V, f = 1.0MHz	
Output Capacitance	Coss	_	9.74	_	рF		
Reverse Transfer Capacitance	C _{rss}	_	7.58	_	рF		
Gate Resistance	Rg	_	68.51	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Qg	_	0.89	_	nC	V 45V V 40V	
Gate-Source Charge	Q _{gs}	_	0.14		nC	$V_{GS} = 4.5V, V_{DS} = 10V,$	
Gate-Drain Charge	Q _{gd}	_	0.16	_	nC	I _D = 1A	
Turn-On Delay Time	t _{D(ON)}	_	4.92		ns		
Turn-On Rise Time	t _R	_	6.93		ns	$V_{DS} = 10V, I_{D} = 1A$	
Turn-Off Delay Time	t _{D(OFF)}	_	21.71	_	ns	$V_{GS} = 4.5V$, $R_G = 6\Omega$	
Turn-Off Fall Time	t _F	_	10.62	_	ns		

Notes:

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 25mm X 25mm square copper plate.
 7. Device mounted on minimum recommended pad layout test board, 10µs pulse duty cycle = 1%.
 8. Short duration pulse test used to minimize self-heating effect.







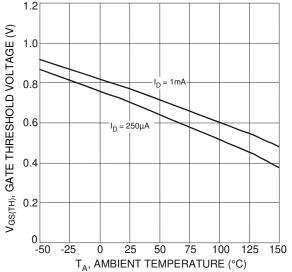
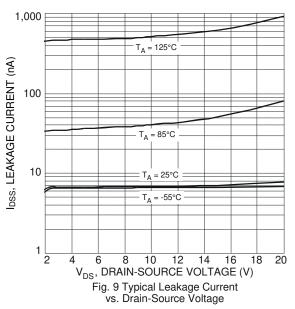
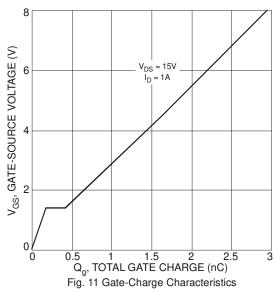
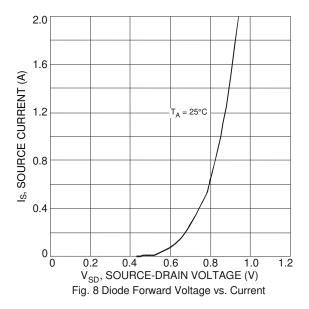
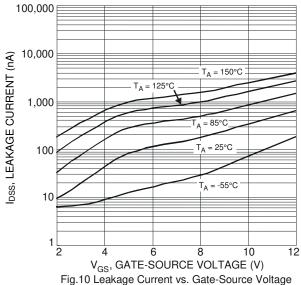


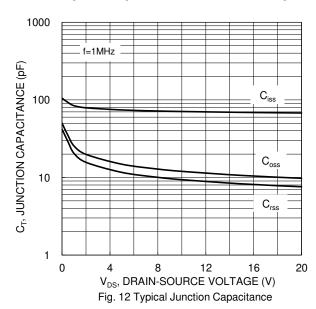
Fig. 7 Gate Threshold Variation vs. Ambient Temperature



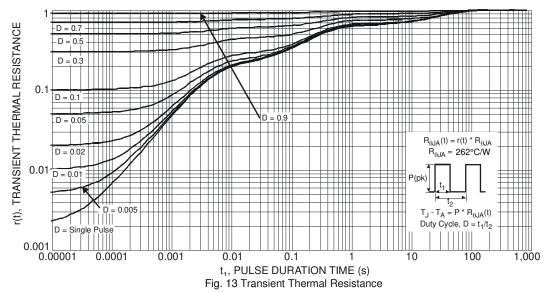










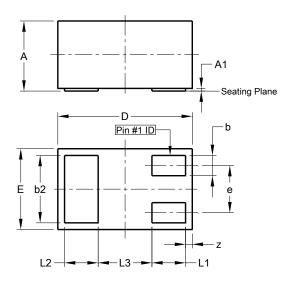




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X1-DFN1006-3

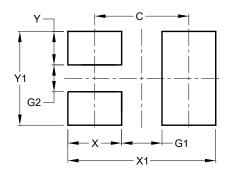


X1-DFN1006-3				
Dim	Min	Max	Тур	
Α	0.47	0.53	0.50	
A1	0.00	0.05	0.03	
b	0.10	0.20	0.15	
b2	0.45	0.55	0.50	
D	0.95	1.075	1.00	
Е	0.55	0.675	0.60	
е	ı	-	0.35	
L1	0.20	0.30	0.25	
L2	0.20	0.30	0.25	
L3	-	-	0.40	
Z	0.02	0.08	0.05	
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X1-DFN1006-3



Dimensions	Value (in mm)
С	0.70
G1	0.30
G2	0.20
X	0.40
X1	1.10
Y	0.25
Y1	0.70



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