TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSIV)

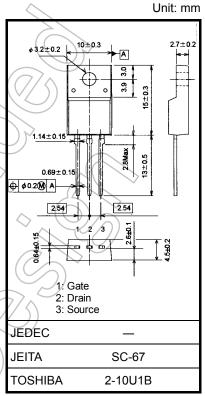
2SK3564

Switching Regulator Applications

- Low drain-source ON-resistance: $R_{DS (ON)} = 3.7 \Omega (typ.)$
- High forward transfer admittance: |Y_{fs}| = 2.6 S (typ.)
- Low leakage current: $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 720 \text{ V)}$
- Enhancement mode: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage			V_{DSS}	900	V
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)			V_{DGR}	900	A
Gate-source voltage			V_{GSS}	<u>±</u> 30	> v
Drain current	DC	(Note 1)	ΙD	3	Α
	Pulse	(Note 1)	I _{DP}	9	(
Drain power dissipation (Tc = 25°C)			P _D	40	/W
Single pulse avalanche energy (Note 2)		EAS	408	12/	
Avalanche current		JAR.	3	A	
Repetitive avalanche energy (Note 3)		(E _{AR} 4.0		mJ	
Channel temperature		Tch	150	Ç	
Storage temperature range		// T _{stg}	-55 to 150	°C	



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

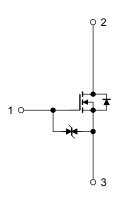
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	Rth (ch-c)	3.125	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 90 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$, L = 83 mH, $I_{AR} = 3.0 \text{ A}$, $R_G = 25 \Omega$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.



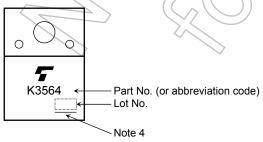
Electrical Characteristics (Ta = 25°C)

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Gate-source brea	akdown voltage	V (BR) GSS	$I_G = \pm 10 \ \mu A, \ V_{DS} = 0 \ V$	±30	_		V
Drain cut-off curr	ent	I _{DSS}	V _{DS} = 720 V, V _{GS} = 0 V	/	_	100	μΑ
Drain-source brea	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	900	_		V
Gate threshold vo	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0)	4.0	V
Drain-source ON	-resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 1.5 A)/<	3.7	4.3	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 20 V, I _D = 1.5 A	0.65	2.6		S
Input capacitance	9	C _{iss}			700		
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	⁷ —	15		pF
Output capacitance		C _{oss}		_	75		
Switching time	Rise time	t _r	10 V I _D = 1.5 A V _{OUT} V _{GS}	- (20		
	Turn-on time	t _{on}	50 Ω \$ R _L = 133 Ω	(60) —	
	Fall time	t _f	V _{DD} ≈ 200 V	71((35		ns
	Turn-off time	t _{off}	Duty ≤ 1%, t _w = 10 μs		125		
Total gate charge	;	Qg) —	17	_	
Gate-source charge		Qgs	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3 \text{ A}$	_	10	_	nC
Gate-drain charge		Q _{gd}		_	7		

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1))) I _{DR}		_	_	3	Α
Pulse drain reverse current (Note 1)	I _{DRP}		_	_	9	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 3 A, V _{GS} = 0 V	_	_	-1.9	V
Reverse recovery time	tri/	$I_{DR} = 3 \text{ A}, V_{GS} = 0 \text{ V},$	_	850	_	ns
Reverse recovery charge	Qrr	dl _{DR} /dt = 100 A/μs	_	4.7	_	μС

Marking

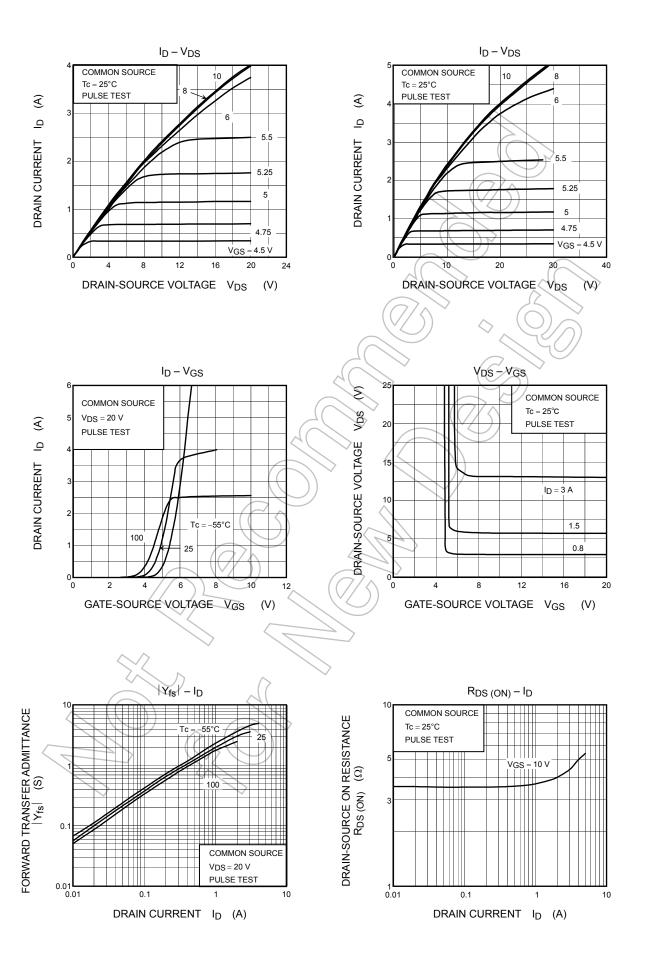


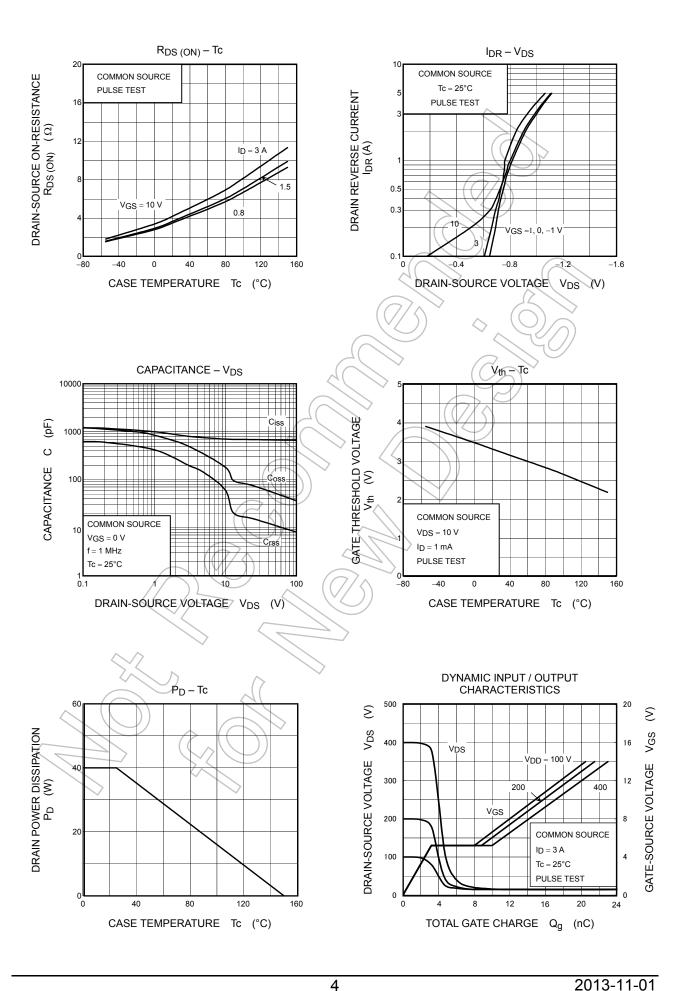
Note 4: A line under a Lot No. identifies the indication of product Labels.

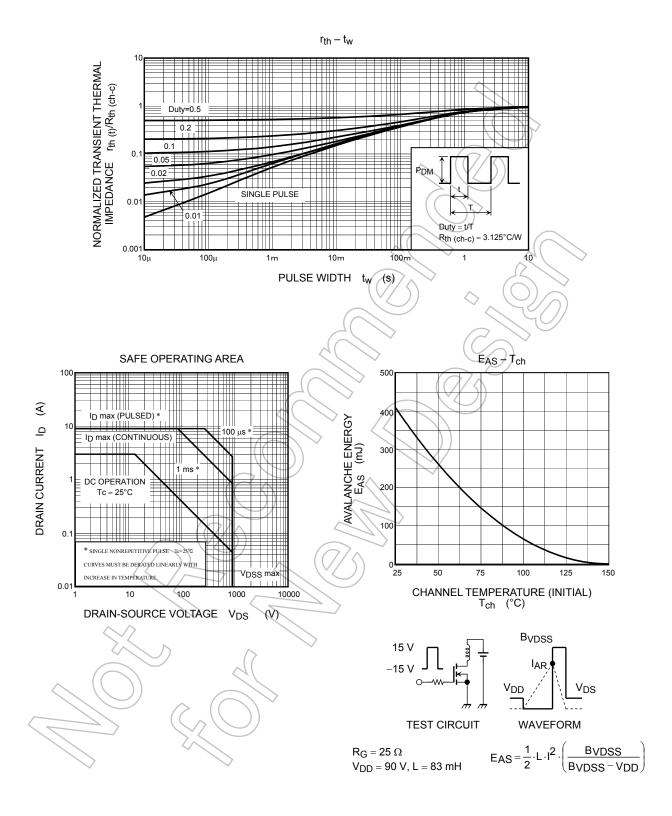
Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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