

High Performance Schottky Rectifier, 120 A



ЦΛІ	F-PAK	(D_67)
IIAL	C-PAN	10-011

Lug termina anode
Base cathode

PRIMARY CHARACTERISTICS				
I _{F(AV)} 120 A				
V_{R}	45 V			
Package	HALF-PAK (D-67)			
Circuit	Single diode			

FEATURES

- 150 °C T_{.1} operation
- Low forward voltage drop
- High frequency operation



- Guard ring for enhanced ruggedness and long term reliability
- · Designed and qualified for industrial level
- UL approved file E222165
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-120NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES				
I _{F(AV)}	Rectangular waveform	120	Α			
V _{RRM}		45	V			
I _{FSM}	t _p = 5 μs sine	26 000	Α			
V _F	120 A _{pk} , T _J = 125 °C	0.62	V			
T _J	Range	-55 to +150	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-120NQ045PbF	UNITS		
Maximum DC reverse voltage	V_{R}	45	V		
Maximum working peak reverse voltage	V_{RWM}	45	V		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS VALUES UN		UNITS	
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 105 °C, rectangular waveform 120			
Maximum peak one cycle non-repetitive surge current		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	26 000	Α
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	1550	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 13 \text{A}, L = 1 \text{mH}$		mJ	
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		Α	



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		120 A	- T _J = 25 °C	0.63	V
Maximum forward voltage drop	V _{FM} ⁽¹⁾	240 A		0.86	
See fig. 1	V _{FM} (··)	120 A	T _J = 125 °C	0.62	
		240 A		0.81	
Maximum reverse leakage current		T _J = 25 °C	V _R = Rated V _R	10	m۸
See fig. 2	IRM \''	T _J = 125 °C	V _R = nateu V _R	500	mA
Maximum junction capacitance	Maximum junction capacitance C _T		$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		pF
Typical series inductance		From top of terminal hole to mounting plane		7.0	nH
Maximum voltage rate of change dV/dt		Rated V _R		10 000	V/µs

Note

 $^{^{(1)}}$ Pulse width $< 500 \mu s$

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temper	erature range	T _J , T _{Stg}		-55 to 150	°C
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	0.38	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth, and greased	0.05	
Approximate weight				30	g
				1.06	oz.
Mounting torque	minimum			3 (26.5)	
Mounting torque — maximum			Non-lubricated threads	4 (35.4)	N·m
Townsing I town to	minimum		Non-lubricated trireads	3.4 (30)	(lbf · in)
Terminal torque	maximum			5 (44.2)	
Case style				HALF-PAK	module

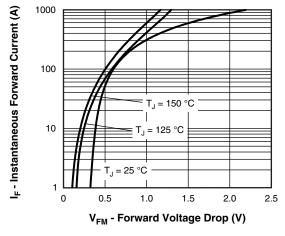


Fig. 1 - Maximum Forward Voltage Drop Characteristics

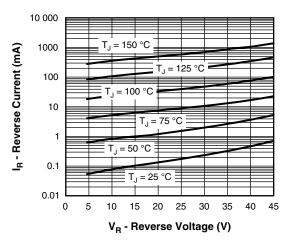


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



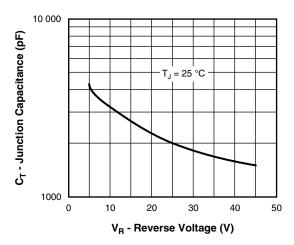


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

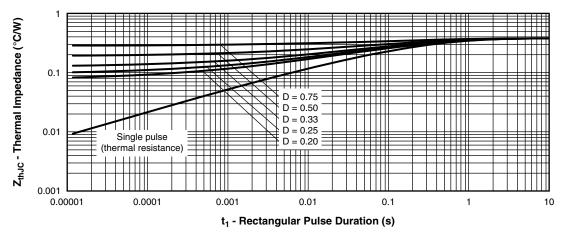


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

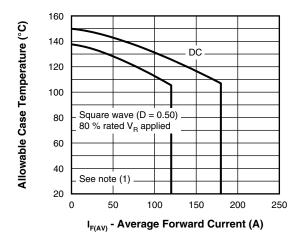


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

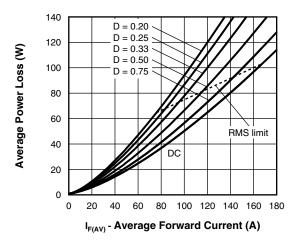
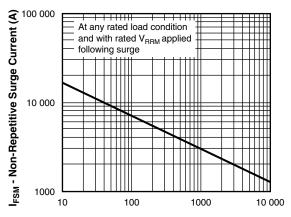


Fig. 6 - Forward Power Loss Characteristics



t_p - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current

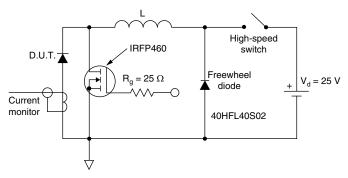


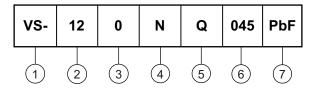
Fig. 8 - Unclamped Inductive Test Circuit

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D)}; I_R \text{ at } V_{R1} = \text{rated } V_R \\ \end{array}$

ORDERING INFORMATION TABLE

Device code



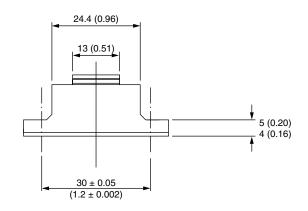
- 1 Vishay Semiconductors product
- 2 Average current rating (x 10)
- Product silicon identification
- 4 N = not isolated
- 5 Q = Schottky rectifier diode
- 6 Voltage rating (045 = 45 V)
- 7 Lead (Pb)-free

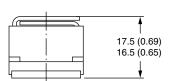
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95020			

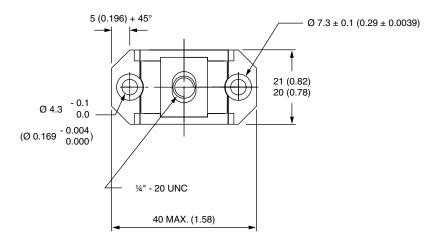


D-67 HALF-PAK

DIMENSIONS in millimeters (inches)









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