



DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	Rds(on) (MAX)	I _{D (MAX)} T _A = +25°C
30V	190mΩ @ V _{GS} = 10V	1A
307	$335m\Omega @ V_{GS} = 4.5V$	0.75A

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Motor Control
- DC-DC Converters
- Load Switch

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- **ESD Protected Gate**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- **PPAP Capable (Note 4)**

Mechanical Data

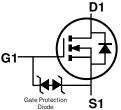
- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.006 grams (Approximate)

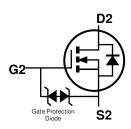


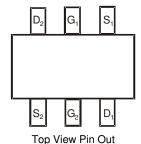


Top View









Q1 N-Channel

Q2 N-Channel

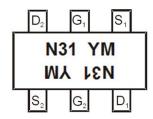
Ordering Information (Note 5)

Part Number	Case	Packaging		
DMN3190LDWQ-7	SOT363	3000/Tape & Reel		
DMN3190LDWQ-13	SOT363	10000/Tape & Reel		

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to https://www.diodes.com/quality/.
- 5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



N31 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: F = 2018)M = Month (ex: 9 = September)

Date Code Key

Year	201	8	2019		2020	20	21	2022		2023	2	2024
Code	F		G		Н			J		K		L
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



$\overline{\textbf{Maximum}} \ \textbf{Ratings} \ (@T_A = +25^{\circ}\text{C}, \text{ unless otherwise specified.})$

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		V_{DSS}	30	V	
Gate-Source Voltage		V _{GSS}	±20	V	
Continuous Drain Current (Note 7) V _{GS} = 10V	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	1000 900	mA	
Continuous Drain Current (Note 7) VGS = 10V	t < 5s	$t < 5s$ $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$ I_D		1300 1000	mA
Maximum Continuous Body Diode Forward Current	(Note 6)	Is	0.5	Α	
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%	s)	I _{DM}	2.0	А	

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)	T _A = +25°C	ב	0.32	W
Total Fower Dissipation (Note 6)	$T_A = +70^{\circ}C$	P_{D}	0.19	VV
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	D	395	°C/W
Thermal Resistance, Junction to Ambient (Note o)	t < 5s	$R_{\theta JA}$	320	G/VV
Total Power Dissipation (Note 7)	$T_A = +25^{\circ}C$	P_D	0.4	W
Total Fower Dissipation (Note 1)	$T_A = +70^{\circ}C$	PD	0.25	VV
Thermal Resistance, Junction to Ambient (Note 7)	Steady State	D	320	
memai hesistance, junction to Ambient (Note 7)	t < 5s	$R_{\theta JA}$	250	°C/W
Thermal Resistance, Junction to Case	$R_{ heta JC}$	143		
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic			Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage		BV _{DSS}	30	_	_	V	$V_{GS} = 0V$, $I_D = 1mA$
Zero Gate Voltage Drain Current @1	Г _С = +25°С	I _{DSS}	_	_	1	μΑ	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage		I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage		V _{GS(TH)}	1.5	_	2.8	V	$V_{DS}=V_{GS},\ I_D=250\mu A$
Static Drain-Source On-Resistance			_	122	190	mΩ	$V_{GS} = 10V, I_D = 1.3A$
Static Drain-Source On-Nesistance		R _{DS(ON)}	_	181	335	11177	$V_{GS} = 4.5V, I_D = 290mA$
Forward Transfer Admittance		Y _{fs}	_	0.7	_	mS	$V_{DS} = 10V, I_D = 250mA$
Diode Forward Voltage		V_{SD}	_	_	1.2	V	$V_{GS} = 0V, I_{S} = 250mA$
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance		C _{iss}	_	87	_	pF	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
Output Capacitance		Coss	_	17	_	pF	$V_{DS} = 20V, V_{GS} = 0V,$ - f = 1.0MHz
Reverse Transfer Capacitance		C_{rss}	_	12	_	pF	1 – 1.01011 12
Gate Resistance		R_g	_	69.8	_	Ω	$f = 1MHz$, $V_{GS} = 0V$, $V_{DS} = 0V$
Total Gate Charge (V _{GS} = 4.5V)		Q_g	_	0.9	_	nC	
Total Gate Charge (V _{GS} = 10V)		Qg	_	2.0	_	nC	\/ 10\/ L 050A
Gate-Source Charge		Q_{gs}	_	0.3	_	nC	$V_{DS} = 10V, I_D = 250mA$
Gate-Drain Charge		Q_{gd}	_	0.3	_	nC	
Turn-On Delay Time		t _{D(ON)}	_	4.5	_	ns	
Turn-On Rise Time		t _R	_	8.9	_	ns	$V_{DD} = 30V, V_{GS} = 10V,$
Turn-Off Delay Time		t _{D(OFF)}	_	30.3	_	ns	$R_G = 10\Omega, I_D = 100 \text{mA}$
Turn-Off Fall Time		t _F	_	15.6	_	ns	

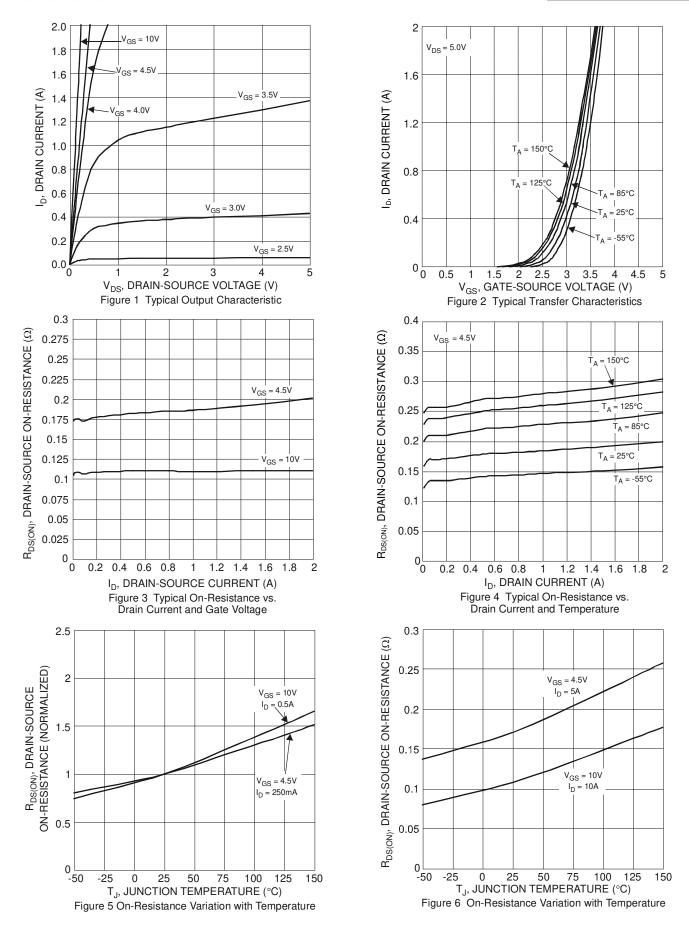
Notes: 6. Device mounted on FR-4 PCB, with minimum recommended pad layout.

^{7.} Device mounted on 1" \times 1" FR-4 PCB with high coverage 2oz. Copper, single sided.

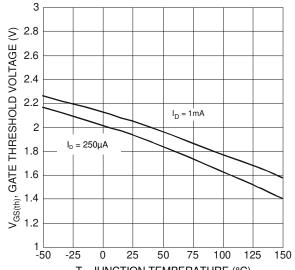
^{8.} Short duration pulse test used to minimize self-heating effect.

^{9.} Guaranteed by design. Not subject to product testing.

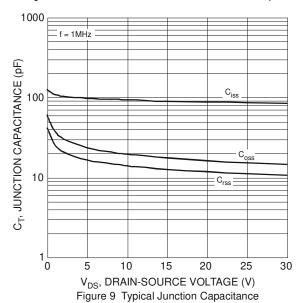


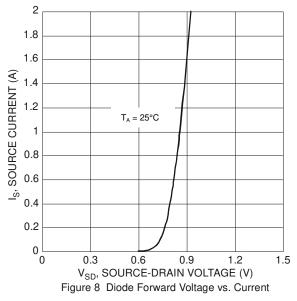


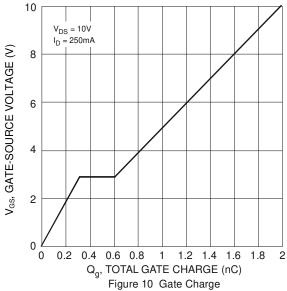




 $T_{\rm J},$ JUNCTION TEMPERATURE (°C) Figure 7 Gate Threshold Variation vs. Junction Temperature





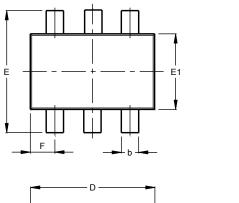


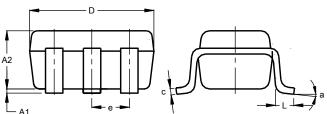


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363



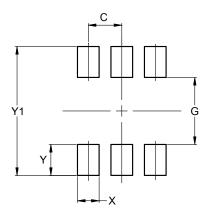


SOT363							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	0.95				
Ь	0.10	0.30	0.25				
C	0.10	0.22	0.11				
D	1.80	2.20	2.15				
Е	2.00	2.20	2.10				
E1	1.15 1.35 1.3		1.30				
е	C	.650 E	SC				
F	0.40	0.45	0.425				
١	0.25	0.40	0.30				
а	0°	8°					
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363



Dimensions	Value (in mm)			
С	0.650			
G	1.300			
Х	0.420			
Υ	0.600			
Y1	2.500			



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