



DMN2009UCA4

Product Summary

BVsss	Rss(on) Typ	Is Max Ta = +25°C
20V	10.2mΩ @ V _{GS} = 3.8V	10.3A

Description

This new generation MOSFET is designed to minimize the on-state resistance (Rss(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Battery managements
- Load switches
- Battery protections

N-CHANNEL ENHANCEMENT MODE MOSFET

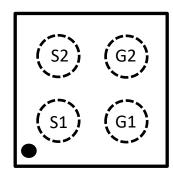
Features

- CSP with Footprint 1.75mm x 1.75mm
- Height = 0.120mm (Typical) for Low Profile
- ESD Protection of Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

Mechanical Data

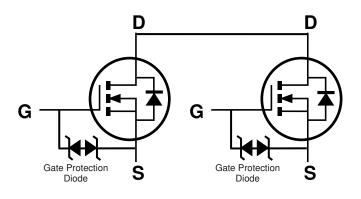
- Package: X4-DSN1717-4
- Terminal Connections: See Diagram Below
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiAu. Solderable per MIL-STD-202, Method 208 (e4)
- Weight: 0.0012 grams (Approximate)

ESD PROTECTED



X4-DSN1717-4

Top View



Equivalent Circuit

Ordering Information (Note 4)

Notes:

Part Number	Paakaga	Package Pack		
Fait Nulliber	Fackage	Qty.	Carrier	
DMN2009UCA4-7	X4-DSN1717-4	3000	Tape & Reel	

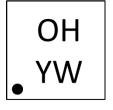
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and

Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information



 $\begin{array}{l} OH = \mbox{Product Type Marking Code} \\ YW = \mbox{Date Code Marking} \\ Y \mbox{ or } \overline{Y} = \mbox{Year (ex: 2 = 2022)} \\ W \mbox{ or } \overline{W} = \mbox{Week (ex: a = week 27; z represents week 52 and 53)} \end{array}$

Date Code Key												
Year	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	2	3	4	5	6	7	8	9	0	1	2	3
Week	Veek 1-26			27-52				53				
Code	A-Z				a	-Z			2	2		

Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit			
Source-Source Voltage	Vsss	20	V			
Gate-Source Voltage			V _{GSS}	±8	V	
	Steady State	TA = +25°C	le.	10.3	А	
Continuous Source Current (Note 5) $V_{GS} = 4.5V$		TA = +70°C	Is	8.3		
	Steady	TA = +25°C	1-	7.5	٨	
Continuous Source Current (Note 5) $V_{GS} = 2.5V$	State	TA = +70°C	Is	6.0	A	
Pulsed Source Current (Note 6)	lsм	64	А			

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 7)	PD	0.9	W
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$ (Note 7)	R _{0JA}	134	°C/W
Power Dissipation (Note 5)	PD	1.9	W
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$ (Note 5)	Reja	65	°C/W
Operating and Storage Temperature Range	TJ, T _{STG}	-55 to +150	°C

 Device mounted on FR-4 material with 1inch² (6.45cm²), 2oz. (0.071mm thick) Cu.
Repetitive rating, pulse width limited by junction temperature.
Device mounted on FR-4 PCB with minimum recommended pad layout, single sided. Notes:



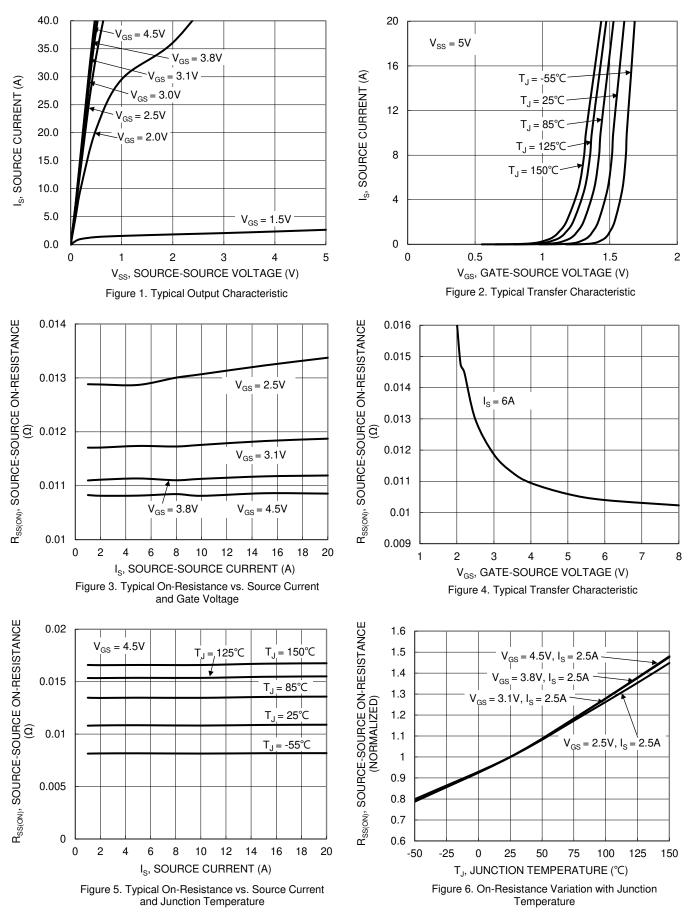
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)	-,		- 71				
Source -Source Breakdown Voltage	BVsss	20	_	_	V	$V_{GS} = 0V$, $I_{S} = 1mA$	
Zero Gate Voltage Source Current TJ = +25°C	lsss	_	_	1	μA	Vss = 16V, Vgs = 0V	
Cata Source Laskage	1	_	—	±10	μA	$V_{GS} = \pm 8V, V_{SS} = 0V$	
Gate-Source Leakage	IGSS	—	_	±1	μA	$V_{GS} = \pm 5V, V_{SS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	0.35		1.4	V	$V_{SS} = 10V$, $I_{S} = 0.64mA$	
		7	9.8	11.9		$V_{GS} = 4.5V, I_S = 2.5A$	
Static Source-Source On-Resistance	Destaut	7.3	10.2	12.9	mΩ	VGS = 3.8V, IS = 2.5A	
Static Source-Source On-Resistance	Rss(on)	8.1	10.8	15.8		V _{GS} = 3.1V, I _S = 2.5A	
		8.6	12	22.6		V _{GS} = 2.5V, I _S = 2.5A	
Diode Forward Voltage	Vss	—	_	1.2	V	V _{GS} = 0V, I _S = 2.5A	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	Ciss	—	1780	—			
Output Capacitance	Coss	—	190	—	pF	$V_{SS} = 10V, V_{GS} = 0V$ f = 1.0kHz	
Reverse Transfer Capacitance	Crss	—	107	—			
Total Gate Charge	Qg	—	17.5	—			
Gate-Source Charge	Q _{gs}	—	2.9	—	nC	$V_{DD} = 10V, V_{GS} = 4V$ Is = 2.5A	
Gate-Drain Charge	Qgd	_	4.3	—		15 = 2.3A	
Turn-On Delay Time	tD(ON)	_	0.22	—			
Turn-On Rise Time	tR	_	0.42	—	1	$V_{DD} = 10V, V_{GS} = 4V$	
Turn-Off Delay Time	tD(OFF)	_	1.48	—	μs	I _S = 2.5A	
Turn-Off Fall Time	tF	_	0.62	_			

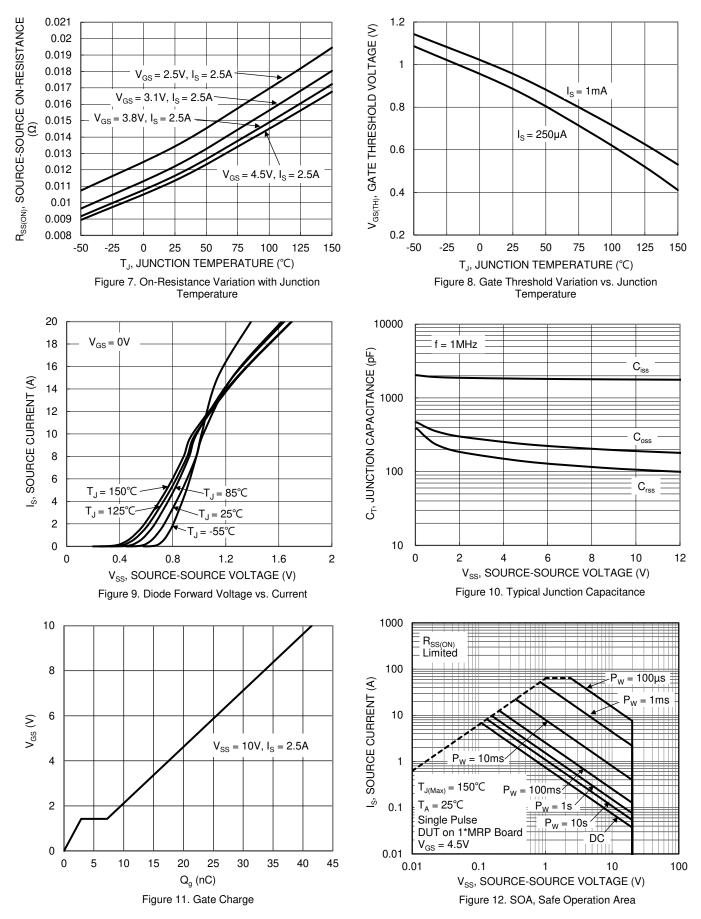
8. Short duration pulse test used to minimize self-heating effect.9. Guaranteed by design. Not subject to production testing. Notes:



DMN2009UCA4









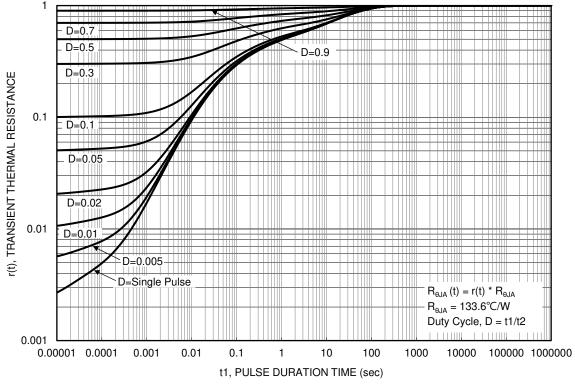
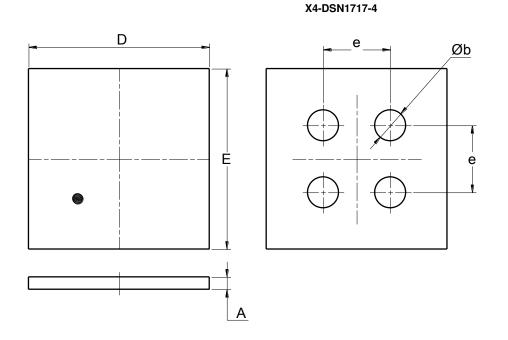


Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

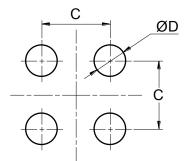


X4-DSN1717-4							
Dim	Min Max Typ						
Α	0.080	0.150	0.120				
b	0.270	0.330	0.300				
D	1.710	1.790	1.750				
Е	1.710	1.790	1.750				
е	0.650 BSC						
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X4-DSN1717-4



Dimensions	Value (in mm)
С	0.650
D	0.300



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