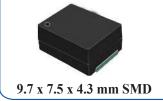
AOCJYR-24.576MHz-M6069LF







Moisture Sensitivity Level (MSL) – 1

### > **OVERVIEW:**

Abracon's AOCJYR series of World's Smallest Profile, Surface Mount- Ovenized Quartz Crystal Oscillators are based on Proprietary Mercury<sup>TM</sup> ASIC technology, patented by Rakon. This Advanced Technology coupled with Rakon's proprietary manufacturing techniques enable  $\pm 10$  ppb stability over -20°C to +70°C, with typical short-term aging of better than  $\pm 2$  ppb per day.

Sophisticated Integrated Oven Control architecture ensures fast warm-up time, while minimizes initial power consumption to 350mW typical at 25°C. Further, the integration of critical functionality improves overall product reliability by reducing FIT rates 10x relative to traditional discrete OCXOs.

The AOCJYR series is offered in Industry leading 9.7 x 7.5 x 4.3 mm SMT package, while AOCJYR-DIL is available in 21.7 x 13.08 x 8.6 mm leaded hermetic package.

#### > FEATURES:

- Compact package size: 9.7 x 7.5 x 4.3mm
- Frequency stability over temperature as low as ±50ppb over -40 to +85°C
- Low power consumption
- High reliability

#### > APPLICATIONS:

- Stratum 3
- Small Cells
- Switches and Routers
- Time & Frequency References
- SyncE and IEEE 1588

#### STANDARD SPECIFICATIONS:

Parameters		Minimum	Typical	Maximum	Units	Notes
Nominal Frequency		24.576			MHz	
Supply Voltage (Vdd)		3.135	3.3	3.465	V	
Input Power (warm-up)			1000		mW	
Input Power (steady-state)				400	mW	@25°C still air
Operable Temperature Range		-40		85	° C	
Storage Temperature Range		-55		+125	° C	
Initial Frequency Tolerance @25°C At time of shipment				±0.5	ppm	See Note 1
Reflow Shift				±1	ppm	After 1hr recovery @25°C
Frequency Stability over Operating Temperature Range in Still Air				±50	ppb	Ref. to (F <sub>MAX</sub> +F <sub>MIN</sub> )/2. See Note 1
Slope in Still Air				±2	ppb/°C	Temperature ramp 1°C/minute max.
Stability vs. Supply Voltage Change			±10		ppb	±5% variation in Vdd, ref. to freq. @ Vdd=3.3V
Load Coefficient			±10		ppb	±5pF variation in load, ref. to freq. @ 15pF load
Frequency Aging (per day)				±2	ppb	See Note 3
Frequency Aging (long-term stability)	First Year			±1	ppm	
	10 Years			±3	ppm	
Warm-up Time			<3		minute	See Note 2

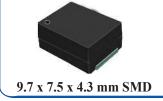




AOCJYR-24.576MHz-M6069LF







## STANDARD SPECIFICATIONS CONTINUED:

Parameters	Minimum	Typical	Maximum	Units	Notes
		$7x10^{-11}$			@25°C, τ=0.1s
		$7x10^{-11}$			@25°C, τ=1.0s
Root Allan Variance		$7x10^{-11}$			@25°C, τ=10s
		8x10 <sup>-11</sup>			@25°C, τ=100s
		8x10 <sup>-11</sup>			@25°C, τ=1000s
Acceleration Sensitivity		<2		ppb/g	Gamma vector of all 3 axes from 30Hz to 1500Hz
Output Type		LVCMOS			
High-level Output Voltage (V <sub>OH</sub> )	90%*Vdd			V	
Low-level Output Voltage (V <sub>OL</sub> )			10%*Vdd	V	
Output Load	10	15	20	pF	
Rise and Fall Time (t <sub>r</sub> , t <sub>f</sub> )			4	ns	
Duty Cycle	45		55	%	Measured at 50% level
Control Voltage (Vc)	0.5		2.5	V	
Frequency Tuning Range (over Control Voltage range)	±5		±15	ppm	Ref. to Frequency @ Vc=1.5V
Frequency Tuning Linearity			1	%	Deviation from linear over control voltage range
Slope Positive					
Port Input Impedance	80			kΩ	
Modulation Bandwidth		3.5		kHz	
Phase Noise @ 24.576MHz Carrier					
@ 1 Hz offset		-55		dBc / Hz	
@ 10 Hz offset		-88		dBc / Hz	
@ 100 Hz offset		110		dBc / Hz	
@ 1,000 Hz offset		-135		dBc / Hz	
@ 10,000 Hz offset		-148		dBc / Hz	
@ 100,000 Hz offset		-152		dBc / Hz	
@ 1,000,000 Hz offset		-153		dBc / Hz	

#### Note:

- 1. The characteristics of the component may be temporarily affected by the processes of assembly and soldering. The frequency specifications apply 48 hours after assembly. Nominal conditions apply unless otherwise stated.
- 2. Time needed for frequency to be within ±20ppb reference to frequency after 1hour, at 25°C. Parameter is assembly and operating history dependent
- 3. After 30 days of continuous operation.

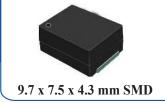




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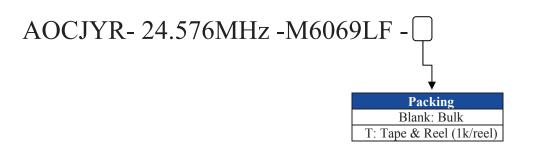




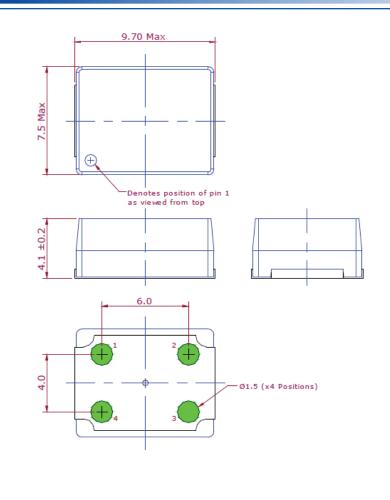
## REFERENCE DESIGN INFORMATION

AOCJYR-24.576MHZ-M6069LF is equivalent to Rakon P/N M6069LF.

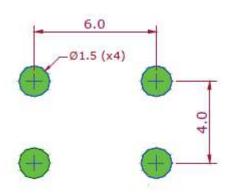
### > PART IDENTIFICATION:



## **OUTLINE DIMENSION:**



#### **Recommended Land Pattern**



Pin	Function
1	Control Voltage
2	Ground
3	RF-output
4	Supply Voltage

#### Note:

- 1. For correct operation, decouple the supply voltage with a  $10\mu F$  capacitor close to the oscillator.
- 2. The GND of the control voltage needs to be connected directly to pin 2 as ground lead impedance may cause performance degradation.

**Dimension: mm** 

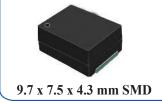




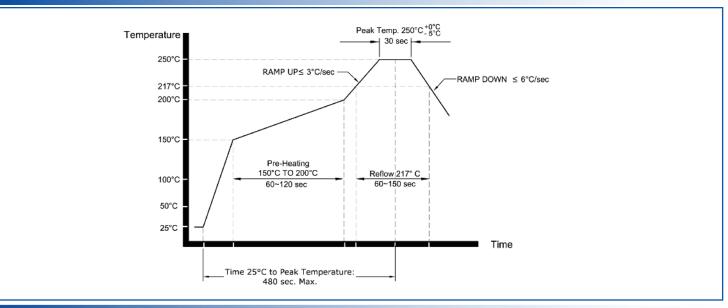
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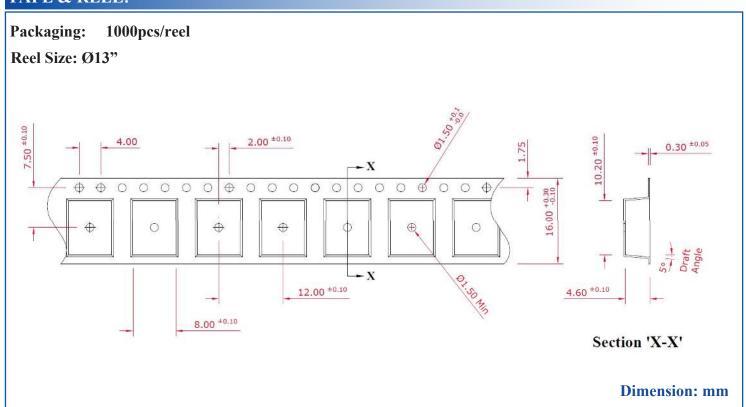




### **REFLOW PROFILE:**



## **► TAPE & REEL:**



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