

BCW66GLT1

General Purpose Transistor

NPN Silicon

Features

- Pb-Free Package is Available

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V_{CEO}	45	Vdc
Collector–Base Voltage	V_{CBO}	75	Vdc
Emitter–Base Voltage	V_{EBO}	5.0	Vdc
Collector Current – Continuous	I_C	800	mAdc

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

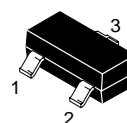
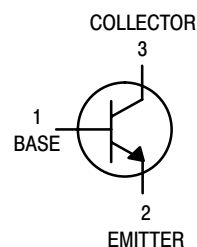
Characteristic	Symbol	Max	Unit
Total Device Dissipation FR–5 Board (Note 1), $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	225	mW
		1.8	mW/ $^\circ\text{C}$
Thermal Resistance, Junction–to–Ambient	$R_{\theta JA}$	556	$^\circ\text{C}/\text{W}$
Total Device Dissipation Alumina Substrate, (Note 2) $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	300	mW
		2.4	mW/ $^\circ\text{C}$
Thermal Resistance, Junction–to–Ambient	$R_{\theta JA}$	417	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature	T_J, T_{stg}	–55 to +150	$^\circ\text{C}$

- FR–5 = $1.0 \times 0.75 \times 0.062$ in.
- Alumina = $0.4 \times 0.3 \times 0.024$ in 99.5% alumina.



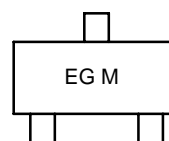
ON Semiconductor®

<http://onsemi.com>



SOT–23
CASE 318
STYLE 6

MARKING DIAGRAM



EG = Specific Device Code
M = Date Code

ORDERING INFORMATION

Device	Package	Shipping†
BCW66GLT1	SOT–23	3000 / Tape & Reel
BCW66GLT1G	SOT–23 (Pb–Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

Collector–Emitter Breakdown Voltage (I _C = 10 mAdc, I _B = 0)	V _{(BR)CEO}	45	–	–	Vdc
Collector–Emitter Breakdown Voltage (I _C = 10 μAdc, V _{EB} = 0)	V _{(BR)CES}	75	–	–	Vdc
Emitter–Base Breakdown Voltage (I _E = 10 μAdc, I _C = 0)	V _{(BR)EBO}	5.0	–	–	Vdc
Collector Cutoff Current (V _{CE} = 45 Vdc, I _E = 0) (V _{CE} = 45 Vdc, I _E = 0, T _A = 150°C)	I _{CES}	–	–	20	nAdc μAdc
Emitter Cutoff Current (V _{EB} = 4.0 Vdc, I _C = 0)	I _{EBO}	–	–	20	nAdc

ON CHARACTERISTICS

DC Current Gain (I _C = 100 μAdc, V _{CE} = 10 Vdc) (I _C = 10 mAdc, V _{CE} = 1.0 Vdc) (I _C = 100 mAdc, V _{CE} = 1.0 Vdc) (I _C = 500 mAdc, V _{CE} = 2.0 Vdc)	h _{FE}	50 110 160 60	– – – –	– – 400 –	–
Collector–Emitter Saturation Voltage (I _C = 500 mAdc, I _B = 50 mAdc) (I _C = 100 mAdc, I _B = 10 mAdc)	V _{CE(sat)}	– –	0.7 0.3	– –	Vdc
Base–Emitter Saturation Voltage (I _C = 500 mAdc, I _B = 50 mAdc)	V _{BE(sat)}	–	–	2.0	Vdc

SMALL–SIGNAL CHARACTERISTICS

Current–Gain — Bandwidth Product (I _C = 20 mAdc, V _{CE} = 10 Vdc, f = 100 MHz)	f _T	100	–	–	MHz
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 1.0 MHz)	C _{obo}	–	–	12	pF
Input Capacitance (V _{EB} = 0.5 Vdc, I _C = 0, f = 1.0 MHz)	C _{ibo}	–	–	80	pF
Noise Figure (V _{CE} = 5.0 Vdc, I _C = 0.2 mAdc, R _S = 1.0 kΩ, f = 1.0 kHz, BW = 200 Hz)	NF	–	–	10	dB

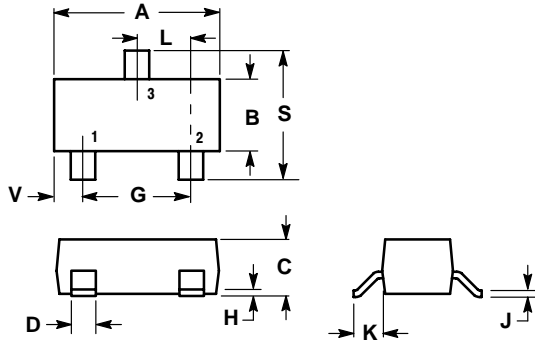
SWITCHING CHARACTERISTICS

Turn–On Time (I _{B1} = I _{B2} = 15 mAdc)	t _{on}	–	–	100	ns
Turn–Off Time (I _C = 150 mAdc, R _L = 150 Ω)	t _{off}	–	–	400	ns

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PACKAGE DIMENSIONS

SOT-23 (TO-236)
CASE 318-08
ISSUE AK

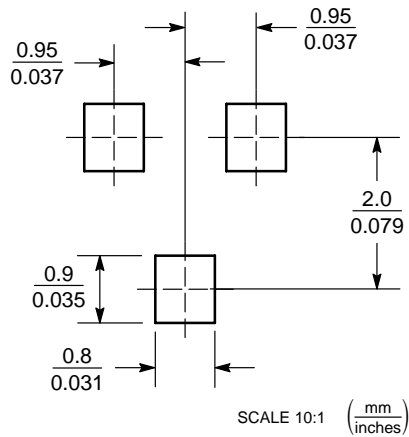


NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. 318-03 AND -07 OBSOLETE, NEW STANDARD 318-08.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.1102	0.1197	2.80	3.04
B	0.0472	0.0551	1.20	1.40
C	0.0350	0.0440	0.89	1.11
D	0.0150	0.0200	0.37	0.50
G	0.0701	0.0807	1.78	2.04
H	0.0005	0.0040	0.013	0.100
J	0.0034	0.0070	0.085	0.177
K	0.0140	0.0285	0.35	0.69
L	0.0350	0.0401	0.89	1.02
S	0.0830	0.1039	2.10	2.64
V	0.0177	0.0236	0.45	0.60

SOLDERING FOOTPRINT*




STYLE 6:

- PIN 1. BASE
- EMITTER
- COLLECTOR

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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