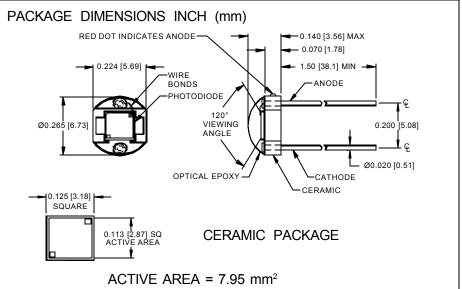
# PHOTONIC DETECTORS INC.

# Silicon Photodiode, U.V. Enhanced Photovoltaic Type PDU-V113





### FEATURES

- Low noise
- U.V. enhanced
- High shunt resistance
- High response

The **PDU-V113** is a silicon, PIN planar diffused, U.V. enhanced photodiode. Ideal for low noise photovoltaic applications. Packaged on a two lead ceramic substrate with a clear U.V. epoxy glob top.

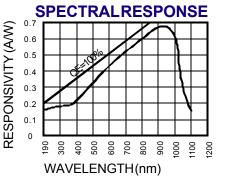
#### **APPLICATIONS**

- U.V. exposure meter
- Water purification
- Fluorescence
- U.V. A & B meters

## **ABSOLUTE MAXIMUM RATING** (TA=25°C unless otherwise noted)

DESCRIPTION

SYMBOL	PARAMETER	MIN	MAX	UNITS	
Vbr	Reverse Voltage		75	V	
T <sub>STG</sub>	Storage Temperature	-40	+100	оС	
To	Operating Temperature Range	-40	+90	°C	
Ts	Soldering Temperature*		+240	°C	
Ι <sub>L</sub>	Light Current		500	mA	



\*1/16 inch from case for 3 secs max

#### ELECTRO-OPTICAL CHARACTERISTICS (TA=25°C unless otherwise noted)

SYMBOL	CHARACTERISTIC	TESTCONDITIONS	MIN	TYP	MAX	UNITS
lsc	Short Circuit Current	H = 100 fc, 2850 K	60	80		$\mu$ A
ΙD	Dark Current	H = 0, V <sub>R</sub> = 10 V		300	500	pА
Rsh	Shunt Resistance	H = 0, V <sub>R</sub> = 10 mV	100	200		MΩ
TC RSH	RsH Temp. Coefficient	H = 0, V <sub>R</sub> = 10 mV		-8		% / °C
CJ	Junction Capacitance	H = 0, V <sub>R</sub> = 0 V**		800		pF
λrange	Spectral Application Range	Spot Scan	250		1100	nm
λρ	Spectral Response - Peak	Spot Scan		850		nm
Vbr	Breakdown Voltage	I = 10 μA	30	50		V
NEP	Noise Equivalent Power	V <sub>R</sub> = 10 mV @ Peak		6x10 <sup>-14</sup>		W/ √ Hz
tr	Response Time	$RL = 1 K\Omega V_R = 0 V$		750		nS

Information in this technical data sheet is believed to be correct and reliable. However, no responsibility is assumed for possible inaccuracies or omission. Specifications are subject to change without notice.\*\*f=1MHz [FORM\_NO. 100-PDU-V113\_REV\_A]