



#### DMTH6012LPSWQ

#### 60V +175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

#### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D MAX</sub> T <sub>C</sub> = +25°С	
60V	14mΩ @ V <sub>GS</sub> = 10V	50.5A	
	21mΩ @ V <sub>GS</sub> = 4.5V	41.2A	

## **Description and Applications**

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

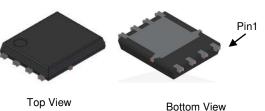
- Engine Management Systems
- **Body Control Electronics**
- **DC-DC Converters**

#### Features

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production-Ensures More Reliable and Robust End Application
- Low R<sub>DS(ON)</sub> Minimizes On State Losses
- Low Input Capacitance
- Fast Switching Speed
- Wettable Flank for Improved Optical Inspection
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMTH6012LPSWQ is suitable for automotive applications requiring specific change control and is AEC-Q101 qualified, is PPAP capable, and is manufactured in IATF16949:2016 certified facilities.

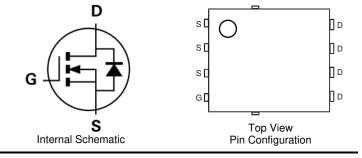
#### **Mechanical Data**

- Case: PowerDI<sup>®</sup>5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.097 grams (Approximate)



PowerDI5060-8 (SWP) (Type Q)





#### **Ordering Information** (Note 4)

Part Number	Case	Packaging
DMTH6012LPSWQ-13	PowerDI5060-8 (SWP) (Type Q)	2,500 / Tape & Reel

1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied. 2. See http://www.diodes.com/quality/lead\_free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"

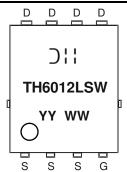
and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

#### Marking Information

Notes:



DII = Manufacturer's Marking TH6012LSW = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 19 = 2019) WW = Week Code (01 to 53)

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# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		V <sub>DSS</sub>	60	V	
Gate-Source Voltage		V <sub>GSS</sub>	±20	V	
	T <sub>A</sub> = +25°C		11.5	•	
Continuous Drain Current, V <sub>GS</sub> = 10V (Note 5)	$T_A = +100^{\circ}C$	I <sub>D</sub>	8.1	A	
	T <sub>C</sub> = +25°C	- I <sub>D</sub>	50.5	•	
Continuous Drain Current, V <sub>GS</sub> = 10V (Note 6)	$T_{C} = +100^{\circ}C$		35.7	A	
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)		I <sub>DM</sub>	200	A	
Maximum Continuous Body Diode Forward Current (Note 6)		Is	50	A	
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)		I <sub>SM</sub>	200	A	
Avalanche Current, L=0.1mH		las	12.6	A	
Avalanche Energy, L=0.1mH		E <sub>AS</sub>	7.9	mJ	

### **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	PD	2.8	W
Thermal Resistance, Junction to Ambient (Note 5)		$R_{ ext{ heta}JA}$	54	°C/W
Total Power Dissipation (Note 6)	T <sub>C</sub> = +25°C	PD	53.6	W
Thermal Resistance, Junction to Case (Note 6)		$R_{ ext{ heta}JC}$	2.8	°C/W
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +175	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

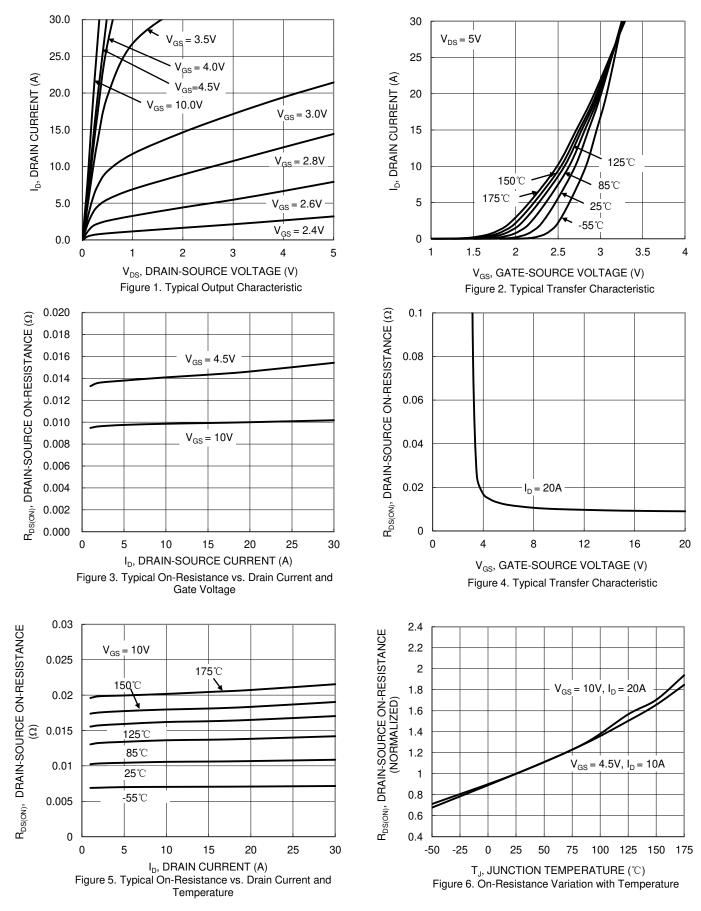
Oberestavistic	Cumphiel	Min	Ture	May	11	Test Condition
	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)		00	1		V	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	—	—	V	$V_{GS} = 0V, I_D = 1mA$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_		1	μA	$V_{DS} = 48V, V_{GS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)			1		1	
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1	—	2.3	V	$V_{DS}=V_{GS},\ I_{D}=250\mu A$
Static Drain-Source On-Resistance			10.6	14	mΩ	$V_{GS} = 10V, I_D = 20A$
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>		14.8	21	11152	$V_{GS} = 4.5V, I_D = 10A$
Diode Forward Voltage	V <sub>SD</sub>	_	0.7	1.2	V	$V_{GS} = 0V, I_S = 1A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C <sub>iss</sub>		785	—		$V_{DS}$ = 30V, $V_{GS}$ = 0V, f = 1MHz
Output Capacitance	Coss	—	281	—	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>		27	—		
Gate Resistance	Rg	—	1.5	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg		7.3	—		V <sub>DS</sub> = 30V, I <sub>D</sub> = 10A
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	-	13.6	—	nC	
Gate-Source Charge	Q <sub>gs</sub>		2.2	—	10	
Gate-Drain Charge	Q <sub>gd</sub>		3.4	—		
Turn-On Delay Time	t <sub>D(ON)</sub>	_	3.2	_		$V_{DD} = 30V, V_{GS} = 10V,$ $I_D = 10A, R_g = 6\Omega$
Turn-On Rise Time	t <sub>R</sub>	_	4.4	_		
Turn-Off Delay Time	t <sub>D(OFF)</sub>		14.7	_	ns	
Turn-Off Fall Time	t <sub>F</sub>		8.5	—		
Body Diode Reverse Recovery Time	t <sub>RR</sub>		23.0	—	ns	I <sub>F</sub> = 10A, di/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>		14.1	_	nC	$T_F = 10A$ , $u/ut = 100A/\mu s$

Notes: 5. Device mounted on FR-4 substrate PCB, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

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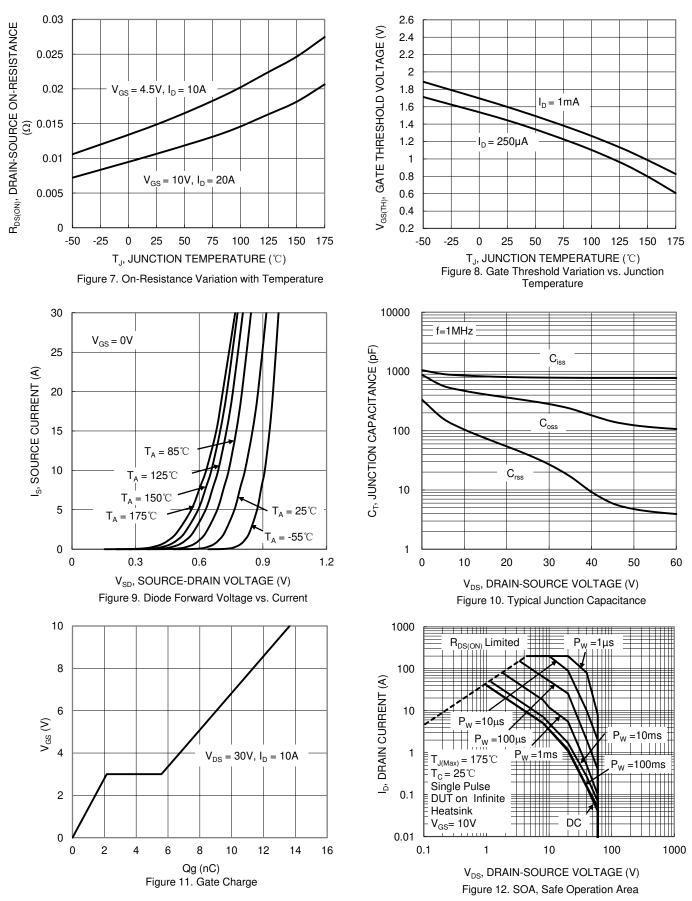
#### DMTH6012LPSWQ



DMTH6012LPSWQ Document number: DS41534 Rev. 3 - 2



## DMTH6012LPSWQ





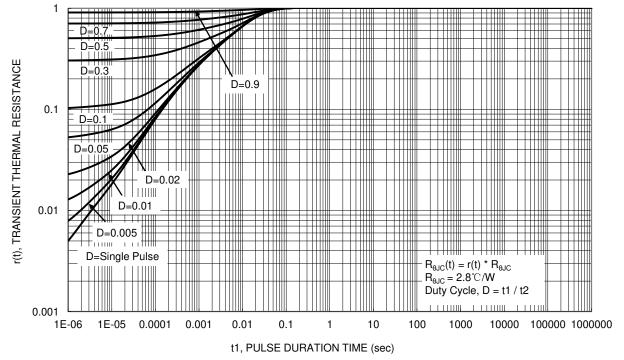
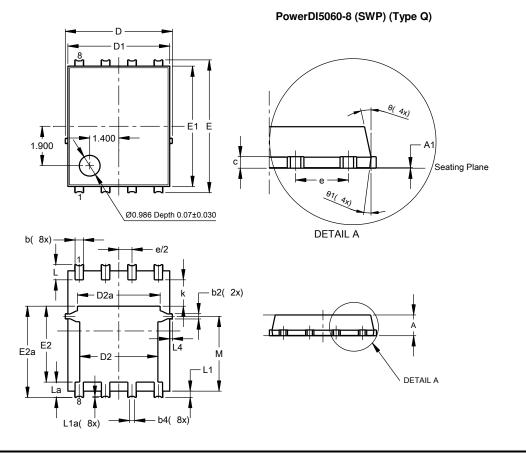


Figure 13. Transient Thermal Resistance



### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

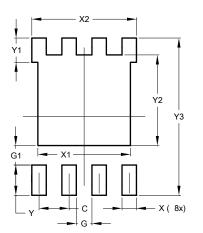


Ρον	PowerDI5060-8 (SWP)					
-	(Type Q)					
Dim	Min	Max	Тур			
Α	0.90	1.10	1.00			
A1	0	0.05				
b	0.30	0.50	0.41			
b2	0.20	0.35	0.25			
b4	C	).25REF				
С	0.230	0.330	0.277			
D	-	.15 BS0	2			
D1	4.70	5.10	4.90			
D2	3.56	3.96	3.76			
D2a	3.78	4.18	3.98			
Е	6	.40 BS0	2			
E1	5.60	6.00	5.80			
E2	3.46	3.86	3.66			
E2a	4.195	4.595	4.395			
е	1	.27BSC	)			
k	1.05					
L	0.635	0.835	0.735			
La	0.635	0.835	0.735			
L1	0.200	0.400	0.300			
L1a	0.050REF					
L4	0.025	0.225	0.125			
М	3.205	4.005	3.605			
θ	10°	12°	11°			
θ1	6°	8°	7°			
All	All Dimensions in mm					

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI5060-8 (SWP) (Type Q)



Dimensions	Value		
Dimensions	(in mm)		
С	1.270		
G	0.660		
G1	0.820		
Х	0.610		
X1	4.100		
X2	4.420		
Y	1.270		
Y1	1.020		
Y2	3.810		
Y3	6.610		



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