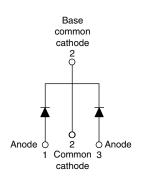


Vishay Semiconductors

# Schottky Rectifier, 2 x 30 A

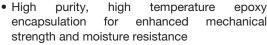


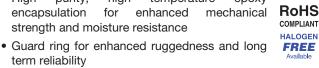


PRODUCT SUMMARY					
Package	TO-220AB				
I <sub>F(AV)</sub>	2 x 30 A				
V <sub>R</sub>	100 V				
V <sub>F</sub> at I <sub>F</sub>	0.69 V				
I <sub>RM</sub> max.	20 mA at 125 °C				
T <sub>J</sub> max.	175 °C				
Diode variation	Common cathode				
E <sub>AS</sub>	11.25 mJ				

### **FEATURES**

- 175 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation





- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

### **DESCRIPTION**

This center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	OL CHARACTERISTICS VALUES					
I <sub>F(AV)</sub>	Rectangular waveform (per device)	60	А			
V <sub>RRM</sub>		100	V			
I <sub>FRM</sub>	T <sub>C</sub> = 139 °C (per leg)	60	Α			
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	1500	A			
V <sub>F</sub>	30 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.69	V			
T <sub>J</sub>	Range	- 65 to 175	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-63CTQ100PbF	VS-63CTQ100-N3	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	100 100		V	
Maximum working peak reverse voltage	V <sub>RWM</sub>	100	100	V	

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST COND	TEST CONDITIONS		UNITS		
Maximum average per leg		50 % duty cycle at T <sub>C</sub> = 139 °C, rectangular waveform		500/   1   1   7   100 00   1   1   1		30	
forward current per device	P I <sub>F(AV)</sub>			60			
Peak repetitive forward current per leg	I <sub>FRM</sub>	Rated V <sub>R</sub> , square wave, 20 kHz, T <sub>C</sub> = 140 °C		60	Α		
Maximum peak one cycle non-repetitive	1	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1500			
surge current per leg	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	300			
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 0.75 A, L = 40 mH		11.25	mJ		
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>B</sub> typical		0.75	А		



# VS-63CTQ100PbF, VS-63CTQ100-N3

# Vishay Semiconductors

ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	NDITIONS	TYP.	MAX.	UNITS	
		30 A	T 05 °C	0.78	0.82	V	
Maximum forward valtage drap	V <sub>FM</sub> <sup>(1)</sup>	60 A	T <sub>J</sub> = 25 °C	0.94	1.0		
Maximum forward voltage drop		30 A	T 105 %0	0.64	0.69		
		60 A	T <sub>J</sub> = 125 °C	0.78	0.83		
Maximum instantaneous reverse current	I <sub>RM</sub>	T <sub>J</sub> = 25 °C	Rated DC voltage	0.02	0.3	mA	
Maximum instantaneous reverse current		T <sub>J</sub> = 125 °C	hated DC voltage	11	20	IIIA	
Maximum junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range 100 kHz to 1 MHz) 25 °C		11	00	pF	
Typical series inductance	L <sub>S</sub>	Measured from top of terminal to mounting plane			.0	nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000		V/µs		

### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 65 to 175	°C			
Maximum thermal resistance, junction to case per leg	R <sub>thJC</sub>	DC operation	1.2	- °C/W			
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50				
Approximate weight			2	g			
Approximate weight			0.07	OZ.			
Mounting torque minimum		Non-lubricated threads	6 (5)	kgf · cm			
maximum	]	Non-iublicateu tilleaus	12 (10)	(lbf $\cdot$ in)			
Marking device		Case style TO-220AB	63CT	Q100			

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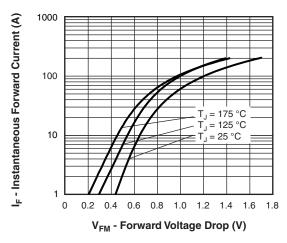


Fig. 1 - Maximum Forward Voltage Drop Characteristics

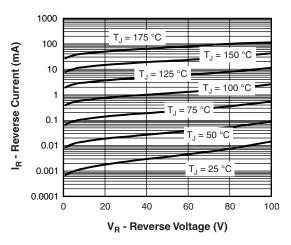


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

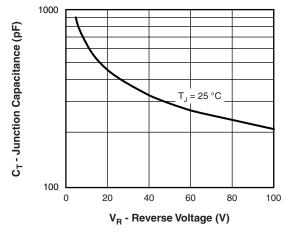


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

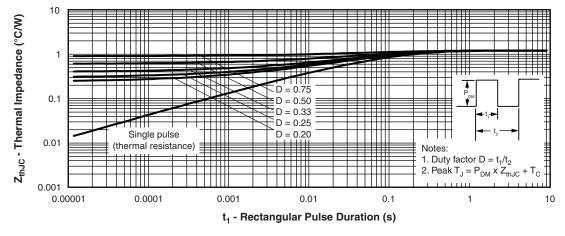


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics



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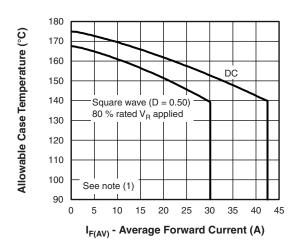


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

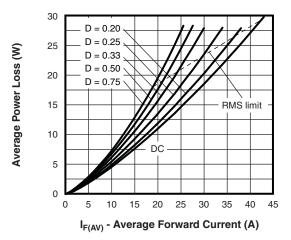


Fig. 6 - Forward Power Loss Characteristics

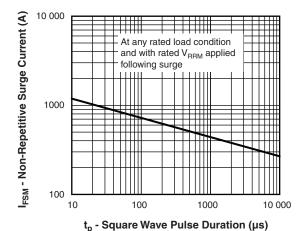


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

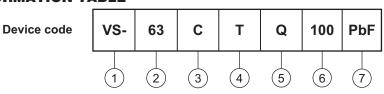
#### Note

Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;  $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = 80 \%$  rated  $V_R$ 

# VS-63CTQ100PbF, VS-63CTQ100-N3

Vishay Semiconductors

### **ORDERING INFORMATION TABLE**



1 - Vishay Semiconductors product

2 - Current rating (60 A)

Circuit configuration

C = Common cathode

4 - Package

T = TO-220

5 - Schottky "Q" series

6 - Voltage rating (100 = 100 V)

7 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-63CTQ100PbF	50	1000	Antistatic plastic tube				
VS-63CTQ100-N3	50	1000	Antistatic plastic tube				

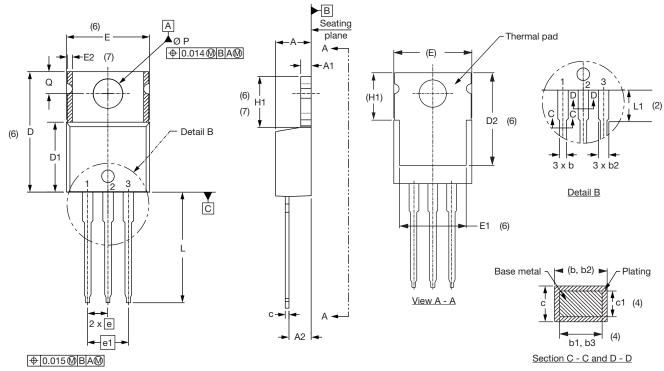
LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95222					
Dort marking information	TO-220AB PbF	www.vishay.com/doc?95225			
Part marking information	TO-220AB -N3	www.vishav.com/doc?95028			



## Vishay Semiconductors

## **TO-220AB**

### **DIMENSIONS** in millimeters and inches



### Lead assignments



- Anode/open
   Cathode
- 3. Anode

**Diodes** 

### Conforms to JEDEC outline TO-220AB

SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

CVM	SYMBOL MILLIMETERS IN		INC	HES	NOTES	
STIVI	BUL	MIN.	MAX.	MIN.	MAX.	NOTES
Е		10.11	10.51	0.398	0.414	3, 6
Е	1	6.86	8.89	0.270	0.350	6
E	2	-	0.76	-	0.030	7
e	)	2.41	2.67	0.095	0.105	
e	1	4.88	5.28	0.192	0.208	
Н	1	6.09	6.48	0.240	0.255	6, 7
L	-	13.52	14.02	0.532	0.552	
L	1	3.32	3.82	0.131	0.150	2
Ø	Р	3.54	3.73	0.139	0.147	
C	)	2.60	3.00	0.102	0.118	
$\epsilon$	)	90° to 93°		90° t	o 93°	

### **Notes**

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- $^{(7)}$  Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline

Lead tip



# **Legal Disclaimer Notice**

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