



DMJ70H1D4SV3

N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D T _C = +25°C
700V	1.5Ω @ $V_{GS} = 10V$	5.0A

Features and Benefits

- Low On-Resistance
- High BV_{DSS} Rating for Power Application
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

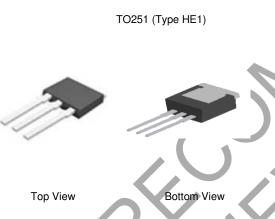
Description and Applications

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

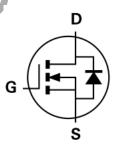
- Adaptor
- LCD & PDP TV
- Lighting

Mechanical Data

- Case: TO251 (Type HE1)
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208@3
- Weight: 0.33 grams (Approximate)







Top View Pin Configuration

Internal Schematic

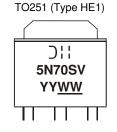
Ordering Information (Note 4)

Part Number	Case	Packaging
DMJ70H1D4SV3	TO251 (Type HE1)	75 Pieces / Tube

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and
- <1000ppm antimony compounds.</p>
 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



DII = Manufacturer's Marking
5N70SV = Product Type Marking Code
YYWW = Date Code Marking
YY or YY= Last Two Digits of Year (ex: 18 = 2018)
WW or WW = Week Code (01 to 53)



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Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V_{DSS}	700	V	
Gate-Source Voltage			±30	V
Continuous Drain Current (Note 5) V _{GS} = 10V	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	I _D	5.0 3.2	А
Maximum Body Diode Forward Current (Note 6)		Is	3.5	Α
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)		I _{DM}	6.0	Α
Avalanche Current (Note 7)	L = 60mH	I _{AS}	0.5	Α
Avalanche Energy (Note 7)	L = 60mH	E _{AS}	7.5	mJ
Peak Diode Recovery dv/dt (Note 7)		dv/dt	6	V/ns

Thermal Characteristics (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Total Power Dissipation (Note 5)	$T_C = +25^{\circ}C$ $T_C = +100^{\circ}C$	PD	78 31	W	
Thermal Resistance, Junction to Ambient (Note 6)		$R_{\theta JA}$	78	°C/W	
Thermal Resistance, Junction to Case (Note 5)		R _{0JC}	1.8	- C/VV	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

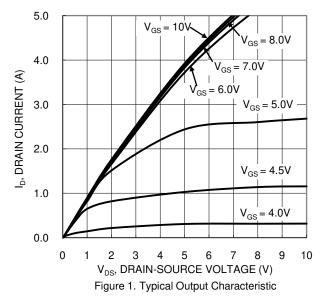
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)					•		
Drain-Source Breakdown Voltage	BV _{DSS}	700	_		V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	\ <u> </u>	1	μΑ	V _{DS} = 700V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}	-	1	100	nA	$V_{GS} = \pm 30V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	2	3.4	4	V	$V_{DS}=V_{GS},\ I_D=250\mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	/	1.2	1.5	Ω	$V_{GS} = 10V$, $I_D = 1A$	
Diode Forward Voltage	V_{SD}	-	0.85	1.3	V	$V_{GS} = 0V$, $I_S = 1A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	C _{iss}	<u> </u>	342	_		V 50V f 1MH7	
Output Capacitance	Coss		65	_	pF	$V_{DS} = 50V$, $f = 1MHz$, $V_{GS} = 0V$	
Reverse Transfer Capacitance	C _{rss}	_	0.5	_		VGS = UV	
Gate Resistance	R _G	_	4.1	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Q_g	_	7.5	_		V F60V I- 2.2A	
Gate-Source Charge	Q_{gs}	_	1.7	_	nC	$V_{DD} = 560V, I_D = 3.2A,$ $V_{GS} = 10V$	
Gate-Drain Charge	Q _{gd}	_	3.0	_		V _{GS} = 10V	
Turn-On Delay Time	t _{D(ON)}	_	8	_			
Turn-On Rise Time	t _R		9	_	ns	$\begin{split} V_{DD} &= 350 V, \ V_{GS} = 10 V, \\ R_G &= 4.7 \Omega, \ I_D = 3.2 A \end{split}$	
Turn-Off Delay Time	t _{D(OFF)}	_	22	_	115		
Turn-Off Fall Time	t _F		5	_			
Body Diode Reverse Recovery Time	t _{RR}	_	178	_	ns	- I _S = 3.2A, dl/dt = 100A/μs	
Body Diode Reverse Recovery Time (T _J = +150°C)	t _{RR}	_	223	_	ns		
Body Diode Reverse Recovery Charge	Q _{RR}	_	1.3	_	μC		
Body Diode Reverse Recovery Charge (T _J = +150°C)	Q _{RR}	_	1.8	_	μC		

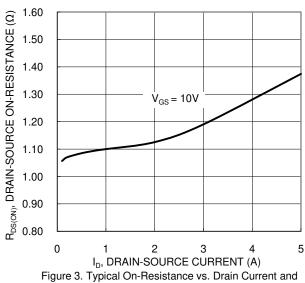
Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.

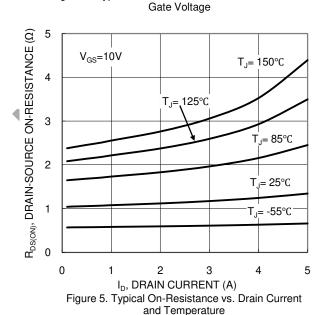
Bevice mounted on FR-4 substrate PC board, 20z. copper, with minimum recommended pad layout.
 Guaranteed by design. Not subject to production testing.
 Short duration pulse test used to minimize self-heating effect.

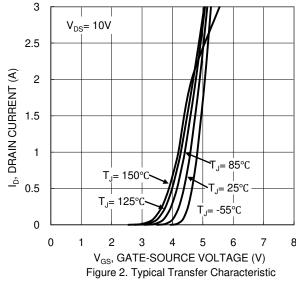


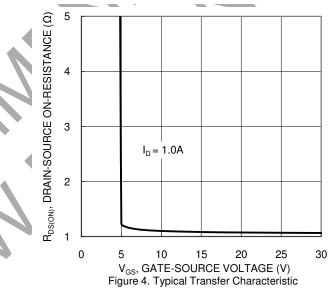
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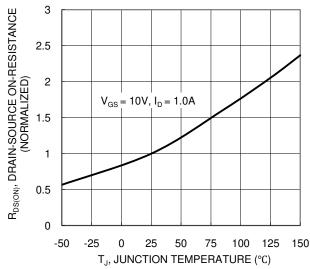


Figure 6. On-Resistance Variation with Temperature

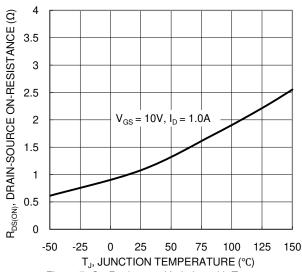
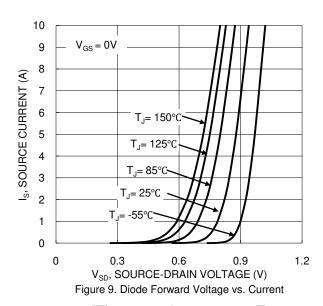


Figure 7. On-Resistance Variation with Temperature



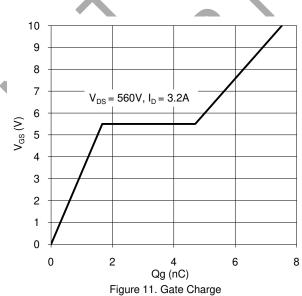
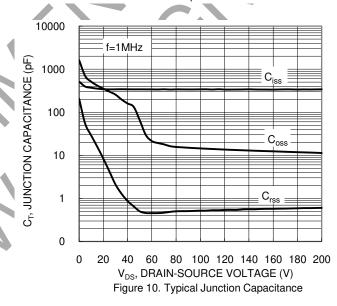
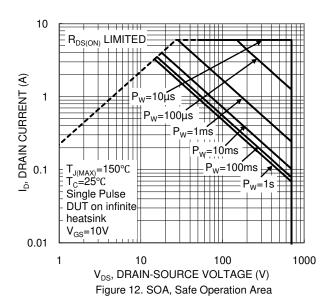
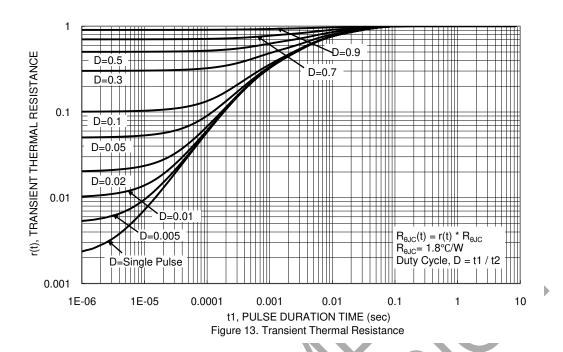


Figure 8. Gate Threshold Variation vs. Junction Temperature



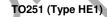


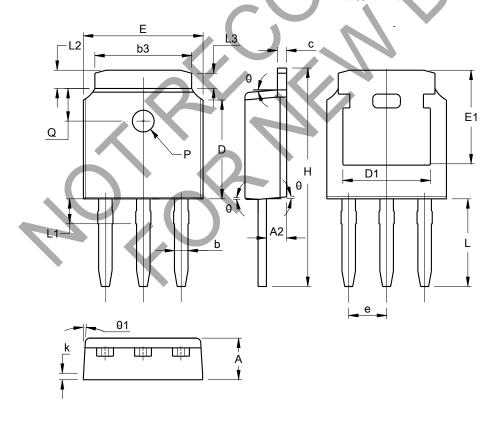




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.





TO251 (Type HE1)					
Dim	Min	Max	Тур		
Α	2.20	2.40	2.30		
A2	0.97	1.17	1.07		
b	0.68	0.90	0.78		
b3	5.20	5.50	5.33		
С	0.43	0.63	0.53		
D	5.98	6.22	6.10		
D1	5	.30 RE	F		
е	2.	286 BS	С		
Е	6.40	6.80	6.60		
E1	4.63	5.03	4.83		
Н	10.00	11.44	11.22		
k	C	.40REI	=		
L	3.90	4.30	4.10		
L1	0.85	1.25	1.05		
L2	0.88	1.28	1.02		
L3	0	.75 RE	F		
Q	1.65	1.95	1.80		
PØ	1.20				
θ	5°	9°	7°		
θ1	5°	9°	7°		
All Dimensions in mm					



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