# Old Company Name in Catalogs and Other Documents

On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1<sup>st</sup>, 2010

**Renesas Electronics Corporation** 

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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# 2SK3378

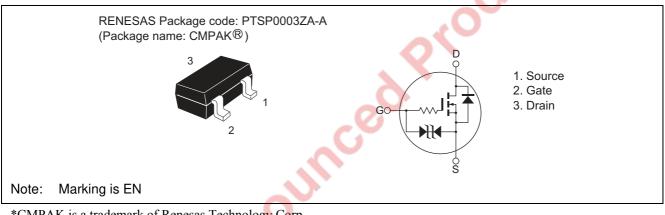
Silicon N Channel MOS FET **High Speed Switching** 

> REJ03G1599-0200 (Previous: ADE-208-805) Rev.2.00 Oct 23, 2007

### **Features**

- Low on-resistance  $R_{DS} = 2.7 \Omega$  typ. ( $V_{GS} = 10 V$ ,  $I_D = 50 mA$ )  $R_{DS} = 4.7 \Omega$  typ. ( $V_{GS} = 4 V$ ,  $I_D = 20 mA$ )
- 4 V gate drive device.
- Small package (CMPAK)

### Outline



\*CMPAK is a trademark of Renesas Technology Corp.

## **Absolute Maximum Rating**

· · · ·	$(Ta = 25^{\circ}C)$		
Item 💊	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	30	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	ID	100	mA
Drain peak current	Note1	400	mA
Body-drain diode reverse drain current	I <sub>DR</sub>	100	mA
Channel dissipation	Pch Note 2	300	mW
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note: 1. PW  $\leq$  10  $\propto$ s, duty cycle  $\leq$  1%

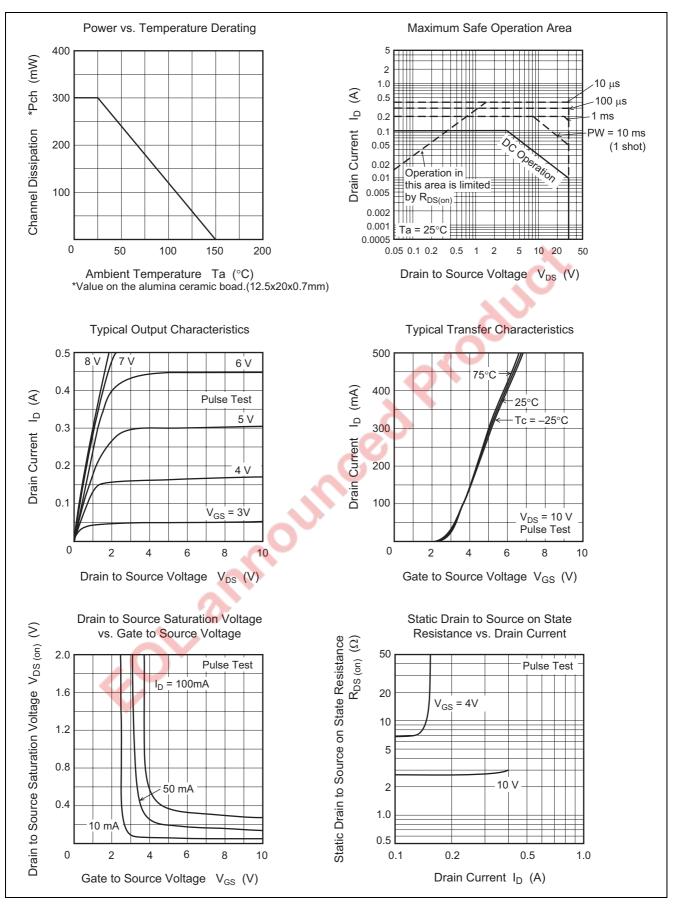
2. Value on the alumina ceramic board (12.5 x 20 x 0.7 mm)

# **Electrical Characteristics**

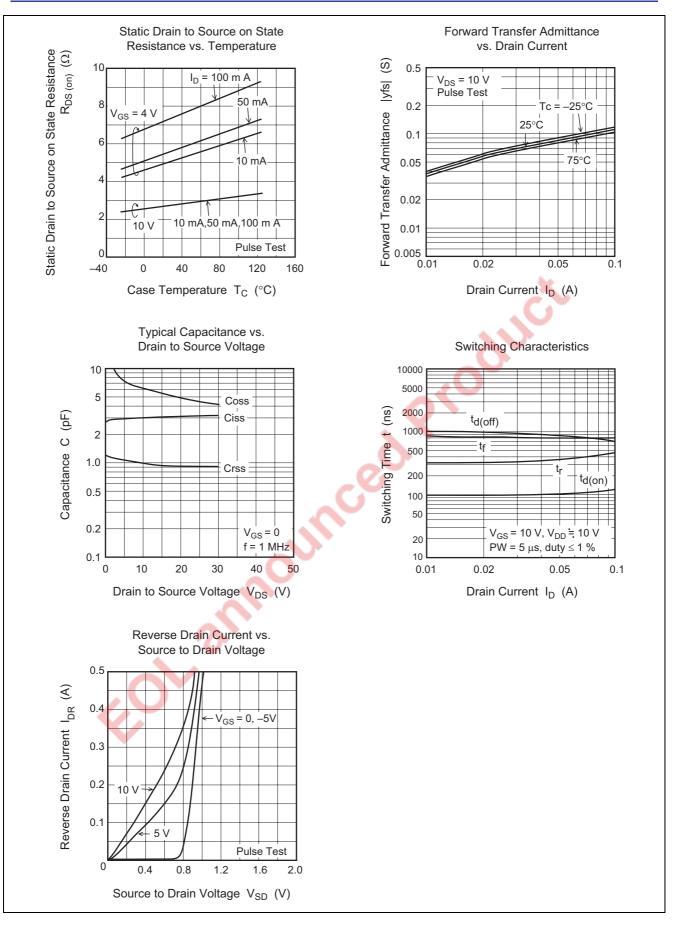
Item	Change In a l	N#:	Trees	N/	11	Teet Oswallting
	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	30		—	V	$I_D = 100 \propto A, V_{GS} = 0$
Gate to source breakdown voltage	V <sub>(BR)GSS</sub>	±20			V	$I_{G} = \pm 100 \propto A, V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_		±5	≪A	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	IDSS			1	≪A	$V_{DS} = 30 V, V_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS(off)</sub>	1.3		2.3	V	$I_D = 10 \propto A, V_{DS} = 5 V$
Static drain to source on state	R <sub>DS(on)</sub>	_	2.7	3.5	Ω	$I_D = 50 \text{ mA}, V_{GS} = 10 \text{ V}^{\text{Note 3}}$
resistance	R <sub>DS(on)</sub>		4.7	7.0	Ω	$I_D = 20 \text{ mA}, V_{GS} = 4 \text{ V}^{\text{Note 3}}$
Forward transfer admittance	y <sub>fs</sub>	55	85	—	mS	$I_D = 50 \text{ mA}, V_{DS} = 10 \text{ V}^{\text{Note 3}}$
Input capacitance	Ciss		3		pF	$V_{DS} = 10 V$
Output capacitance	Coss	—	8	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	—	1	—	pF	f = 1 MHz
Turn-on delay time	t <sub>d(on)</sub>	—	100	—	ns	$I_{\rm D} = 50 \text{ mA}, V_{\rm GS} = 10 \text{ V}$
Rise time	tr		330	—	ns	$R_L = 200 \Omega$
Turn-off delay time	t <sub>d(off)</sub>	—	1150	—	ns	
Fall time	t <sub>f</sub>		940	—	ns	
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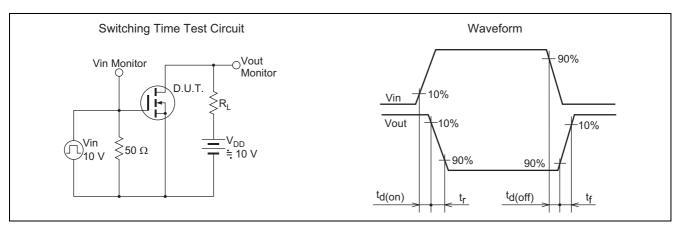
### **Main Characteristics**



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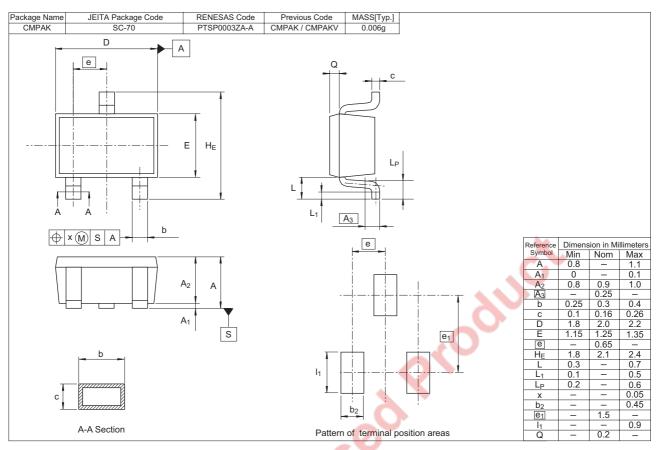


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### **Package Dimensions**



### **Ordering Information**

Part No.	Quantity	~		Shipping Container	
2SK3378ENTL-E	3000 pcs		Taping		
2SK3378ENTR-E	3000 pcs		Taping		
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