



#### **QUADRUPLE 2-INPUT AND GATES**

#### **Description**

The 74LV08A provides provides four independent 2-input AND gates with standard push-pull outputs. The device is designed for operation with a power supply range of 2.0V to 5.5V.

The inputs are tolerant to 5.5V allowing this device to be used in a mixed voltage environment. The device is fully specified for partial power down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output preventing damaging current backflow when the device is powered down.

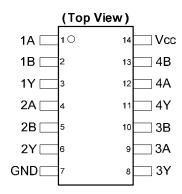
The gates perform the Boolean function:

$$Y = A \bullet B$$
 or  $Y = \overline{\overline{A} + \overline{B}}$ 

#### **Features**

- Wide Supply Voltage Range from 2.0V to 5.5V
- Sinks or Sources 12mA at V<sub>CC</sub> = 4.5V
- CMOS Low Power Consumption
- IOFF Supports Partial-Power Down Operation
- Inputs or Outputs accept up to 5.5V
- Inputs Can Be Driven by 3.3V or 5V Allowing for Voltage Translation Applications
- Schmitt Trigger Action at All Inputs
- ESD Protection Tested per JESD 22
  - Exceeds 200-V Machine Model (A115)
  - Exceeds 2000-V Human Body Model (A114)
  - Exceeds 1000-V Charged Device Model (C101)
- Latch-Up Exceeds 100mA per JESD 78, Class I
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

#### Pin Assignments



SO-14 / TSSOP-14

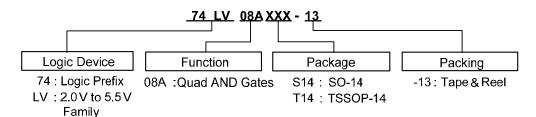
#### **Applications**

- General Purpose Logic
- Power Down Signal Isolation
- Wide Array of Products Such As:
  - PCs, networking, Notebooks, Ultrabooks, Netbooks
  - Computer Peripherals, Hard Drives, CD/DVD ROM
  - TV, DVD, DVR, set top box

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

### **Ordering Information**



Device	Package Code	Packaging	13" Tape	and Reel
Device	Fackage Code	(Note 4)	Quantity	Part Number Suffix
74LV08AS14-13	S14	SO-14	2500/Tape & Reel	-13
74LV08AT14-13	T14	TSSOP-14	2500/Tape & Reel	-13

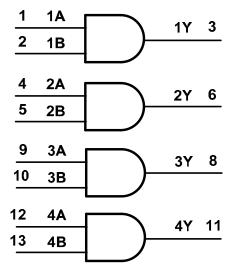
Note: 4. The taping orientation and tape details can be found at http://www.diodes.com/datasheets/ap02007.pdf



### **Pin Descriptions**

Pin Number	Pin Name	Description
1	1A	Data Input
2	1B	Data Input
3	1Y	Data Output
4	2A	Data Input
5	2B	Data Input
6	2Y	Data Output
7	GND	Ground
8	3Y	Data Output
9	3A	Data Input
10	3B	Data Input
11	4Y	Data Output
12	4A	Data Input
13	4B	Data Input
14	Vcc	Supply Voltage

# **Logic Diagram**



### **Function Table**

Inputs		Output
Α	В	Υ
L	X	L
X	L	L
Н	Н	Н

### Absolute Maximum Ratings (Note 5) (@TA = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
ESD MM	Machine Model ESD Protection	200	V
V <sub>CC</sub>	Supply Voltage Range	-0.5 to +7.0	V
VI	Input Voltage Range	-0.5 to +7.0	V
I <sub>IK</sub>	Input Clamp Current V <sub>I</sub> < 0V	-20	mA
I <sub>OK</sub>	Output Clamp Current Vo<-0V	-50	mA
Io	Continuous Output Current -0.5V < V <sub>O</sub> V <sub>CC</sub> +0.5V	±25	mA
Icc	Continuous Current Through V <sub>CC</sub>	50	mA
I <sub>GND</sub>	Continuous Current Through GND	-50	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T <sub>STG</sub>	Storage Temperature	-65 to +150	°C
P <sub>TOT</sub>	Total Power Dissipation	500	mW

Note: 5. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.



# Recommended Operating Conditions (Note 6) (@TA = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	Supply Voltage	_	2.0	5.5	V
VI	Input Voltage	_	0	5.5	V
Vo	Output Voltage	_	0	V <sub>CC</sub>	V
		2.0V	_	-50	mA
	High-Level Output Current	2.3V to 2.7V	_	-2	μA
Іон	High-Level Output Current	3.0V to 3.6V	_	-6	mA
		4.5V to 5.5V	_	-12	mA
		2.0V	_	50	μA
	Low-Level Output Current	2.3V to 2.7V	_	2	mA
loL	Low-Level Output Current	3.0V to 3.6V	_	6	mA
		4.5V to 5.5V	_	12	mA
		2.3V to 2.7V	_	200	
Δt/ΔV	Input Transition Rise or Fall Rate	3.0V to 3.6V	_	100	ns/V
		4.5V to 5.5V	_	20	
TA	Operating Free-Air Temperature	_	-40	+125	°C

Note:

### **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Cumbal	Parameter	Test Conditions	V	T <sub>A</sub> = -40	to +85°C	T <sub>A</sub> = -40 1	to +125°C	Unit
Symbol	Parameter	lest Conditions	V <sub>CC</sub>	Min	Max	Min	Max	Unit
		_	2.0V	1.5	_	1.5	_	
.,	High-Level Input	_	2.3V to 2.7V	V <sub>CC</sub> X 0.7	_	V <sub>CC</sub> X 0.7	_	V
ViH	Voltage	_	3.0V to 3.6V	V <sub>CC</sub> X 0.7	_	V <sub>CC</sub> X 0.7	_	V
		_	4.5V to 5.5V	V <sub>CC</sub> X 0.7	_	V <sub>CC</sub> X 0.7	_	
		_	2.0V	_	0.5	_	0.5	
\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	Low-Level Input	_	2.3V to 2.7V	_	V <sub>CC</sub> X 0.3	_	V <sub>CC</sub> X 0.3	V
VIL	Voltage	_	3.0V to 3.6V	_	V <sub>CC</sub> X 0.3	_	V <sub>CC</sub> X 0.3	V
		_	4.5V to 5.5V	_	V <sub>CC</sub> X 0.3	_	V <sub>CC</sub> X 0.3	X 0.3
		I <sub>OH</sub> = -50μA	2.0V to 5.5V	V <sub>CC</sub> -0.1	_	V <sub>CC</sub> -0.1	_	- V
.,	High-Level	I <sub>OH</sub> = -2mA	2.3V	2.0	_	2.0	_	
V <sub>OH</sub>	Output Voltage	I <sub>OH</sub> = -6mA	3.0V	2.48	_	2.48	_	V
		I <sub>OH</sub> = -12mA	4.5V	3.8	_	3.8	_	
		I <sub>OL</sub> = 50μA	2.0V to 5.5V	_	0.1	_	0.1	
V	Low-Level	I <sub>OL</sub> = 2mA	2.3V	_	0.4	_	0.4	V
V <sub>OL</sub>	Output Voltage	I <sub>OL</sub> = 6mA	3.0V	_	0.44	_	0.44	V
		I <sub>OL</sub> = 12mA	4.5V	_	0.55	_	0.55	
l <sub>OFF</sub>	Power Down Leakage Current	$V_{I}$ or $V_{O} = 0$ to 5.5V	0V	_	5	_	5	μΑ
l <sub>l</sub>	Input Current	V <sub>I</sub> =GND or 5.5V	0 to 5.5V		±1	_	±1	μA
Icc	Supply Current	$V_I$ = GND or $V_{CC}$ $I_O$ =0	5.5V	_	20	_	20	μΑ

<sup>6.</sup> Unused inputs should be held at  $V_{\text{\tiny CC}}$  or Ground.



# **Switching Characteristics**

Symbol	Symbol Parameter Test Conditions	Test	V	-	Γ <sub>A</sub> = +25°(	<b>.</b>	-40°C to	+85°C	-40°C to	+125°C	Unit
Syllibol		Conditions	V <sub>CC</sub>	Min	Тур	Max	Min	Max	Min	Max	Ullit
		Figure 1	2.5V ± 0.2V	_	7.9	13.8	1	16	1	17	
			3.3V ± 0.3V	_	5.6	8.8	1	10.5	1	11.5	ns
	Propagation	$C_L = 15pF$	5.0V ± 0.5V	_	4.1	5.9	1	7	1	8	
t <sub>PD</sub>	Delay A <sub>N</sub> to Y <sub>N</sub>	Einma 4	2.5V ± 0.2V	_	10.5	17.3	1	20	1	21	
		Figure 1 C <sub>L</sub> = 50 pF	3.3V ± 0.3V	_	7.5	12.5	1	14	1	15	ns
			5.0V ± 0.5V	_	5.5	7.9	1	9	1	10	

# Operating Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

	Parameter	Test Conditions	V <sub>CC</sub>	Тур	Unit
0	Power Dissipation	F= 10 MHz	3.3V	8	5
C <sub>pd</sub>	Capacitance per Gate	C <sub>L</sub> =50pF	5.0V	10	pF

### **Noise Characteristics**

 $V_{CC} = 3V$ ,  $C_L = 50pF T_A = +25^{\circ}C$ 

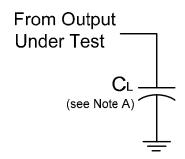
Symbol	Parameter	Min	Тур	Max	Unit
$V_{OL(p)}$	Quiet Output, Maximum Dynamic V <sub>OL</sub>	_	0.2	0.8	V
V <sub>OL(V)</sub>	Quiet Output, Minimum Dynamic V <sub>OL</sub>	_	-0.1	-0.8	V
V <sub>OH(V)</sub>	Quiet Output, Minimum Dynamic V <sub>OH</sub>	_	3.1	_	V
V <sub>IH(D)</sub>	High Level Dynamic Input Voltage	2.31	_	_	V
V <sub>IL(D)</sub>	Low Level Dynamic Input Voltage	-	-	0.99	V

# **Package Characterisitics**

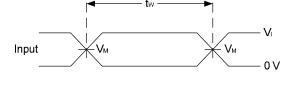
Symbol	Parameter	Test Conditions	V <sub>cc</sub>	Min	Тур	Max	Unit
C <sub>i</sub>	Input Capacitance	$V_i = V_{CC} - \text{or GND}$	2.0V to 5.5V	1	3.3	10	pF



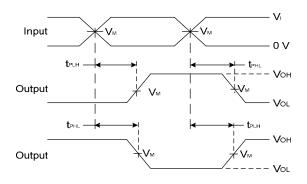
#### **Parameter Measurement Information**



V	Int	outs	.,		
Vcc	VI	t <sub>r</sub> /t <sub>f</sub>	VM	CL	
2.0V to 5.5V	Vcc	<3ns	V <sub>CC</sub> /2	15pF or 50pF	



Voltage Waveform **Pulse Duration** 



Voltage Waveform Propagation Delay Times **Inverting and Non Inverting Outputs** 

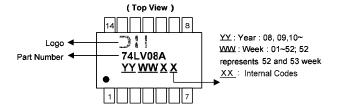
- Notes: A. Includes test lead and test apparatus capacitance.

  B. All pulses are supplied at pulse repetition rate ≤ 10MHz
  C. Inputs are measured separately one transition per measurement
  - D.  $t_{\text{PLH}}$  and  $t_{\text{PHL}}$  are the same as  $t_{\text{PD}}$

Figure 1 Load Circuit and Voltage Waveforms

### **Marking Information**

(1) SO14, TSSOP14



**Part Number** Package 5 of 8 www.diodes.com

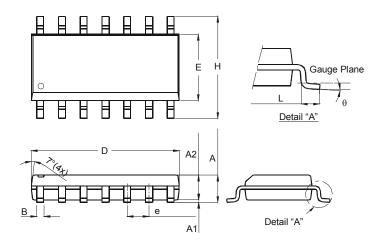


74LV08AS14	SO-14
74LV08AT14	TSSOP-14

### Package Outline Dimensions (All dimensions in mm.)

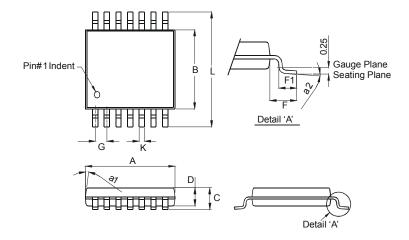
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

#### Package Type: SO-14



SO-14		
Dim	Min	Max
Α	1.47	1.73
A1	0.10	0.25
A2	1.45 Typ	
В	0.33	0.51
D	8.53	8.74
E	3.80	3.99
е	1.27 Typ	
Н	5.80	6.20
L	0.38	1.27
θ	0°	8°
All Dimensions in mm		

#### Package Type: TSSOP-14



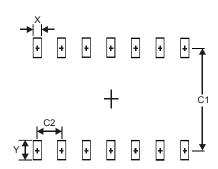
TSSOP-14			
Dim	Min	Max	
a1	7° (4X)		
a2	0°	8°	
Α	4.9	5.10	
В	4.30	4.50	
O		1.2	
D	8.0	1.05	
F	1.00 Typ		
F1	0.45	0.75	
G	0.65 Typ		
K	0.19	0.30	
Ĺ	6.40 Typ		
All Dimensions in mm			



### **Suggested Pad Layout**

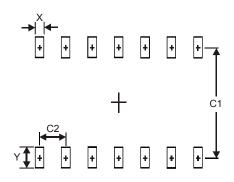
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

#### Package Type: SO-14



Dimensions	Value (in mm)
Х	0.60
Y	1.50
C1	5.4
C2	1.27

#### Package Type: TSSOP-14



Dimensions	Value (in mm)	
Х	0.45	
Υ	1.45	
C1	5.9	
C2	0.65	



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