

# RCX100N25

## Nch 250V 10A Power MOSFET

$V_{\mathrm{DSS}}$	250V
R <sub>DS(on)</sub> (Max.)	320m $Ω$
I <sub>D</sub>	10A
$P_D$	40W

#### Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Drive circuits can be simple.
- 4) Parallel use is easy.
- 5) Pb-free lead plating; RoHS compliant
- 6) 100% Avalanche tested

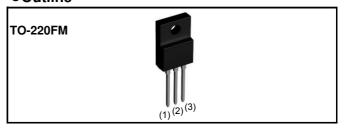
## Application

**Switching Power Supply** 

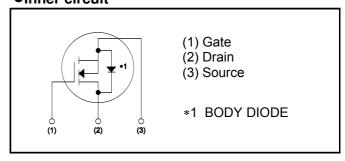
**Automotive Motor Drive** 

Automotive Solenoid Drive

#### Outline



## •Inner circuit



Packaging specifications

	9g -p	
	Packaging	Bulk
	Reel size (mm)	ı
Type	Tape width (mm)	-
Туре	Quantity (pcs)	500
	Taping code	-
	Marking	RCX100N25

## ● Absolute maximum ratings(T<sub>a</sub> = 25°C)

Paramete	Symbol	Value	Unit	
Drain - Source voltage	V <sub>DSS</sub>	250	V	
Continuous drain surrent	T <sub>c</sub> = 25°C	I <sub>D</sub> *1	±10	А
Continuous drain current	T <sub>c</sub> = 100°C	I <sub>D</sub> *1	±5.4	А
Pulsed drain current	I <sub>D,pulse</sub> *2	±40	А	
Gate - Source voltage	V <sub>GSS</sub>	±30	V	
Avalanche energy, single puls	E <sub>AS</sub> *3	7.29	mJ	
Avalanche current		I <sub>AR</sub> *3	5.0	А
Power dissipation $ T_c = 25^{\circ}C $ $ T_a = 25^{\circ}C $		P <sub>D</sub>	40	W
		P <sub>D</sub>	2.23	W
Junction temperature		Tj	150	°C
Range of storage temperature		T <sub>stg</sub>	-55 to +150	°C

## ●Thermal resistance

Parameter	Symbol	Values			Unit
	Symbol	Min.	Тур.	Max.	Offic
Thermal resistance, junction - case	$R_{thJC}$	-	-	3.125	°C/W
Thermal resistance, junction - ambient	$R_{thJA}$	-	-	56	°C/W
Soldering temperature, wavesoldering for 10s	T <sub>sold</sub>	-	-	265	°C

# ●Electrical characteristics(T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions	Values			Unit	
r arameter	Symbol	Conditions	Min. Typ. Ma		Max.		
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V$ , $I_D = 1mA$	250	ı	-	V	
Zero gate voltage drain current	I <sub>DSS</sub>	$V_{DS} = 250V, V_{GS} = 0V$ $T_j = 25^{\circ}C$	ı	ı	10	μА	
Gate - Source leakage current	$I_{\mathrm{GSS}}$	$V_{GS} = \pm 30V, V_{DS} = 0V$	ı	ı	±100	nA	
Gate threshold voltage	$V_{GS(th)}$	$V_{DS}$ = 10V, $I_D$ = 1mA	3.0	ı	5.0	V	
		$V_{GS} = 10V, I_D = 5.0A$	-	245	320		
Static drain - source on - state resistance	R <sub>DS(on)</sub> *4	$V_{GS} = 10V, I_{D} = 5.0A$ $T_{j} = 125^{\circ}C$	-	455	640	mΩ	
Forward transfer admittance	g <sub>fs</sub>	$V_{DS} = 10V, I_{D} = 5.0A$	2.7	5.4	-	S	

# ●Electrical characteristics(T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions	Values			Unit
- Farameter	Symbol Conditions -		Min.	Тур.	Max.	Offic
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	1440	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 25V	-	75	-	pF
Reverse transfer capacitance	$C_{rss}$	f = 1MHz	-	40	-	
Turn - on delay time	t <sub>d(on)</sub> *4	V <sub>DD</sub> ≃ 125V, V <sub>GS</sub> = 10V	-	29	-	
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = 5.0A	-	40	-	no
Turn - off delay time	t <sub>d(off)</sub> *4	$R_L = 12\Omega$	-	40	-	ns
Fall time	t <sub>f</sub> *4	$R_G = 10\Omega$	-	16	-	

# ●Gate Charge characteristics(T<sub>a</sub> = 25°C)

Parameter	Cumbal	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	$Q_g^{*4}$	V <sub>DD</sub> ≃ 125V	-	26.5	-	
Gate - Source charge	Q <sub>gs</sub> *4	I <sub>D</sub> = 10A	-	10.25	-	nC
Gate - Drain charge	Q <sub>gd</sub> *4	V <sub>GS</sub> = 10V	-	9.8	-	
Gate plateau voltage	V <sub>(plateau)</sub>	$V_{DD} \simeq 125V, I_D = 10A$	-	7.3	-	V

# ●Body diode electrical characteristics (Source-Drain)(T<sub>a</sub> = 25°C)

Darameter	Cumbal	Conditions	Values			Unit
Parameter	Parameter Symbol Conditions		Min.	Тур.	Max.	Offic
Continuous source current	l <sub>S</sub> *1	T <sub>c</sub> = 25°C	-	-	10	Α
Pulsed source current	I <sub>SM</sub> *2	1 <sub>c</sub> = 23 C	-	-	40	Α
Forward voltage	V <sub>SD</sub> *4	V <sub>GS</sub> = 0V, I <sub>S</sub> = 10A	-	-	1.5	V
Reverse recovery time	t <sub>rr</sub> *4	I <sub>S</sub> = 5.0A	-	100	-	ns
Reverse recovery charge	Q <sub>rr</sub> *4	di/dt = 100A/μs	-	365	-	nC

<sup>\*1</sup> Limited only by maximum temperature allowed.

<sup>\*2</sup> Pw  $\leq$  10 $\mu$ s, Duty cycle  $\leq$  1%

<sup>\*3</sup> L  $\simeq$  500 $\mu$ H, V<sub>DD</sub> = 50V, Rg = 10 $\Omega$ , starting T<sub>j</sub> = 25°C

<sup>\*4</sup> Pulsed

Fig.1 Power Dissipation Derating Curve

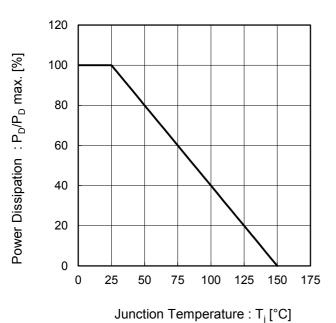
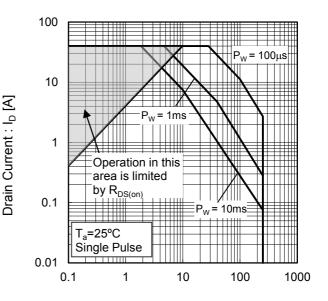
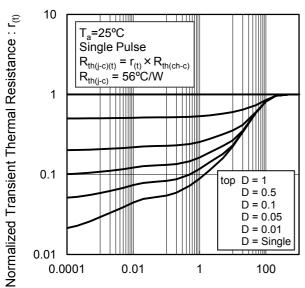


Fig.2 Maximum Safe Operating Area



Drain - Source Voltage :  $V_{DS}$  [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width



Pulse Width: Pw[s]

Fig.4 Avalanche Current vs Inductive Load

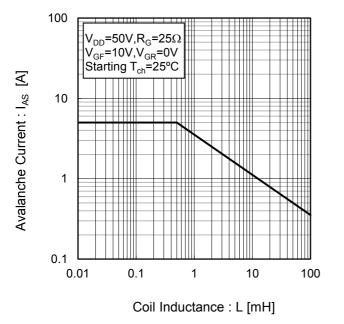
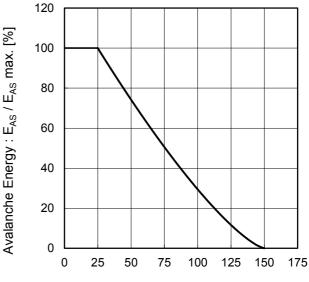
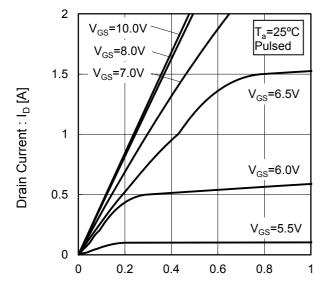


Fig.5 Avalanche Energy Derating Curve vs Junction Temperature



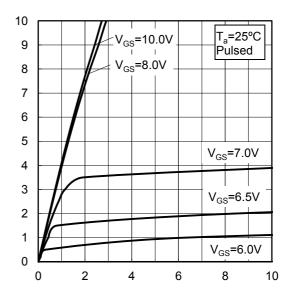
Junction Temperature : T<sub>i</sub> [°C]

Fig.6 Typical Output Characteristics(I)



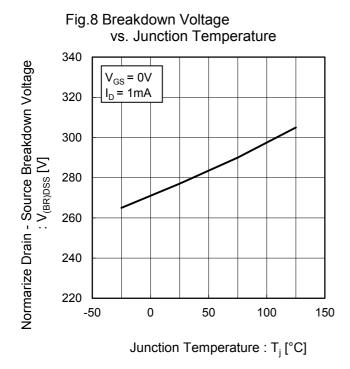
Drain - Source Voltage :  $V_{DS}$  [V]

Fig.7 Typical Output Characteristics(II)



Drain - Source Voltage : V<sub>DS</sub> [V]

Drain Current : I<sub>D</sub> [A]



100 V<sub>DS</sub>= 10V 10 Drain Current: I<sub>D</sub> [A] 1 T<sub>a</sub>= 125°C 0.1 T<sub>a</sub>= 75°C T<sub>a</sub>= 25°C  $T_a = -25^{\circ}C$ 0.01 0.001 2 6 8 0 4 10

Fig.9 Typical Transfer Characteristics

Fig.10 Gate Threshold Voltage vs. Junction Temperature 5.5  $V_{DS} = 10V$  $I_D = 1mA$ Gate Threshold Voltage: V<sub>GS(th)</sub> [V] 5.0 4.5 4.0 3.5 3.0 2.5 0 50 -50 100 150 Junction Temperature : T<sub>i</sub> [°C]

10 V<sub>DS</sub>= 10V Transconductance: g<sub>fs</sub> [S] 1 -25°C =25°C =75°C =125°C 0.1 0.01 0.01 0.1 1 10 100 Drain Current : I<sub>D</sub> [A]

Fig.11 Transconductance vs. Drain Current

Gate - Source Voltage : V<sub>GS</sub> [V]

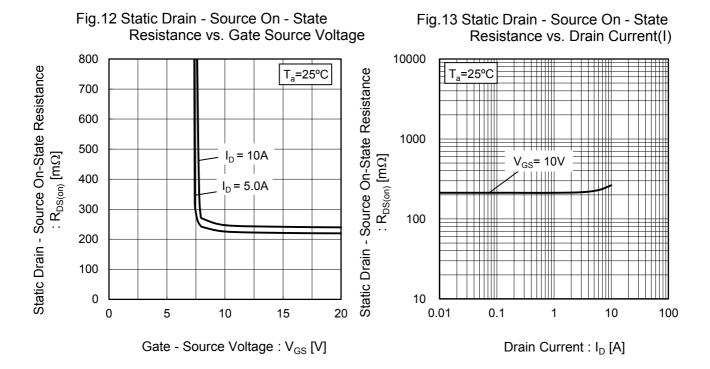
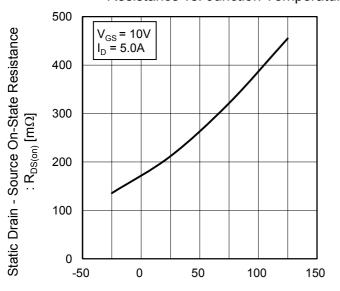


Fig.14 Static Drain - Source On - State Resistance vs. Junction Temperature



Junction Temperature : T<sub>i</sub> [°C]

10

0.01

## •Electrical characteristic curves

Resistance vs. Drain Current(II) 10000 Static Drain - Source On-State Resistance V<sub>GS</sub>= 10V T<sub>a</sub>=125°C <sup>~</sup>a=25°C 1000 -25°C  $:R_{\text{DS(on)}}\left[\text{m}\Omega\right]$ 100

0.1

1

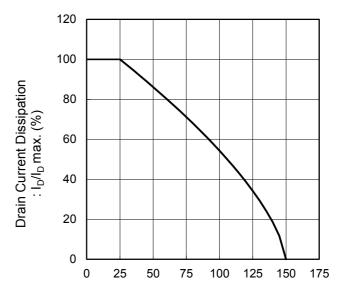
Drain Current : I<sub>D</sub> [A]

10

100

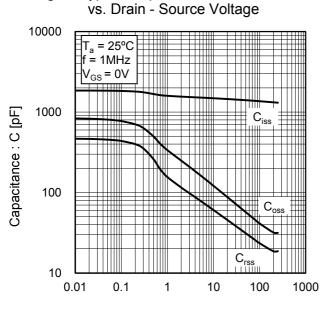
Fig.15 Static Drain - Source On - State

Fig.16 Drain Current Derating Curve



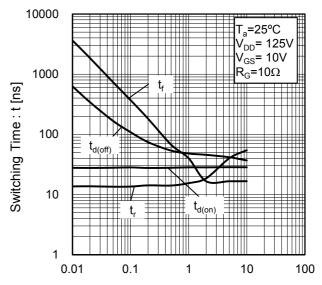
Junction Temperature :  $T_j$  [°C]

Fig.17 Typical Capacitance



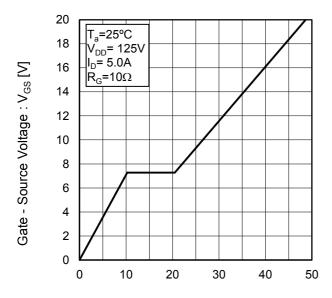
Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.18 Switching Characteristics



Drain Current : I<sub>D</sub> [A]

Fig.19 Dynamic Input Characteristics



Total Gate Charge : Q<sub>g</sub> [nC]

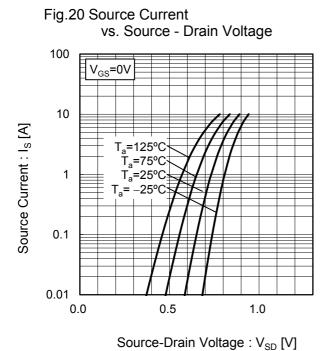


Fig21 Reverse Recovery Time vs. Source Current

1000

Ta=25°C

di / dt = 100A / 

VGs = 0V

10

0.1

1
10
100

Source Current : I<sub>S</sub> [A]

## Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

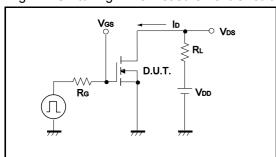


Fig.2-1 Gate Charge Measurement Circuit

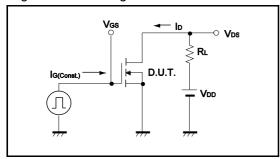


Fig.3-1 Avalanche Measurement Circuit

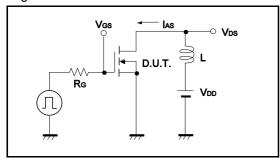


Fig.1-2 Switching Waveforms

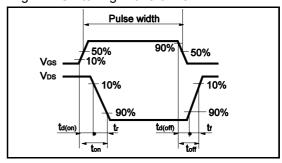


Fig.2-2 Gate Charge Waveform

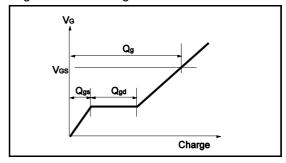
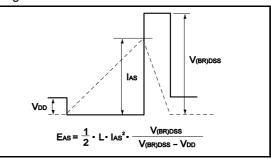
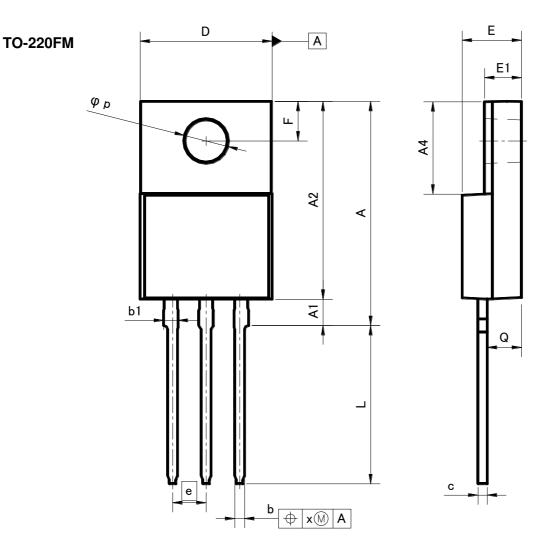


Fig.3-2 Avalanche Waveform



## ●Dimensions (Unit : mm)



DIM	MILIMI	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
Α	16.60	17.60	0.654	0.693	
A1	1.80	2.20	0.071	0.087	
A2	14.80	15.40	0.583	0.606	
A4	6.80	7.20	0.268	0.283	
b	0.70	0.85	0.028	0.033	
b1	1.10	1.50	0.043	0.059	
С	0.70	0.85	0.028	0.033	
D	9.90	10.30	0.39	0.406	
Е	4.40	4.80	0.173	0.189	
е	2.	54	0.	10	
E1	2.70	3.00	0.106	0.118	
F	2.80	3.20	0.11	0.126	
L	11.50	12.50	0.453	0.492	
р	3.00	3.40	0.118	0.134	
Q	2.10	3.10	0.083	0.122	
х	_	0.381	_	0.015	

Dimension in mm/inches

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CLASSIV	CLASSII	CLASSⅢ	CLASSⅢ

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  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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