



80V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Product Summary

| BV _{DSS} | R _{DS(ON)} | I _D T _C = +25°C |
|-------------------|-------------------------------|--|
| | 7.8mΩ @ V _{GS} = 10V | 91A |
| 80V | 11mΩ @ V _{GS} = 4.5V | 77A |

Features

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low R_{DS(ON)} Minimizes On-State Losses
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Description and Applications

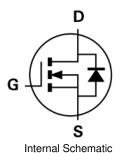
This new generation MOSFET is designed to minimize $R_{DS(ON)}$, yet maintain superior switching performance. This device is ideal for use in power management and load switch.

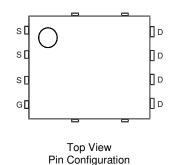
- DC-DC Converters
- Load Switch

Mechanical Data

- Case: PowerDI[®]5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.097 grams (Approximate)







Ordering Information (Note 4)

| Part Number | Case | Packaging |
|----------------|---------------|-------------------|
| DMTH8008LPS-13 | PowerDI5060-8 | 2,500/Tape & Reel |

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



);; = Manufacturer's Marking
TH8008LS = Product Type Marking Code
YYWW = Date Code Marking
YY = Year (ex: 19 = 2019)
WW = Week (01 to 53)



Maximum Ratings (@ $T_C = +25^{\circ}C$, unless otherwise specified.)

| Characteristic | | | Symbol | Value | Unit |
|---|-----------------|---|------------------|----------|------|
| Drain-Source Voltage | | | V_{DSS} | 80 | V |
| Gate-Source Voltage | | | V _{GSS} | ±20 | V |
| Continuous Drain Current, V _{GS} = 10V (Note 7) | Steady State | $T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$ | I _D | 91 64 | А |
| Maximum Continuous Body Diode Forward Current (Note 7) | | | Is | 69 | Α |
| Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%) | | | I _{DM} | 360 | Α |
| Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%) | | | I _{SM} | 360 | Α |
| Avalanche Current, L = 0.1mH (Note 8) | | | I _{AS} | 23 | Α |
| Avalanche Energy, L = 0.1mH (Note 8) | | | Eas | 26.5 | mJ |

Thermal Characteristics (@T_C = +25°C, unless otherwise specified.)

| Characteristic | | Symbol | Value | Unit |
|--|------------------------|------------------|-------------|------|
| Total Power Dissipation (Note 5) | T _A = +25°C | P_{D} | 1.5 | W |
| Thermal Resistance, Junction to Ambient (Note 5) | Steady State | $R_{\theta JA}$ | 99 | °C/W |
| Total Power Dissipation (Note 6) | T _A = +25°C | P _D | 3 | W |
| Thermal Resistance, Junction to Ambient (Note 6) | Steady State | $R_{\theta JA}$ | 45 | °C/W |
| Total Power Dissipation (Note 7) | T _C = +25°C | P _D | 100 | W |
| Thermal Resistance, Junction to Case (Note 7) | | $R_{	heta JC}$ | 1.5 | °C/W |
| Operating and Storage Temperature Range | | $T_{J_1}T_{STG}$ | -55 to +175 | °C |

Electrical Characteristics (@T_C = +25°C, unless otherwise specified.)

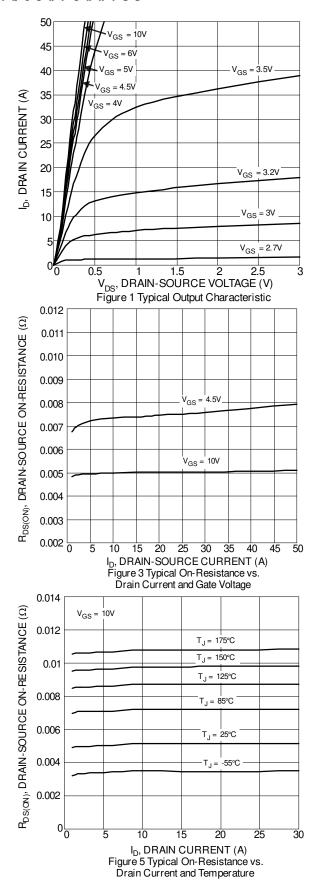
| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition | |
|--|---------------------|-----|------|------|-------|--|--|
| OFF CHARACTERISTICS (Note 9) | | | | • | | • | |
| Drain-Source Breakdown Voltage | BV _{DSS} | 80 | _ | _ | V | $V_{GS} = 0V$, $I_D = 1mA$ | |
| Zero Gate Voltage Drain Current | I _{DSS} | 1 | _ | 1 | μΑ | $V_{DS} = 64V, V_{GS} = 0V$ | |
| Gate-Source Leakage | I _{GSS} | _ | _ | ±100 | nA | $V_{GS} = \pm 20V, V_{DS} = 0V$ | |
| ON CHARACTERISTICS (Note 9) | | | | | | | |
| Gate Threshold Voltage | V _{GS(TH)} | 1.3 | _ | 2.8 | V | $V_{DS} = V_{GS}$, $I_D = 1mA$ | |
| Static Drain-Source On-Resistance | | _ | 5 | 7.8 | mΩ | $V_{GS} = 10V, I_D = 14A$ | |
| Static Diain-Source On-Nesistance | R _{DS(ON)} | _ | 8 | 11 | 11177 | $V_{GS} = 4.5V, I_D = 12A$ | |
| Diode Forward Voltage | V _{SD} | _ | 0.8 | 1.2 | V | V _{GS} = 0V, I _S = 14A | |
| DYNAMIC CHARACTERISTICS (Note 10) | | | | | | | |
| Input Capacitance | Ciss | | 2345 | _ | | V _{DS} = 40V, V _{GS} = 0V, f = 1MHz | |
| Output Capacitance | Coss | | 842 | _ | pF | | |
| Reverse Transfer Capacitance | C _{rss} | | 51.9 | _ | | | |
| Gate Resistance | R_{G} | | 1.7 | _ | Ω | $V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$ | |
| Total Gate Charge (V _{GS} = 4.5V) | Q_G | | 21.7 | _ | | | |
| Total Gate Charge (V _{GS} = 10V) | Q_{G} | | 41.2 | _ | nC | V _{DD} = 40V. I _D = 2A | |
| Gate-Source Charge | Q _{GS} | | 5.0 | _ | ПС | VDD = 40V, ID = 2A | |
| Gate-Drain Charge | Q_{GD} | | 10.6 | _ | | | |
| Turn-On Delay Time | t _{D(ON)} | | 5.8 | _ | | | |
| Turn-On Rise Time | t _R | _ | 5.4 | _ | ns | $V_{DD} = 40V, V_{GS} = 10V,$ | |
| Turn-Off Delay Time | t _{D(OFF)} | _ | 24.5 | _ | 115 | $I_D = 2A, R_G = 1.6\Omega$ | |
| Turn-Off Fall Time | t _F | | 43.2 | _ | | | |
| Body Diode Reverse Recovery Time | t _{RR} | _ | 61 | _ | ns | I= 2A di/dt 100A/us | |
| Body Diode Reverse Recovery Charge | Q_{RR} | _ | 181 | _ | nC | $I_F = 2A$, di/dt = 100A/ μ s | |

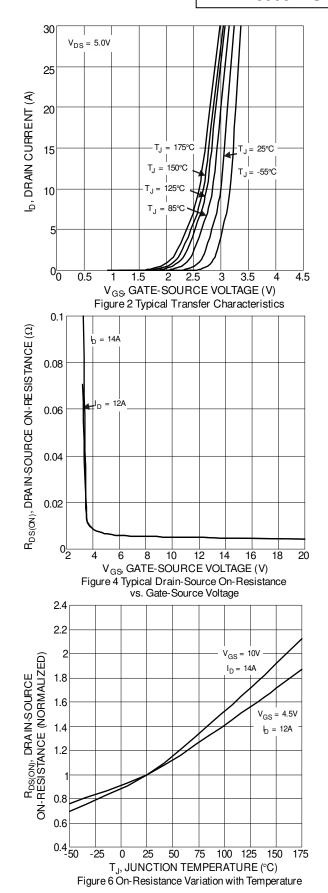
Notes:

- 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.7. Thermal resistance from junction to soldering point (on the exposed drain pad).
- 8. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep $T_{J} = +25$ °C.
- Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.



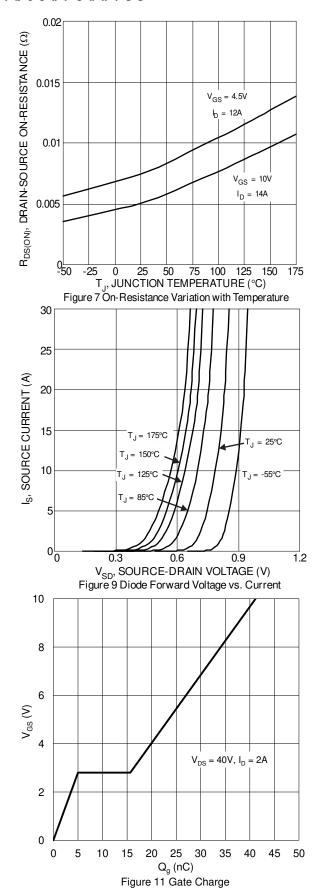












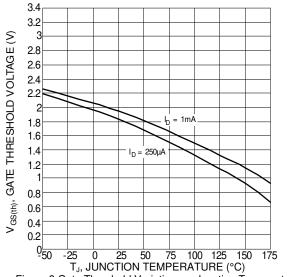
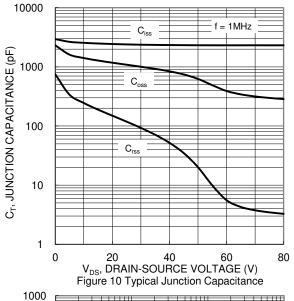
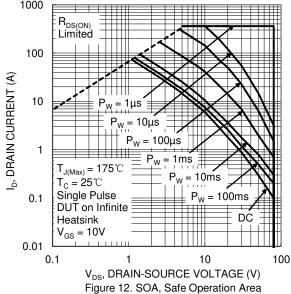
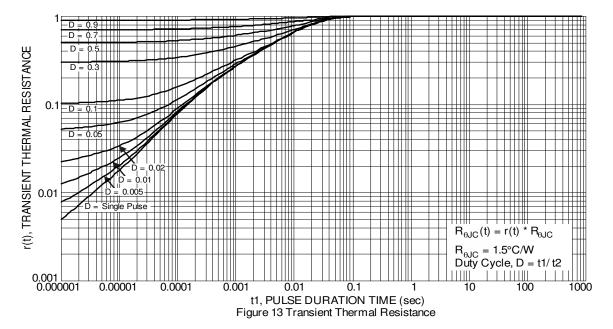


Figure 8 Gate Threshold Variation vs. Junction Temperature







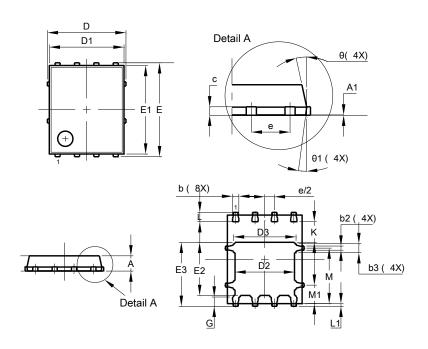




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8

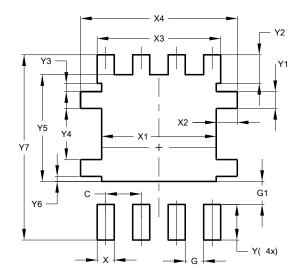


| PowerDI5060-8 | | | | |
|----------------------|-------|----------|-------|--|
| Dim | Min | Max | Тур | |
| Α | 0.90 | 1.10 | 1.00 | |
| A1 | 0.00 | 0.05 | - | |
| b | 0.33 | 0.51 | 0.41 | |
| b2 | 0.200 | 0.350 | 0.273 | |
| b3 | 0.40 | 0.80 | 0.60 | |
| C D | 0.230 | 0.330 | 0.277 | |
| | | 5.15 BSC | ; | |
| D1 | 4.70 | 5.10 | 4.90 | |
| D2 | 3.70 | 4.10 | 3.90 | |
| D3 | 3.90 | 4.30 | 4.10 | |
| Е | (| 3.15 BSC | ; | |
| E1 | 5.60 | 6.00 | 5.80 | |
| E2 | 3.28 | 3.68 | 3.48 | |
| E3 | 3.99 | 4.39 | 4.19 | |
| е | | 1.27 BSC | | |
| G | 0.51 | 0.71 | 0.61 | |
| K | 0.51 | - | - | |
| L | 0.51 | 0.71 | 0.61 | |
| L1 | 0.100 | 0.200 | 0.175 | |
| М | 3.235 | 4.035 | 3.635 | |
| M1 | 1.00 | 1.40 | 1.21 | |
| Θ | 10° | 12° | 11° | |
| Θ1 | 6° | 8° | 7° | |
| All Dimensions in mm | | | | |

Suggested Pad Layout

 $Please see \ http://www.diodes.com/package-outlines.html \ for \ the \ latest \ version.$

PowerDI5060-8



| Dimensions | Value (in mm) | | | |
|-----------------|---------------|--|--|--|
| С | 1.270 | | | |
| G | 0.660 | | | |
| G1 | 0.820 | | | |
| X | 0.610 | | | |
| X1 | 4.100 | | | |
| X2 | 0.755 | | | |
| Х3 | 4.420 | | | |
| X4 | 5.610 | | | |
| Υ | 1.270 | | | |
| Y1 | 0.600 | | | |
| Y2 | 1.020 | | | |
| Y3 | 0.295 | | | |
| Y4 | 1.825 | | | |
| Y5 | 3.810 | | | |
| Y6 | 0.180 | | | |
| Y7 6.610 | | | | |



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