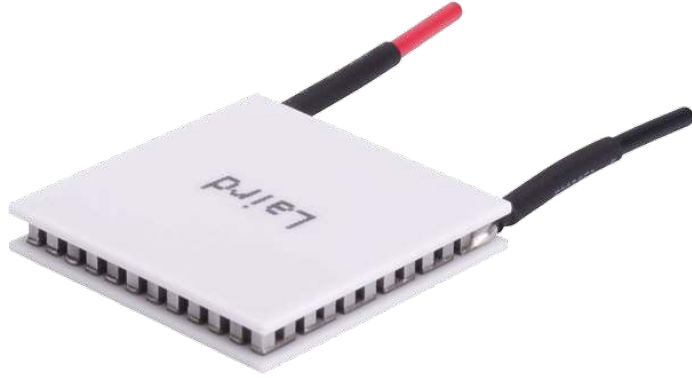


**Ceramic Plate Series Thermoelectric Cooler**

The CP10-71-05-L1-RT-W4.5 is a high-performance and highly reliable standard Thermoelectric Cooler. Assembled with Bismuth Telluride semiconductor material and thermally conductive Aluminum Oxide ceramics. It has a maximum  $Q_c$  of 18.5 Watts when  $\Delta T = 0$  and a maximum  $\Delta T$  of 70.5 °C at  $Q_c = 0$ .

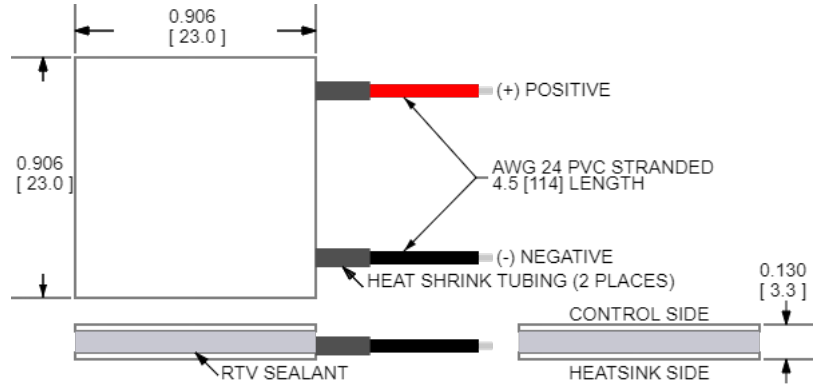


**Features**

- Compact geometric sizes
- DC Operation
- RoHS-compliant

**Applications**

- Thermoelectric Coolers for Reagent Storage
- Thermoelectric Coolers for Handheld Cosmetic Lasers
- Cooling for Centrifuges
- Heads-Up Displays, Imaging Sensors
- Peltier Cooling for Machine Vision



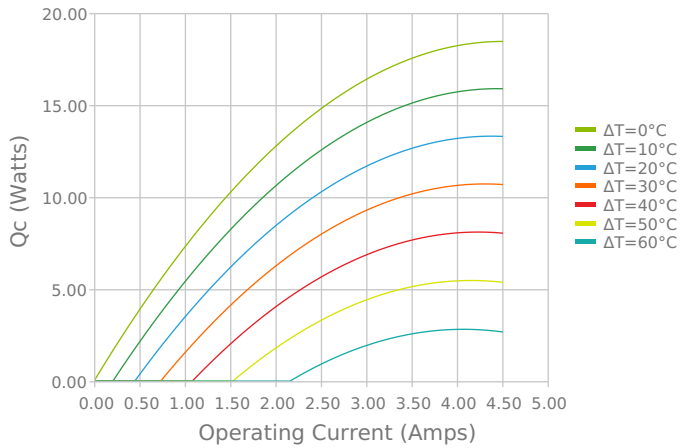
CERAMIC MATERIAL:  $Al_2O_3$   
 SOLDER CONSTRUCTION: 138°C, BiSn  
 Note: Allow 0.020 in [0.5 mm] around perimeter of the thermoelectric cooler and lead wire attachment to accommodate sealant

INCHES [ MM ]

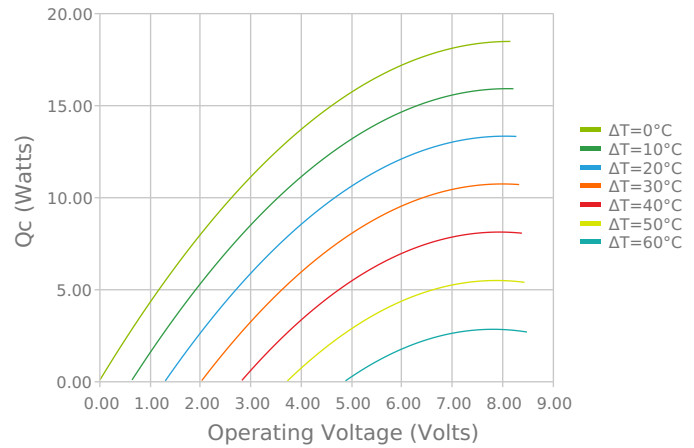
**ELECTRICAL AND THERMAL PERFORMANCE**

For maximum performance, be sure to orient the CONTROL side of the TEC against the application to be managed and the HEATSINK side against the heat sink or other heat rejection method. The CONTROL side is always opposite the side with lead attachments. Lead attachment is a passive heat loss and less impactful if located on the side that attaches to the heat exchanger.

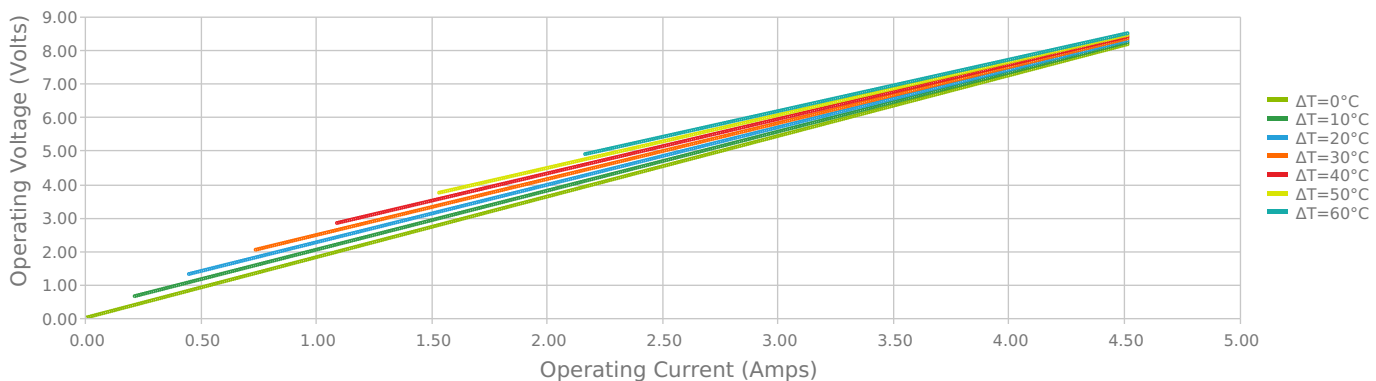
Heat Pumped at Cold Side  
 $T_{hot} = 27\text{ °C}$



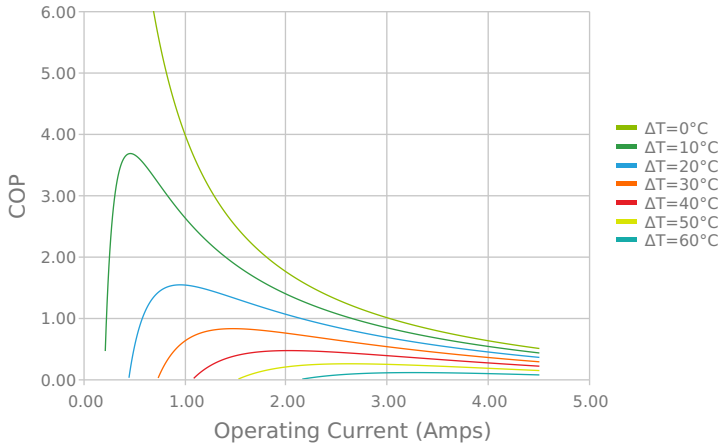
Heat Pumped at Cold Side  
 $T_{hot} = 27\text{ °C}$



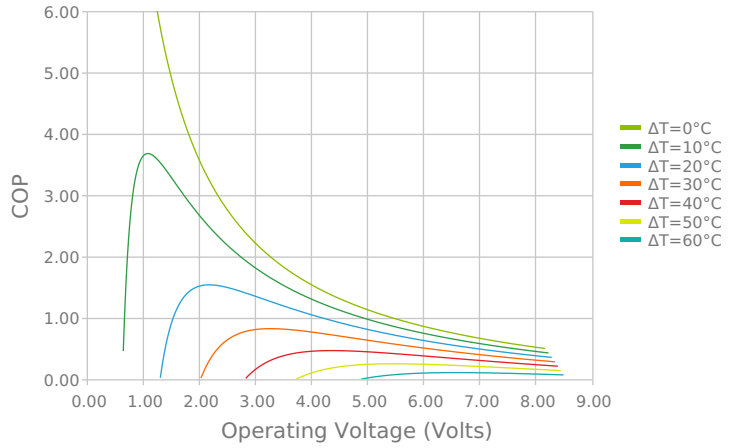
Current vs Voltage (I vs V)  
 $T_{hot} = 27\text{ °C}$



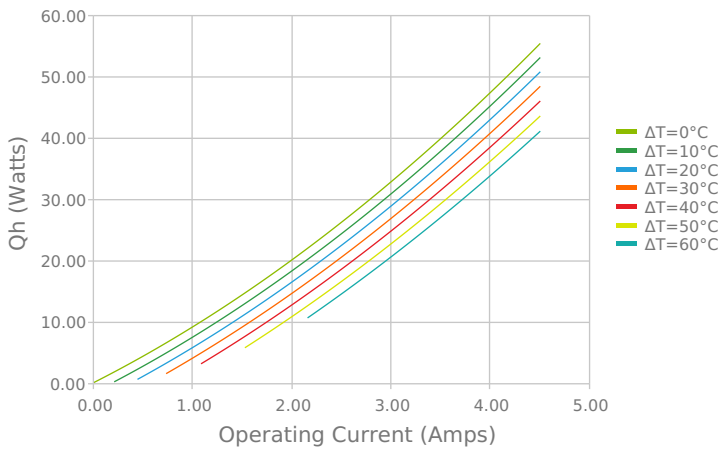
Coefficient of Performance (COP = Qc/Pin)  
 Thot = 27 °C



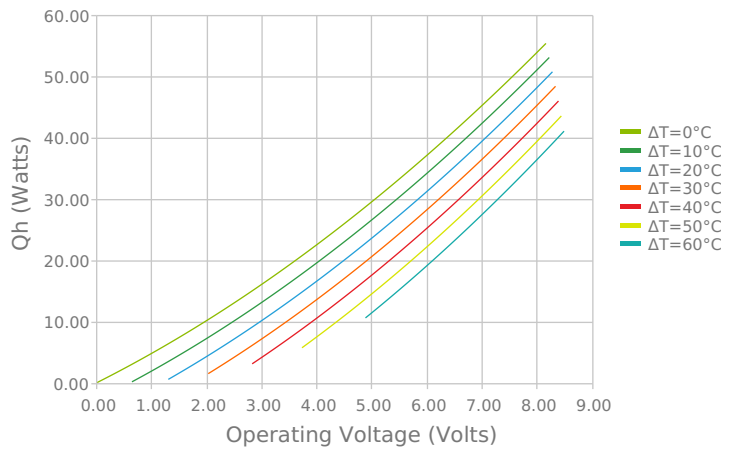
Coefficient of Performance (COP = Qc/Pin)  
 Thot = 27 °C



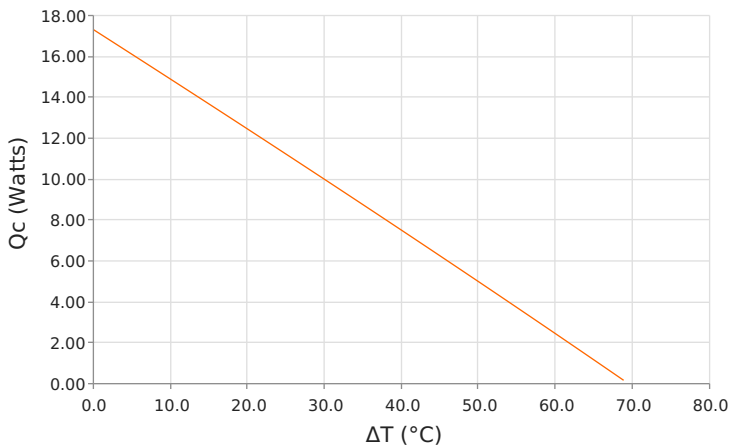
Total Heat Dissipated at Hot Side (Qh=Qc+Pin)  
 Thot = 27 °C



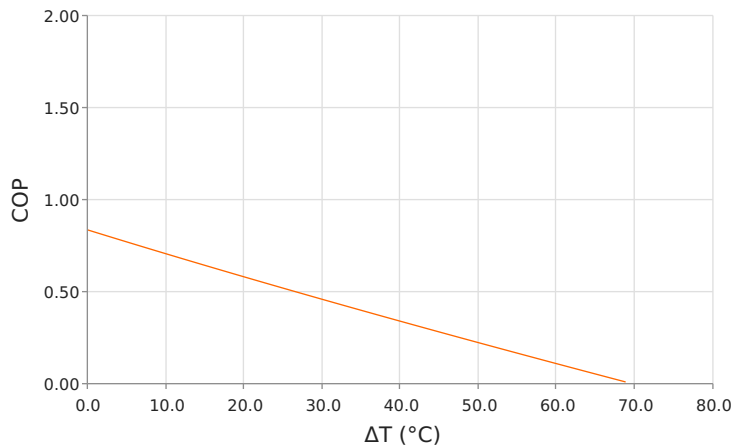
Total Heat Dissipated at Hot Side (Qh=Qc+Pin)  
 Thot = 27 °C



Heat Pumped at Cold Side (Qc)  
 Thot = 27 °C | Current = 3.4 Amps



Coefficient of Performance (COP = Qc/Pin)  
 Thot = 27 °C | Current = 3.4 Amps



## SPECIFICATIONS\*

	27.0 °C	35.0 °C	50.0 °C
<b>Hot Side Temperature</b>			
<b>Qcmax (<math>\Delta T = 0</math>)</b>	18.5 Watts	19.0 Watts	20.0 Watts
<b><math>\Delta T_{max}</math> (<math>Q_c = 0</math>)</b>	70.5°C	73.5°C	78.8°C
<b>I<sub>max</sub> (I @ <math>\Delta T_{max}</math>)</b>	4.0 Amps	4.0 Amps	3.9 Amps
<b>V<sub>max</sub> (V @ <math>\Delta T_{max}</math>)</b>	7.8 Volts	8.1 Volts	8.6 Volts
<b>Module Resistance</b>	1.81 Ohms	1.88 Ohms	2.03 Ohms
<b>Max Operating Temperature</b>	80 °C		
<b>Weight</b>	6.0 gram(s)		

\* Specifications reflect thermoelectric coefficients updated March 2020

## FINISHING OPTIONS

Suffix	Thickness	Flatness / Parallelism	Hot Face	Cold Face	Lead Length
L1	3.300 ±0.025 mm 0.130 ± 0.0010 in	0.025 mm / 0.025 mm 0.001 in / 0.001 in	Lapped	Lapped	114.3 mm 4.50 in

## SEALING OPTIONS

Suffix	Sealant	Color	Temp Range	Description
RT	RTV	Translucent or White	-60 to 204°C	Non-corrosive, silicone adhesive

## NOTES

1. Max operating temperature: 80°C
2. Do not exceed I<sub>max</sub> or V<sub>max</sub> when operating module
3. Reference assembly guidelines for recommended installation
4. Solder tinning also available on metallized ceramics

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