



2.5V/3.3V, High Bandwidth, Hot Insertion 8-Bit, 2-Port, Bus Switch

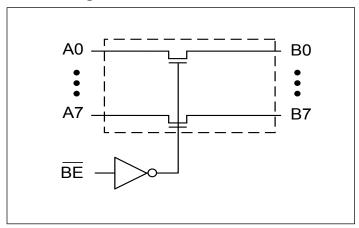
Features

- → Near-Zero propagation delay
- → 5-ohm switches connect inputs to outputs
- → High Bandwidth Operation (>400 MHz)
- → Permits Hot Insertion
- → 5V I/O Tolerant
- → Rail-to-Rail 3.3V or 2.5V Switching
- → 2.5V Supply Voltage Operation
- → Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- → Halogen and Antimony Free. "Green" Device (Note 3)
- → Packaging (Pb-free & Green):
 - 20-pin 173 mil wide plastic TSSOP (L)
 - 20-pin 150mil Wide plastic QSOP (Q)

Description

Diodes' PI3C3245 is a 2.5 volt or 3.3 volt 8-bit, 2-port bus switch designed with a low On-Resistance (5-ohm) allowing inputs to be connected directly to outputs. The bus switch creates no additional propagational delay or additional ground bounce noise. The switches are turned ON by the Bus Enable ($\overline{\rm BE}$) input signal. It is very useful in switching signals that have high bandwidth (>400 MHz).

Block Diagram



Pin Configuration

•		
№ Д	10	20 VCC
А0 🗆	2	19 D BE
A1 🛚	3	18 DB0
A2 [4	17 D B1
А3 🛚	5	16 B2
A4 🛚	6	15 B3
A5 🗆	7	14 D B4
A6 🗆	8	13 B5
A7 🛚	9	12 B6
GND [10	11 B7

Truth Table(1)

Function	BE	A0- 7
Disconnect	Н	Hi-Z
Connect	L	B0-7

Note: H = High Voltage Level

L = Low Voltage Level

Hi-Z = High Impedance

Pin Description

Pin Name	Description
BE	Bus Enable Input (Active LOW)
A0-7	Bus A
B0-7	Bus B
GND	Ground
V _{CC}	Power

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.





Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	65°C to +150°C
Ambient Temperature with Power Applied	-40°C to +85°C
Supply Voltage to Ground Potential (Inputs&V _{CC} Only)	0.5V to 4.6V
Supply Voltage to Ground Potential (Outputs&D/O Onl	y)-0.5V to 4.6V
DC Input Voltage	–0.5V to 5.5V
DC Output Current	120mA
Power Dissipation	0.5W

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, $V_{CC} = 3.3\text{V} \pm 10\%$)

Parameters	Description	Test Conditions(1)	Min.	Typ .(2)	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			V
$V_{\rm IL}$	Input LOW Voltage	Guaranteed Logic LOW Level	-0.5		0.8	V
I_{IH}	Input HIGH Current	$V_{CC} = Max., V_{IN} = V_{CC}$			±1	
I_{IL}	Input LOW Current	$V_{CC} = Max., V_{IN} = GND$			±1	μA
I _{OZH} (3)	High Impedance Output Current	$0 \le A, B \le V_{CC}$			±1	
V _{IK}	Clamp Diode Voltage	V_{CC} = Min., I_{IN} = -18 mA		-0.73	-1.2	V
_		$V_{CC} = Min., V_{IN} = 0.0V, I_{ON} = 48mA$		5	7	
R _{ON}	Switch On Resistance ⁽⁴⁾	$V_{CC} = Min, V_{IN} = 2.4V, I_{ON} = 15mA$		8	15	Ω

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at V_{CC} = 3.3V, T_A = 25°C ambient and maximum loading.
- 3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
- 4. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A,B) pins.

Capacitance ($T_A = 25^{\circ}C$, f = 1 MHz)

Parameters ⁽¹⁾	Description	Test Conditions	Тур.	Units
C_{IN}	Input Capacitance	$V_{\rm IN} = 0V$	3.5	pF
Coff	A/B Capacitance, Switch Off	$V_{\rm IN} = 0V$	5.0	pF
Con	A/B Capacitance, Switch On	$V_{\rm IN} = 0V$	10.0	pF

Notes:

1. This parameter is determined by device characterization but is not production tested.





Power Supply Characteristics

Parameters	Description	Test Conditions(1)		Min.	Typ.(2)	Max.	Units
I_{CC}	Quiescent Power Supply Current	$V_{CC} = Max.$	$V_{IN} = GND \text{ or } V_{CC}$		260	500	A
ΔI_{CC}	Supply Current per Input HIGH	V _{CC} = Max.	$V_{\rm IN} = 3.0 V^{(3)}$			750	μΑ

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- 2. Typical values are at Vcc = 3.3V, $+25^{\circ}C$ ambient.
- 3. Per TTL driven input (control input only); A and B pins do not contribute to $\Delta Icc.$

Switching Characteristics over 3.3V Operating Range

			PI3C	3245	
			Co	m.	
Parameters	Description	Test Conditions(1)	Min.	Max.	Units
t _{PLH}	Propagation Delay ^(1,2) Ax to Bx	$C_L = 50 \text{ pF}$ $R_L = 500\Omega$		0.25	
t _{PZH}	Bus Enable Time BE to Ax or Bx	$C_L = 50 \text{ pF}$	1.5	6.5	ns
t _{PHZ}	Bus Disable Time BE to Ax or Bx	$R_{L} = 500\Omega$ $R = 500\Omega$	1.5	5.5	

Notes:

Switching Characteristics over 2.5V Operating Range

			PI3C	3245	
			Co	m.	
Parameters	Description	Test Conditions(1)	Min.	Max.	Units
t _{PLH} t _{PHL}	Propagation Delay ^(1,2) Ax to Bx	$C_L = 50 \text{ pF}$ $R_L = 500\Omega$		0.25	
t _{PZH}	Bus Enable Time BE to Ax or Bx	$C_L = 50 \text{ pF}$	1.5	9.8	ns
t _{PHZ}	Bus Disable Time BE to Ax or Bx	$R_{L} = 500\Omega$ $R = 500\Omega$	1.5	8.3	

Notes

^{1.} This parameter is guaranteed but not tested on Propagation Delays.

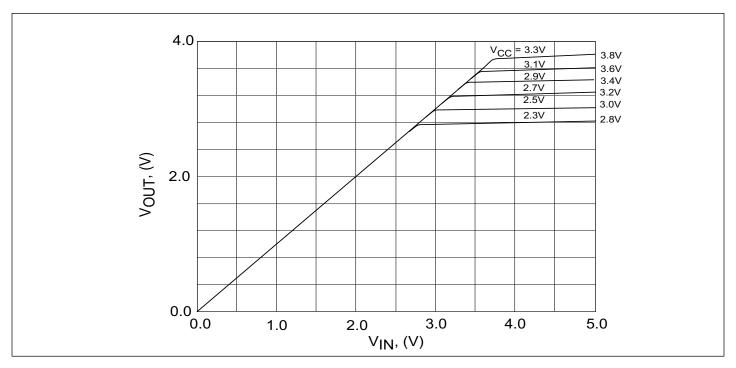
^{2.} The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25 ns for 50 pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

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Output Voltage vs. Input Voltage over Various Supply Voltages

Part Marking

L Package

PĪ3C 3245LE YYWWGG

YYWW: Date Code (Year & Workweek)

1st G: Assembly Site Code 2nd G: Wafer Fab Site Code

The Bar above 2nd "G" means Cu wire The Bar above "I" means Fab3 of MGN

Q Package



YY: Year

WW: Workweek

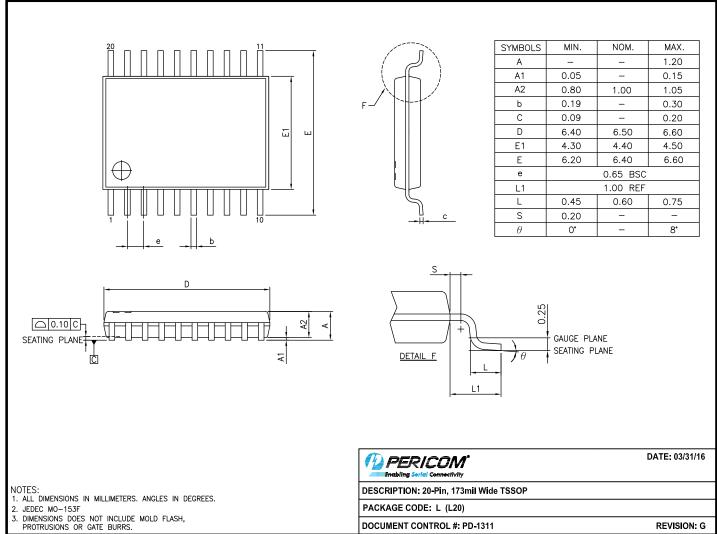
1st X: Assembly Site Code 2nd X: Fab Site Code

The Bar above "I" means Fab3 of MGN The Bar above fab code means Cu wire





Packaging Mechanical: 20-TSSOP (L)

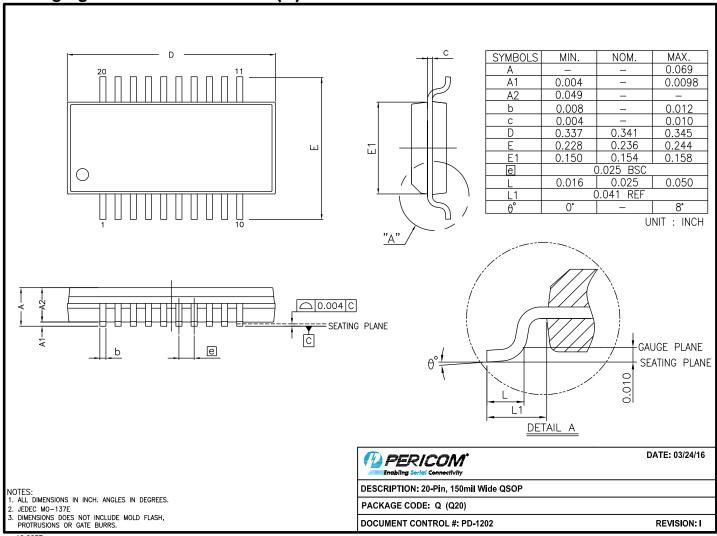


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Packaging Mechanical: 20-QSOP (Q)



For latest package info.

 $please\ check: http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/packaging-mech$

Ordering Information

Ordering Code	Package Code	Package Type
PI3C3245LEX	L	20-Pin, 173mil Wide (TSSOP)
PI3C3245QEX	Q	20-Pin, 150mil Wide (QSOP)

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. E = Pb-free and Green
- 5. X suffix = Tape/Reel





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