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### High-Efficiency, 26V Step-Up Converters for Two to Six White LEDs

#### General Description

The MAX1561/MAX1599 step-up converters drive up to six white LEDs with a constant current to provide backlight in cell phones, PDAs, and other hand-held devices. The step-up converter topology allows series connection of the white LEDs so the LED currents are identical for uniform brightness. This configuration eliminates the need for ballast resistors and expensive factory calibration.

The MAX1561/MAX1599 include an internal, high-voltage, low-RDS(ON) N-channel MOSFET switch for high efficiency and maximum battery life. A single Dual Mode™ input provides a simple means of brightness adjustment and on/off control. Fast 1MHz (500kHz for the MAX1599) current-mode, pulse-width modulated (PWM) operation allows for small input and output capacitors and a small inductor while minimizing ripple on the input supply/battery. Programmable soft-start eliminates inrush current during startup.

Both devices are available in a space-saving, 8-pin 3mm x 3mm thin DFN package with exposed paddle (EP).

#### Applications

Cell Phones and Smart Phones PDAs. Palmtops, and Wireless Handhelds e-Books and Subnotebooks White LED Display Backlighting

#### Features

- ♦ Accurate Current Regulation for Uniform Illumination
- High Efficiency Up to 87%
- ♦ Flexible Analog or PWM Dimming Control
- ♦ Up to 900mW Output Power with Internal 30V **MOSFET Switch**
- ♦ Fast 1MHz (MAX1561) or 500kHz (MAX1599) PWM Operation
- **♦** Small, Low-Profile External Components
- ♦ 2.6V to 5.5V Input Range
- ♦ 26V (max) Output with Overvoltage Protection
- ♦ Optimized for Low 15mV<sub>P-P</sub> Input Ripple
- ♦ Soft-Start with Zero Inrush Current
- ♦ 0.3µA Shutdown Current
- Tiny 8-Pin 3mm x 3mm Thin DFN Package

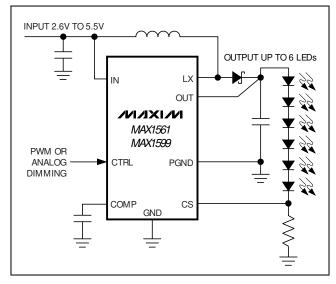
#### Ordering Information

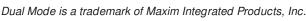
PART	TEMP RANGE	PIN-PACKAGE	TOP MARK
MAX1561ETA	-40°C to +85°C	8 TDFN-EP*	ACS
MAX1599ETA	-40°C to +85°C	8 TDFN-EP*	AHG

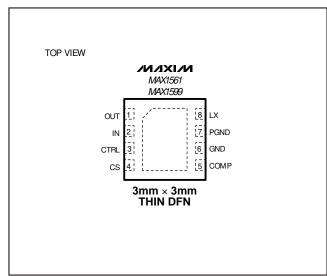
<sup>\*</sup>EP = Exposed paddle.

#### Typical Operating Circuit

### Pin Configuration







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#### **ABSOLUTE MAXIMUM RATINGS**

IN to GND0.3V to +6V PGND to GND0.3V to +0.3V LX, OUT to GND0.3V to +30V CTRL to GND0.3V to the lower of +6V or (V <sub>IN</sub> + 2V)	Continuous Power Dissipation (T <sub>A</sub> = +70°C) 3mm x 3mm 8-Pin TDFN (derate 24.4mW/°C above +70°C)1950mW Operating Temperature Range40°C to +85°C
COMP, CS to GND0.3 to (V <sub>IN</sub> + 0.3V)	Junction Temperature+150°C
ILX1A	Storage Temperature Range65°C to +150°C Lead Temperature (soldering, 10s)+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### **ELECTRICAL CHARACTERISTICS**

 $(V_{IN}=3V,\ V_{OUT}=20V,\ C_{OUT}=0.1\mu F,\ C_{COMP}=0.15\mu F,\ R_{SENSE}=7.5\Omega,\ V_{CTRL}=1.5V,\ Figure\ 1,\ T_A=0^{\circ}C$  to +85°C, unless otherwise noted. Typical values are at  $T_A=+25^{\circ}C$ .)

PARAMETER	CONDITIO	NS	MIN	TYP	MAX	UNITS
Supply Voltage			2.6		5.5	V
UVLO Threshold	V <sub>IN</sub> rising or falling		2.10	2.38	2.55	V
UVLO Hysteresis				40		mV
Quiescent Current	No switching			0.38	0.55	mA
Shutdown Supply Current	CTRL = GND, V <sub>OUT</sub> = V <sub>IN</sub>	$T_A = +25^{\circ}C$ $T_A = +85^{\circ}C$		0.3	2	μΑ
OVLO Threshold	V <sub>OUT</sub> rising	1A = 100 0	26	27	29	V
OVLO Hysteresis				2		V
OUT. DE O	V <sub>OUT</sub> = 26V, V <sub>CTRL</sub> > 0.24V		10	20	30	
OUT Input Bias Current	V <sub>OUT</sub> = V <sub>IN</sub> , CTRL = GND			0.01	1	μΑ
Output Voltage Range	(Note 1)		(V <sub>IN</sub> - V <sub>D</sub>	1)	25.5	٧
ERROR AMPLIFIER			•			
CTRL to CS Regulation	V <sub>CTRL</sub> = 1V, V <sub>IN</sub> = 2.6V to 5.5V		0.095	0.100	0.105	V
OO laant Bigg Ourset	V	$T_A = +25^{\circ}C$		0.01	1	
CS Input Bias Current	V <sub>CS</sub> = V <sub>CTRL</sub> /10	T <sub>A</sub> = +85°C		0.03		μΑ
CTRL Input Resistance	V <sub>CTRL</sub> ≤ 1.0V		290	500	780	kΩ
CTRL Dual-Mode Threshold			100	170	240	mV
CTRL Dual-Mode Hysteresis				5		mV
CTDL Chutdawa Faabla Dalay	(Note O)	MAX1561	6.5	8.2	10.5	
CTRL Shutdown Enable Delay	(Note 2)	MAX1599	13.6	16.4	21.0	ms
CS-to-COMP Transconductance	V <sub>COMP</sub> = 1.0V		32	50	82	μS
OSCILLATOR						
On another Francisco	MAX1561		0.80	1.0	1.25	NAL I-
Operating Frequency	MAX1599		0.4	0.5	0.6	MHz
Minimum Duty Cycle	PWM mode			12		0/
Minimum Duty Cycle	Pulse skipping			0		%
Maximum Duty Cycle	CTRL = IN, CS = GND		91	94		%
	1					

\_\_ /N/XI/M

#### **ELECTRICAL CHARACTERISTICS (continued)**

 $(V_{IN}=3V, V_{OUT}=20V, C_{OUT}=0.1\mu F, C_{COMP}=0.15\mu F, R_{SENSE}=7.5\Omega, V_{CTRL}=1.5V, Figure 1, T_A=0^{\circ}C$  to +85°C, unless otherwise noted. Typical values are at  $T_A=+25^{\circ}C$ .)

PARAMETER	CONDITIO	DNS	MIN	TYP	MAX	UNITS
N-CHANNEL SWITCH						
LX On-Resistance	I <sub>L</sub> X = 190mA			1.45	2.25	Ω
LVI sakaga Current	Very 20V CTDL CND	$T_A = +25^{\circ}C$		0.01	5	
LX Leakage Current	$V_{LX} = 28V$ , CTRL = GND	$T_A = +85^{\circ}C$		1		μΑ
LX Current Limit	Duty cycle = 90%		450	700	950	mA

#### **ELECTRICAL CHARACTERISTICS**

 $(V_{IN}=3V, V_{OUT}=20V, C_{OUT}=0.1\mu F, C_{COMP}=0.15\mu F, R_{SENSE}=7.5\Omega, V_{CTRL}=1.5V, Figure 1, T_A=-40^{\circ}C to +85^{\circ}C, unless otherwise noted.)$  (Note 3)

PARAMETER		CONDITIONS	MIN TYP	MAX	UNITS	
Supply Voltage			2.6	5.5	V	
UVLO Threshold	V <sub>IN</sub> rising or falling		2.10	2.55	V	
Quiescent Current	No switching			0.55	mA	
OVLO Threshold	V <sub>OUT</sub> rising		26	29	V	
OLIT learnet Biological Comments	V <sub>OUT</sub> = 26V, V <sub>CTR</sub>	L > 0.24V	10	30		
OUT Input Bias Current	V <sub>OUT</sub> = V <sub>IN</sub> , CTRL	= GND		1	μΑ	
Output Voltage Range	(Note 1)		(V <sub>IN</sub> - V <sub>D1</sub> )	25.5	V	
ERROR AMPLIFIER						
CTRL to CS Regulation	V <sub>CTRL</sub> = 1V, V <sub>IN</sub> =	2.6V to 5.5V	0.093	0.107	V	
CTRL Input Resistance	V <sub>CTRL</sub> ≤ 1.0V		240	780	kΩ	
CTRL Dual-Mode Threshold			100	240	mV	
CTDI Chutdawa Frabla Dalay	(1)	MAX1561	6	11		
CTRL Shutdown Enable Delay	(Note 2)	MAX1599	13	22	ms	
CS-to-COMP Transconductance	V <sub>COMP</sub> = 1.0V		32	85	μS	
OSCILLATOR						
On anation For any	MAX1561		0.75	1.25		
Operating Frequency	MAX1599		0.37	0.63	MHz	
Maximum Duty Cycle	CTRL = IN, CS = C	GND	91		%	
N-CHANNEL SWITCH			·			
LX On-Resistance				2.25	Ω	
LX Current Limit	Duty cycle = 90%		450	950	mA	

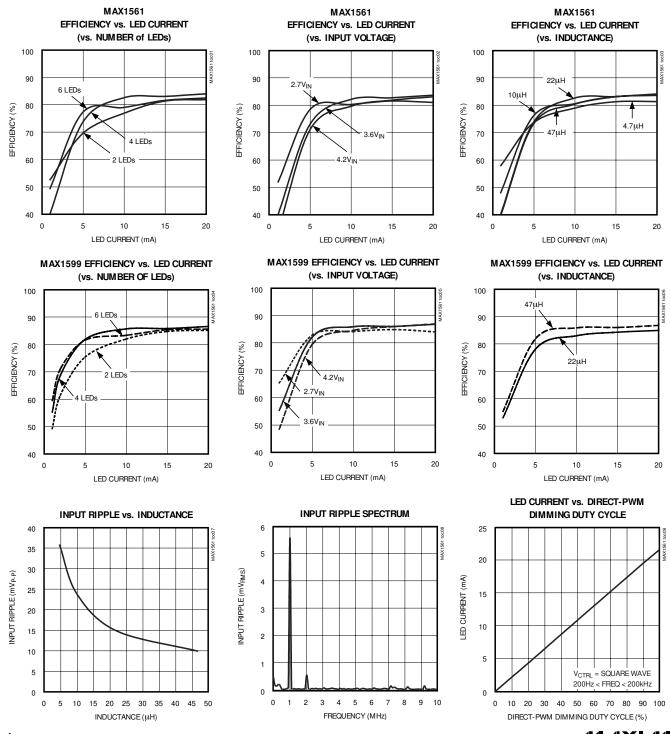
Note 1:  $V_{D1}$  is the diode forward-voltage drop of diode D1 in Figure 1.

Note 2: Time from CTRL going below the dual-mode threshold to IC shutdown.

**Note 3:** Specifications to -40°C are guaranteed by design and not production tested.

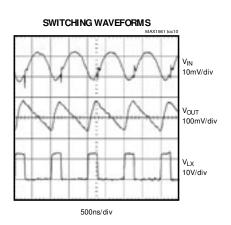
#### Typical Operating Characteristics

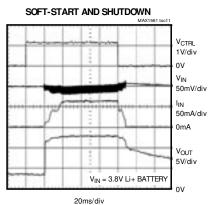
(Circuit of Figure 1,  $V_{IN}$  = 3.6V,  $I_{LED}$  = 15mA, L1 = 22 $\mu$ H,  $C_{IN}$  = 2.2 $\mu$ F,  $C_{OUT}$  = 0.1 $\mu$ F,  $C_{COMP}$  = 0.1 $\mu$ F,  $R_{SENSE}$  = 7.5 $\Omega$ , 4 LEDs,  $T_A$  = +25°C, unless otherwise noted.)

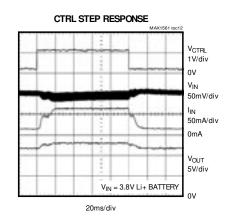


#### Typical Operating Characteristics (continued)

(Circuit of Figure 1,  $V_{IN}$  = 3.6V,  $I_{LED}$  = 15mA, L1 = 22 $\mu$ H,  $C_{IN}$  = 2.2 $\mu$ F,  $C_{OUT}$  = 0.1 $\mu$ F,  $C_{COMP}$  = 0.1 $\mu$ F,  $R_{SENSE}$  = 7.5 $\Omega$ , 4 LEDs,  $T_A$  = +25°C, unless otherwise noted.)







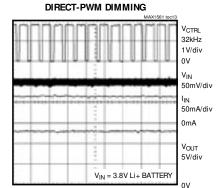
 $V_{\mathsf{IN}}$ 

3.5V

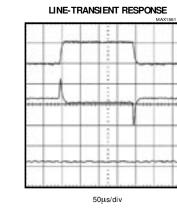
500mV/div

50mA/div

100mV/div



50μs/div



#### Pin Description

PIN	NAME	FUNCTION
1	OUT	Overvoltage Sense. When V <sub>OUT</sub> is greater than 27V, the internal N-channel MOSFET turns off until V <sub>OUT</sub> drops below 25V, then the IC reenters soft-start. Connect a 0.1µF capacitor from OUT to PGND.
2	IN	Input Voltage Supply. Input voltage range is 2.6V to 5.5V. Connect a 2.2µF capacitor from IN to PGND.
3	CTRL	Brightness Control Input. LED brightness is controlled by the voltage applied to CTRL. Varying the voltage from 0.24V to 1.62V adjusts the brightness from dim to bright, respectively. Any voltage above 1.62V does not increase brightness. If CTRL is held below 100mV for more than 8.2ms (16.4ms for the MAX1599), the IC shuts down.
4	CS	Current-Sense Feedback Input. Connect a resistor from CS to GND to set the LED bias current. The voltage at CS regulates to $V_{CTRL}$ / 10 or 0.162V, whichever is lower.
5	COMP	Compensation Input. Connect a $0.1\mu F$ capacitor (C <sub>COMP</sub> ) from COMP to GND. C <sub>COMP</sub> stabilizes the converter and controls soft-start. C <sub>COMP</sub> discharges to GND when in shutdown.
6	GND	Ground. Connect to PGND and the exposed pad directly under the IC.
7	PGND	Power Ground. Connect to GND and the exposed pad directly under the IC.
8	LX	Inductor Connection. This pin is high impedance during shutdown.
Exposed Pad	EP	Ground. Connect directly to GND and PGND under the IC.

#### Detailed Description

The MAX1561/MAX1599s' high efficiency and small size make them ideally suited to drive up to six series-connected LEDs. The device operates as a boost DC-DC converter that regulates output current rather than voltage. The MAX1561/MAX1599 provide even illumination by sourcing the same output current through each LED, eliminating the need for expensive factory calibration. The fast 1MHz (500kHz for the MAX1599) internal oscillator allows for a small inductor and small input and output capacitors while minimizing input and output ripple.

The single analog control input allows easy adjustment of LED brightness and on/off control. This allows either simple logic-level on/off control, analog voltage control, or PWM duty-cycle control of both brightness and shutdown. In shutdown, supply current is reduced to a low  $0.3\mu A$ . A soft-start gradually illuminates the LEDs, eliminating the inrush current during startup.

#### Soft-Start

The MAX1561/MAX1599 attain soft-start by charging CCOMP gradually with a current source. When VCOMP rises above 1.25V, the internal MOSFET begins switching at a reduced duty cycle. When VCOMP rises above 2.25V, the duty cycle is at its maximum. See the *Typical Operating Characteristics* for an example of soft-start operation.

#### Shutdown

The MAX1561/MAX1599 enter shutdown when VCTRL is less than 100mV for more than 8.2ms (16.4ms for the MAX1599). In shutdown, supply current is reduced to 0.3µA by powering down the entire IC except for the CTRL voltage-detection circuitry. CCOMP is discharged during shutdown, allowing the device to reinitiate softstart when it is enabled. Although the internal N-channel MOSFET does not switch in shutdown, there is still a DC current path between the input and the LEDs through the inductor and Schottky diode. The minimum forward voltage of the LED array must exceed the maximum input voltage to ensure that the LEDs remain off in shutdown. However, with two or more LEDs, the forward voltage is large enough to keep leakage current low, less than 1µA (typ). Typical shutdown timing characteristics are shown in the Typical Operating Characteristics.

#### **Overvoltage Protection**

Overvoltage lockout (OVLO) occurs when V<sub>OUT</sub> is above 27V. The protection circuitry stops the internal MOSFET from switching and causes V<sub>COMP</sub> to decay to 0V. The device comes out of OVLO and into soft-start when V<sub>OUT</sub> falls below 25V.

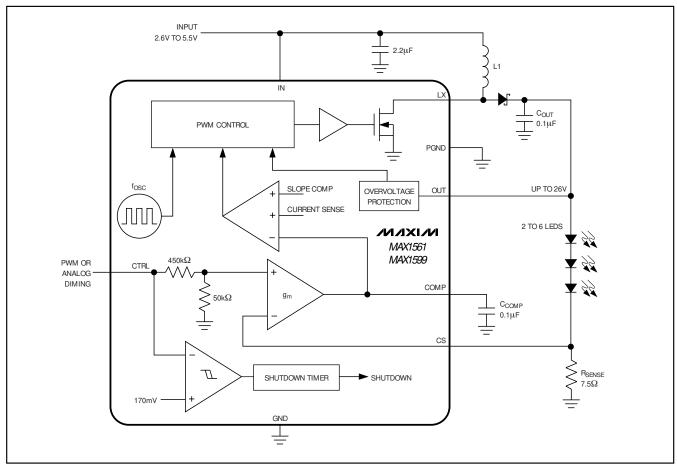


Figure 1. Functional Diagram

### Design Procedure

#### Adjusting LED Current

Adjusting the MAX1561/MAX1599s' output current changes the brightness of the LEDs. An analog input (CTRL) and the sense-resistor value set the output current. Output current is given by:

$$I_{LED} = \frac{V_{CTRL}}{10 \times R_{SENSE}}$$

The V<sub>CTRL</sub> voltage range for adjusting output current is 0.24V to 1.62V. To set the maximum current, calculate RSENSE when V<sub>CTRL</sub> is at its maximum as follows:

$$R_{SENSE} = \frac{1.62V}{10 \times I_{LED(MAX)}}$$

Power dissipation in RSENSE is typically less than 5mW, making a standard chip resistor sufficient.

#### **PWM Dimming Control**

CTRL is also used as a digital input allowing LED brightness control with a logic-level PWM signal applied directly to CTRL. The frequency range is from 200Hz to 200kHz, while 0% duty cycle corresponds to zero current and 100% duty cycle corresponds to full current. The error amplifier and compensation capacitor form a lowpass filter so PWM dimming results in DC current to the LEDs without the need for any additional RC filters; see the *Typical Operating Characteristics*.

#### Capacitor Selection

The exact values of input and output capacitors are not critical. The typical value for the input capacitor is  $2.2\mu F$  and the typical value for the output capacitor is  $0.1\mu F$ . Larger value capacitors can be used to reduce

input and output ripple, but at the expense of size and higher cost. CCOMP stabilizes the converter and controls soft-start. Connect a  $0.1\mu F$  capacitor from COMP to GND. For stable operation, COUT must not exceed 10 times CCOMP.

#### Inductor Selection

Inductor values range from 10 $\mu$ H to 47 $\mu$ H. A 22 $\mu$ H (47 $\mu$ H for the MAX1599) inductor optimizes the efficiency for most applications while maintaining low 15mVp-p input ripple. With input voltages near 5V, a larger value of inductance can be more efficient. To prevent core saturation, ensure that the inductor-saturation current rating exceeds the peak inductor current for the application. Calculate the peak inductor current with the following formula:

$$I_{PEAK} = \frac{V_{OUT(MAX)} \times \ I_{LED(MAX)}}{0.8 \times V_{IN(MIN)}} + \frac{V_{IN(MIN)} \times 0.8 \mu s}{2 \times L}$$

#### Schottky Diode Selection

The MAX1561/MAX1599s' high switching frequency demands a high-speed rectification diode (D1) for optimum efficiency. A Schottky diode is recommended due to its fast recovery time and low forward-voltage drop. Ensure that the diode's average and peak current rating exceed the average output current and peak inductor current. In addition, the diode's reverse breakdown voltage must exceed Vout. The RMS diode current can be calculated from:

$$I_{\text{DIODE(RMS)}} \cong \sqrt{I_{\text{OUT}} \times I_{\text{PEAK}}}$$

#### Applications Information

PC Board Layout

Due to fast switching waveforms and high-current paths, careful PC board layout is required. An evaluation kit (MAX1561EVKIT) is available to speed design. Order MAX1599 samples separately.

When laying out a board, minimize trace lengths between the IC and RSENSE, the inductor, the diode, the input capacitor, and the output capacitor. Keep traces short, direct, and wide. Keep noisy traces, such as the LX node trace, away from CS. The IN bypass capacitor ( $C_{IN}$ ) should be placed as close to the IC as possible. PGND and GND should be connected directly to the exposed paddle underneath the IC. The ground connections of  $C_{IN}$  and  $C_{OUT}$  should be as close together as possible. The traces from IN to the inductor and from the Schottky diode to the LEDs may be longer.

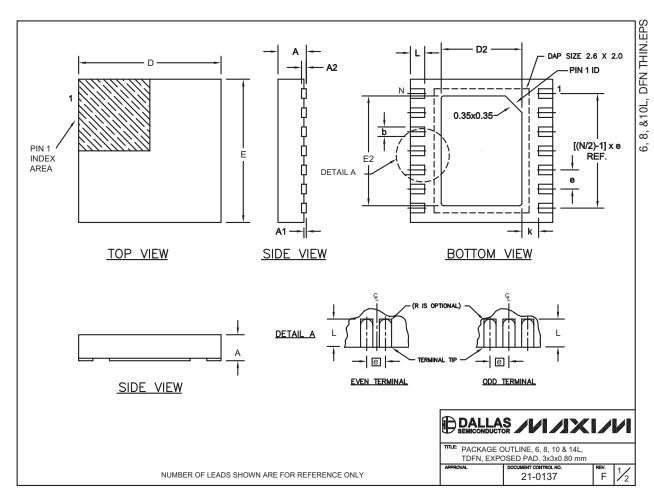
Chip Information

**TRANSISTOR COUNT: 2895** 

PROCESS: BICMOS

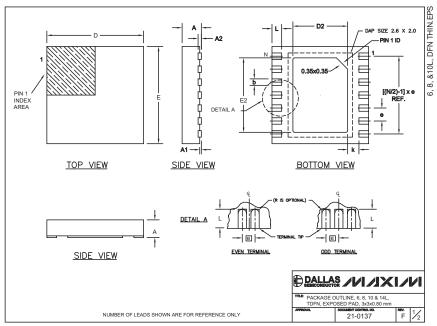
#### Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to <a href="https://www.maxim-ic.com/packages">www.maxim-ic.com/packages</a>.)



Package Information (continued)

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to <a href="https://www.maxim-ic.com/packages">www.maxim-ic.com/packages</a>.)



COMM	ON DIME	NSIONS						
SYMBOL	MIN.	MAX.						
A	0.70							
D	2.90							
E	2.90		1					
A1	0.00		1					
k		25 MIN.	1					
A2	0.	20 RFF.	†					
PKG. CODE	RIATION: N	D2	E2	е	JEDEC SPEC	b	[(N/2)-1] x e	
	1		F2	_	IEDEC SPEC	h	[(N/2)-1] v a	
	1		E2 2.30±0.10	e 0.95 BSC	JEDEC SPEC MO229 / WEEA	b 0.40±0.05	[(N/2)-1] x e	
PKG. CODE	N	D2		-		_		
PKG. CODE T633-1	N 6	D2 1.50±0.10	2.30±0.10	0.95 BSC	MO229 / WEEA	0.40±0.05	1.90 REF	
PKG. CODE T633-1 T833-1	N 6 8	D2 1.50±0.10 1.50±0.10	2.30±0.10 2.30±0.10	0.95 BSC 0.65 BSC	MO229 / WEEA MO229 / WEEC	0.40±0.05 0.30±0.05	1.90 REF 1.95 REF	
PKG. CODE T633-1 T833-1 T1033-1	N 6 8 10	D2 1.50±0.10 1.50±0.10 1.50±0.10	2.30±0.10 2.30±0.10 2.30±0.10	0.95 BSC 0.65 BSC 0.50 BSC	MO229 / WEEA MO229 / WEEC MO229 / WEED-3	0.40±0.05 0.30±0.05 0.25±0.05	1.90 REF 1.95 REF 2.00 REF	

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