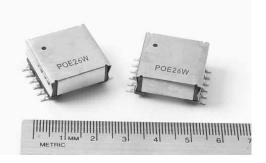
Effective July 2017 Supersedes September 2009

Power Over Ethernet (PoE)/PD Configurable forward transformer



Product features

- Versatile design allows multiple output variations
- Forward topology, 300 kHz switching frequency
- Input range from 29.5 V 60.0 V
- 1500 Vac isolation between primary and secondary
- Power 26 watts
- Low leakage inductance

Applications

- For IEEE 802.3af-compliant Power over Ethernet applications
- UPS, VoiP phone, Wireless LAN access point, Bluetooth access point, Network camera, Building access systems
- Retail Point-of-information systems
- Vending and gaming machines

Environmental data

- Storage temperature range (component): -40 °C to +125 °C
- Operating temperature range: -40 °C to +125 °C (ambient plus self-temperature rise)
- Solder reflow temperature: J-STD-020 (latest revision) compliant





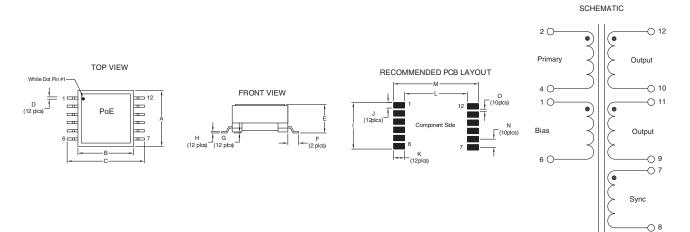
Product specifications

						DCR/	DCR/	DCR/	DCR/	Leakage	Pri	
		Primary				Pri	Sec	Bias	Sync	Induct.	Current	Turns ratio pins Pri (2 - 4):
		Induct.				(ohms)	(ohms)	(ohms)	(ohms)	(uh)	Pk	V1 (12 - 10): V2 (11 - 9):
Part Number	Watts	(uH)	Output	Bias	Sync	max	max	max	max	typ.	(Adc)	Bias (1 - 6): Sync (7 - 8)
PoE26W3.3VS5-R	26	160	(2)x3.3V@4.0A	10.0V@0.1A	5V@0.1A	0.100	0.025	0.90	0.42	1.0	2.6	1:0.29:0.29:0.83:0.42 +/-2%
PoE26W3.3VS10-R	26	160	(2)x3.3V@4.0A	10.0V@0.1A	10V@0.1A	0.100	0.025	0.90	0.90	1.0	2.6	1:0.29:0.29:0.83:0.83 +/-2%
PoE26W5V-R	26	160	(2)x5.0V@2.6A	10.0V@0.1A	5.0V@0.1A	0.100	0.050	0.90	0.42	1.0	2.6	1:0.42:0.42:0.83:0.42 +/-2%

(1) Test parameters: 100 kHz, 0.100 Vrms, 0.0 Adc (2) DCR limits maximum @ +20 $^\circ\text{C}$

(3) Leakage Inductance 300 kHz, 0.01 Vrms, 0.0 Adc

Dimensions- mm



DIMENSIONS

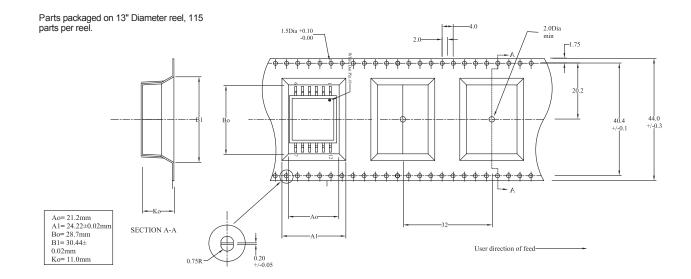
Γ	Α	В	С	D	E	F	G	н	I			L	М		
	mm	J	K	mm	mm	N	0								
	max.	ref.	max.	ref.	max.	ref.	ref.	ref.	ref.	mm	mm	ref.	max.	mm	mm
	21.5	22.0	28.5	0.7	10.8	2.95	0.1	0.4	17.25	2.25	3.15	23.2	29.5	3.0	0.75

1) Tolerances A - H are \pm 0.25 mm unless specified otherwise.

2) Tolerances I - O are \pm 0.10 mm unless specified otherwise 3) All soldering surfaces are coplaner to within \pm 0.102 mm.

4) Do not rout traces or vias underneath the transformer

Packaging information- mm



Solder Reflow Profile

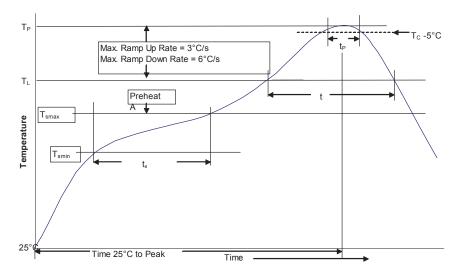


	Table 1 - Star	ndard SnF	Pb Solder (T _C)	
		Volume	Volume	
	Package	mm³	mm ³	
	Thickness	<350	≥350	
	<2.5mm	235°C	220°C	
	≥2.5mm	220°C	220°C	
•	Table 2 - Lea	d (Pb) Fre	e Solder (T _C)	
	Table 2 - Lea	d (Pb) Fre Volume	e Solder (T _C) Volume	Volume
	Table 2 - Lea Package		• •	Volume mm ³
		Volume	Volume	
-	Package	Volume mm ³	Volume mm ³	mm ³
•	Package Thickness	Volume mm ³ <350 260°C	Volume mm ³ 350 - 2000	mm ³ >2000

Reference JDEC J-STD-020

Profile Feature		Standard SnPb Solder	Lead (Pb) Free Solder	
Preheat and Soak	 Temperature min. (T_{smin}) 	100°C	150°C	
	 Temperature max. (T_{smax}) 	150°C	200°C	
	 Time (T_{smin} to T_{smax}) (t_s) 	60-120 Seconds	60-120 Seconds	
Average ramp up ra	te T _{smax} to T _p	3°C/ Second Max.	3°C/ Second Max.	
Liquidous temperatu	ire (TL)	183°C	217°C	
Time at liquidous (t)	60-150 Seconds	60-150 Seconds	
Peak package body	temperature (T _P)*	Table 1	Table 2	
Time (tp)** within 5	°C of the specified classification temperature (T_{C})	20 Seconds**	30 Seconds**	
Average ramp-down	rate (T _p to T _{smax})	6°C/ Second Max.	6°C/ Second Max.	
Time 25°C to Peak	Temperature	6 Minutes Max.	8 Minutes Max.	

 * Tolerance for peak profile temperature (T_p) is defined as a supplier minimum and a user maximum.

** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.

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