LIXYS Preliminary Technical Information **IXTP 8N50PM PolarHV[™]** V_{DSS} V 500 = Α 4 **Power MOSFET** D25 $\mathbf{R}_{\mathsf{DS(on)}}$ **0.8** Ω \leq (Electrically Isolated Tab) N-Channel Enhancement Mode Avalanche Rated Symbol **Test Conditions** Maximum Ratings **OVERMOLDED TO-220** (IXTP...M) OUTLINE $V_{\rm DSS}$ $T_{1} = 25^{\circ} C \text{ to } 150^{\circ} C$ 500 V $T_{...}^{"}$ = 25° C to 150° C; R_{GS} = 1 M Ω 500 V V ± 30 V_{gs} Continuous V_{gsm} Transient ± 40 Isolated Tab $T_c = 25^\circ C$ 4 G DS T_{c}° = 25° C, pulse width limited by T_{IM} 14 $T_c = 25^\circ C$ 8 A $T_c^{\vee} = 25^{\circ}C$ É_{AR}

20

10

41

150

300

260

4

1.13/10 Nm/lb.in.

-55 ... +150

-55 ... +150

400

mJ

mJ

V/ns

W

°C

°C

°C

°C

°C

g

G = Gate D = Drain S = Source

Features

¹ Plastic overmolded tab for electrical isolation

- L International standard package
- ¹ Unclamped Inductive Switching (UIS) rated
- Low package inductance
- easy to drive and to protect

Symbol	aracteristic Values				
$(T_{J} = 25^{\circ}C)$, unless otherwise specified)	Min.	Тур.	Max.	
BV _{DSS}	$V_{_{\rm GS}}$ = 0 V, I _D = 250 µA	500			V
V _{GS(th)}	$V_{_{\rm DS}} = V_{_{\rm GS}}, I_{_{\rm D}} = 250 \mu A$	3.0		5.5	V
I _{GSS}	$V_{GS} = \pm 30 V_{DC}, V_{DS} = 0$			±100	nA
I _{DSS}	$V_{DS} = V_{DSS}$ $V_{GS} = 0 V$	T _J = 125° C		5 50	μΑ μΑ
R _{DS(on)}	$V_{_{GS}}$ = 10 V, $I_{_{D}}$ = 4 A Pulse test, t ≤300 µs, duty	cycle d \leq 2 %		0.8	Ω

Advantages

¹ Easy to mount

- ¹ Space savings
- ¹ High power density

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 $\mathbf{E}_{\underline{\mathsf{AS}}}$

 \mathbf{P}_{D}

T,

T__{JM}

T_{stg}

 \mathbf{T}_{L}

Mď

Weight

dv/dt

 T_{c} = 25° C

 $T_c = 25^\circ C$

 $I_{s} \leq I_{DM}$, di/dt $\leq 100 \text{ A/}\mu s$, $V_{DD} \leq V_{DSS}$

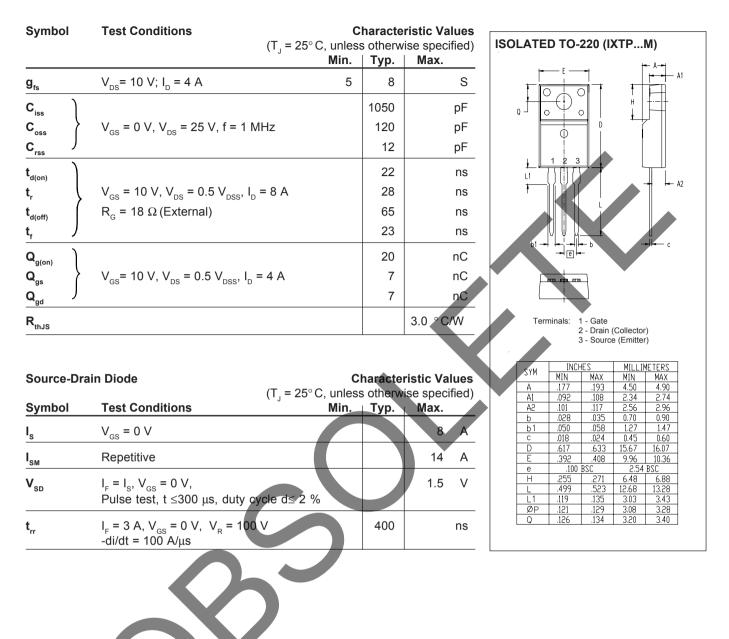
1.6 mm (0.062 in.) from case for 10 s

T_J ≤150° C, R_G = 18 Ω

Plastic body for 10 s

Mounting torque





PRELIMINARY TECHNICAL INFORMATION

The product presented herein is under development. The Technical Specifications offered are derived from data gathered during objective characterizations of preliminary engineering lots; but also may yet contain some information supplied during a pre-production design evaluation. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

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IXYS MOSFETs and IGBTs are covered by	4,835,592	4,931,844	5,049,961	5,237,481	6,162,665	6,404,065 B1	6,683,344	6,727,585
one or moreof the following U.S. patents:	4,850,072	5,017,508	5,063,307	5,381,025	6,259,123 B1	6,534,343	6,710,405B2	6,759,692
	4,881,106	5,034,796	5,187,117	5,486,715	6,306,728 B1	6,583,505	6,710,463	6,771,478 B2