



DMN2710UW

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
00)/	0.45Ω @ V _{GS} = 4.5V	0.9A
20V	0.6Ω @ V _{GS} = 2.5V	0.8A

Description and Applications

This MOSFET has been designed to minimize the on-state resistance (RDs(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- General Purpose Interfacing Switch
- Power Management Functions
- DC-DC Converters
- Analog Switch

20V N-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

Mechanical Data

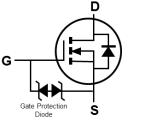
- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (c3)
- Weight: 0.027 grams (Approximate)

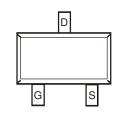




SOT323

Top View





Equivalent Circuit

Top View

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2710UW-7	SOT323	3,000/Tape & Reel
DMN2710UW-13	SOT323	10,000/Tape & Reel

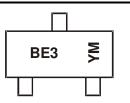
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

 See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



 $\begin{array}{l} \text{BE3} = \text{Product Type Marking Code} \\ \text{YM} = \text{Date Code Marking} \\ \hline \hline \textbf{Y} = \text{Year (ex: H = 2020)} \\ \text{M} = \text{Month (ex: 9 = September)} \end{array}$

Date Code Key

Notes:

Year	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	Н	I	J	K	L	М	Ν	0	Р	R	S	Т
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	VDSS	20	V	
Gate-Source Voltage		V _{GSS}	±6	V
Continuous Drain Current (Note 6) $V_{GS} = 4.5V$	ID	0.9 0.7	A	
Pulsed Drain Current (10µs Pulse, Duty Cycle=1%)	ldм	5	A	
Maximum Body Diode Forward Current (Note 5)		ls	0.6	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	0.47	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	268	°C/W
Total Power Dissipation (Note 6)		PD	0.6	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{0JA}	212	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)	<i></i>		- 71-			
Drain-Source Breakdown Voltage	BVDSS	20			V	V _{GS} = 0V, I _D = 250µA
Zero Gate Voltage Drain Current $@T_C = +25^{\circ}C$	IDSS	_	_	100	nA	$V_{DS} = 20V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	±1.0	μA	$V_{GS} = \pm 4.5 V, V_{DS} = 0 V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	VGS(TH)	0.5	—	1.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
		_	0.13	0.45		$V_{GS} = 4.5V, I_D = 600mA$
Static Drain-Source On-Resistance	RDS(ON)		0.16	0.6	Ω	$V_{GS} = 2.5V, I_{D} = 500mA$
			0.22	0.75		$V_{GS} = 1.8V, I_D = 350mA$
Diode Forward Voltage	Vsd		0.7	1.2	V	$V_{GS} = 0V, I_{S} = 150mA$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss		42	—	pF	
Output Capacitance	Coss		13	—	pF	VDS = 16V, VGS = 0V f = 1.0MHz
Reverse Transfer Capacitance	Crss		6.5	_	pF	1 = 1.0WH2
Total Gate Charge	Qg		0.6	—	nC	
Gate-Source Charge	Qgs	_	0.1	—	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$
Gate-Drain Charge	Qgd	_	0.1	_	nC	$I_D = 250 \text{mA}$
Turn-On Delay Time	tD(ON)	_	4.9	—	ns	
Turn-On Rise Time	t _R	_	3.1	_	ns	$V_{DD} = 10V, V_{GS} = 4.5V,$
Turn-Off Delay Time	tD(OFF)	_	386	—	ns	$R_L = 47\Omega, R_g = 10\Omega$
Turn-Off Fall Time	tF		174	—	ns	I _D = 200mA
Reverse Recovery Time	t _{RR}	—	88	_	ns	I _F = 1.0A, di/dt = 100A/µs
Reverse Recovery Charge	QRR	_	29	_	nC	IF = 1.0A, di/dt = 100A/µs

Notes:

Device mounted on FR-4 substrate PC board, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing.



DMN2710UW

= 85°C

25°C

2

2.5

7

8

6

3

-55°C

1.5

4

50

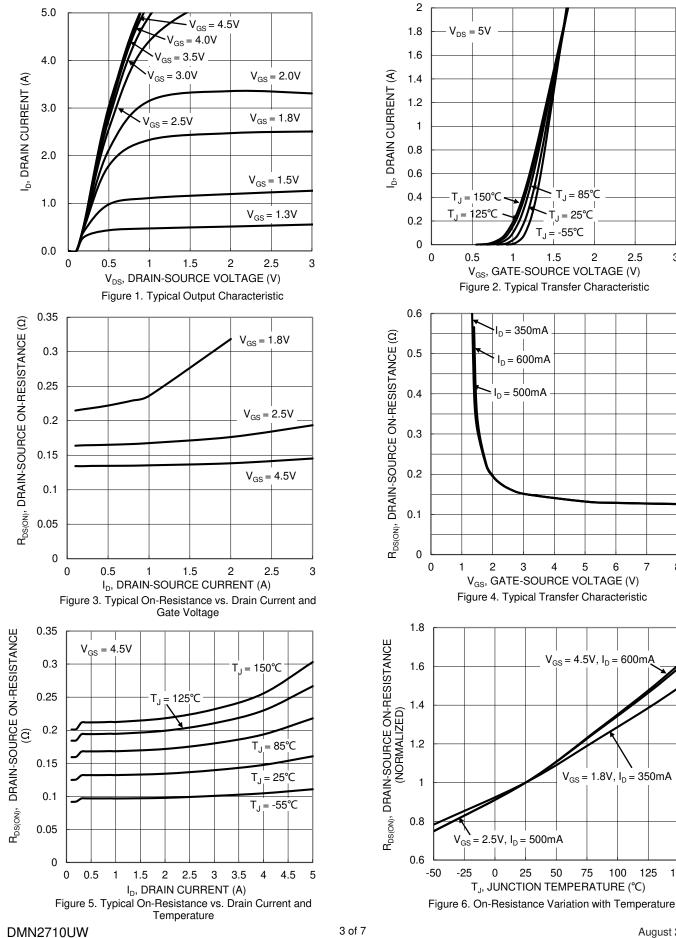
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 $V_{GS} = 4.5V, I_{D} = 600mA$

 $V_{GS} = 1.8V, I_{D} = 350mA$

100



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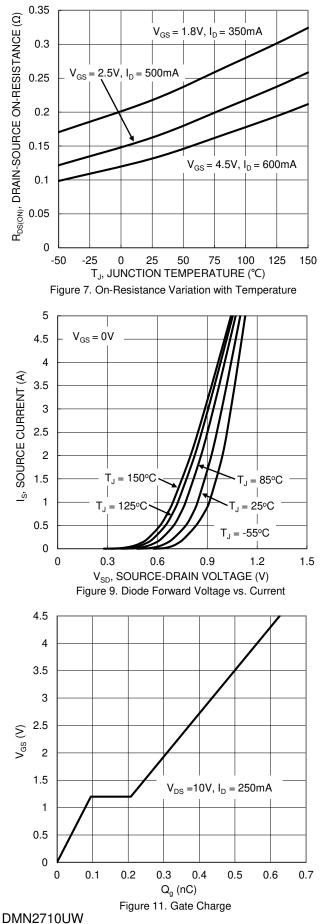
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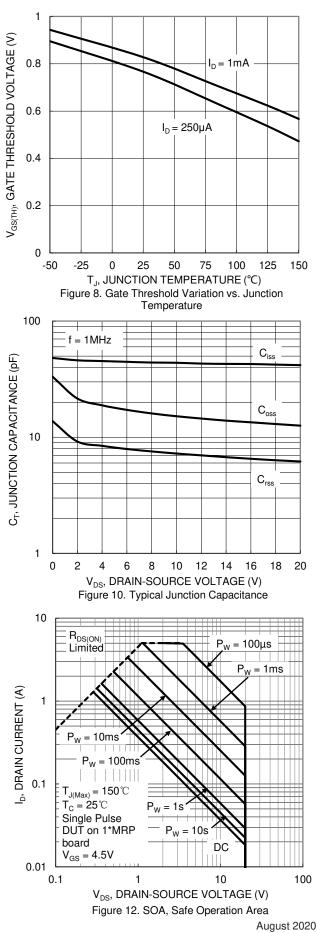
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Document number: DS42640 Rev. 2 - 2



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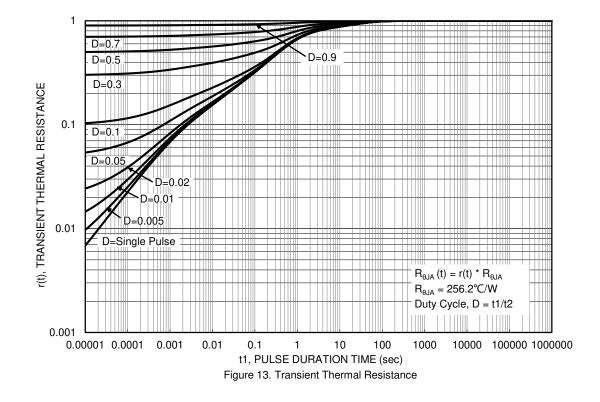


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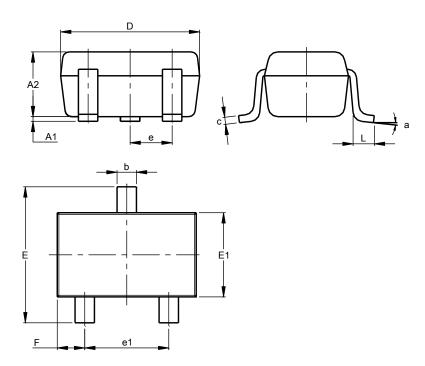




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

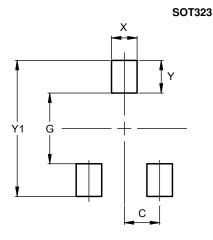




SOT323								
Dim	Min	Max	Тур					
A1	0.00	0.10	0.05					
A2	0.90	1.00	0.95					
b	0.25	0.40	0.30					
С	0.10	0.18	0.11					
D	1.80	2.20	2.15					
Е	2.00	2.20	2.10					
E1	1.15	1.35	1.30					
e	C).650 B	SC					
e1	1.20	1.40	1.30					
F	0.375	0.475	0.425					
L	0.25	0.40	0.30					
а	0°	8°						
All	All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.470
Y	0.600
Y1	2.500



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