

R6020ENZ1

Nch 600V 20A Power MOSFET

V_{DSS}	600V
$R_{DS(on)}(Max.)$	0.196Ω
I _D	20A
P_D	120W

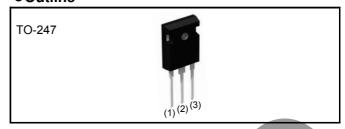
Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Gate-source voltage (V $_{\mbox{\footnotesize GSS}})$ guaranteed to be $\pm 20\mbox{\footnotesize V}.$
- 4) Drive circuits can be simple.
- 5) Parallel use is easy.
- 6) Pb-free lead plating; RoHS compliant

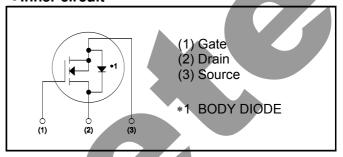
Application

Switching Power Supply

Outline



•Inner circuit



Packaging specifications

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	Packaging	Tube			
	Reel size (mm)	-			
Typo	Tape width (mm)	-			
Type	Basic ordering unit (pcs)	450			
	Taping code	C9			
	Marking	R6020ENZ1			

●Absolute maximum ratings (T_a = 25°C)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V_{DSS}	600	V
Continuous drain current T _c = 25°C	l _D *1	±20	Α
$T_c = 100^{\circ}C$	I _D ^{*1}	±10.9	А
Pulsed drain current	I _{D,pulse} *2	±60	А
Gate - Source voltage	V_{GSS}	±20	V
Avalanche energy, single pulse	E _{AS} *3	418	mJ
Avalanche energy, repetitive	E _{AR} *3	0.63	mJ
Avalanche current, repetitive	I _{AR}	3.4	А
Power dissipation (T _c = 25°C)	P_{D}	120	W
Junction temperature	T _j	150	°C
Range of storage temperature	T _{stg}	−55 to +150	°C
Reverse diode dv/dt	dv/dt *4	15	V/ns

Absolute maximum ratings

Parameter	Symbol	Conditions	Values	Unit
Drain - Source voltage slope	dv/dt	$V_{DS} = 480V$ $T_j = 25^{\circ}C$	50	V/ns

●Thermal resistance

Parameter	Symbol	Values			Unit
- Farametei	Зуппоп	Min.	Тур.	Max.	Orin
Thermal resistance, junction - case	R_{thJC}	-	-	1.04	°C/W
Thermal resistance, junction - ambient	R_{thJA}	-	-	30	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}		1	265	°C

●Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
- arameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	600	-	-	V
		$V_{DS} = 600V, V_{GS} = 0V$				
Zero gate voltage drain current	I _{DSS}	$T_j = 25^{\circ}C$	-	0.1	100	μΑ
		T _j = 125°C	-	-	1000	
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	1	±100	nA
Gate threshold voltage	V _{GS (th)}	$V_{DS} = 10V$, $I_D = 1mA$	2	-	4	V
		$V_{GS} = 10V, I_D = 9.5A$				
Static drain - source on - state resistance	R _{DS(on)} *5	T _j = 25°C	-	0.170	0.196	Ω
		T _j = 125°C		0.360		
Gate input resistance	R_{G}	f = 1MHz, open drain	-	5.8	-	Ω

●Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
r ai ai ii etei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Transconductance	g _{fs} *5	$V_{DS} = 10V, I_{D} = 10A$	5	10	-	S
Input capacitance	C _{iss}	V _{GS} = 0V	-	1400	-	
Output capacitance	C _{oss}	V _{DS} = 25V	-	1200	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	130	-	
Effective output capacitance, energy related	C _{o(er)}	V _{GS} = 0V	-	56		
Effective output capacitance, time related	C _{o(tr)}	V _{DS} = 0V to 480V		266		pF
Turn - on delay time	t _{d(on)} *5	V _{DD} ≃ 480V, V _{GS} = 10V		35	-	
Rise time	t _r *5	I _D = 10A	V -	53	-	no
Turn - off delay time	t _{d(off)} *5	$R_L = 47.5\Omega$	-	150	-	ns
Fall time	t _f *5	$R_G = 10\Omega$	-	67	-	

●Gate Charge characteristics (T_a = 25°C)

Parameter	Symbol Conditions		Values			Unit
r ai ai nietei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	Qg *5	V _{DD} ≈ 480V	-	60	-	
Gate - Source charge	Q _{gs} *5	I _D = 20A	-	8	-	nC
Gate - Drain charge	Q _{gd} *5	V _{GS} = 10V	-	33	-	
Gate plateau voltage	V _(plateau)	$V_{DD} \simeq 480V$, $I_D = 20A$	-	6.9	-	V

^{*1} Limited only by maximum temperature allowed.

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^{*2} $P_W \leq 10 \mu s,$ Duty cycle $\leq 1\%$

^{*3} I_D = 3.4A, V_{DD} = 50V

^{*4} Reference measurement circuits Fig.5-1.

^{*5} Pulsed

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
- Faranietei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Inverse diode continuous, forward current	l _S *1	T _c = 25°C	-	ı	20	А
Inverse diode direct current, pulsed	I _{SM} *2	1 c = 20 G	-	1	60	A
Forward voltage	V _{SD} *5	$V_{GS} = 0V, I_{S} = 20A$	-	-	1.5	V
Reverse recovery time	t _{rr} *5		-	550	-	ns
Reverse recovery charge	Q _{rr} *5	I _S = 20A di/dt = 100A/μs	-	10.4	1	μС
Peak reverse recovery current	I _{rrm} *5			38	-	Α

●Typical Transient Thermal Characteristics

Symbol	Value	Unit	Symbol	Value	Unit
R _{th1}	0.283		C _{th1}	0.00969	
R _{th2}	0.430	K/W	C _{th2}	0.226	Ws/K
R _{th3}	0.250		C _{th3}	13.8	

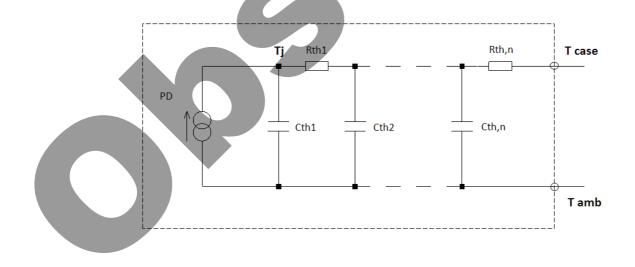
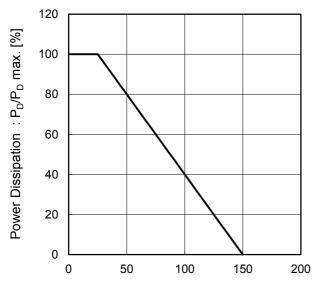
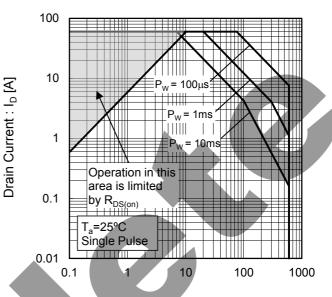


Fig.1 Power Dissipation Derating Curve



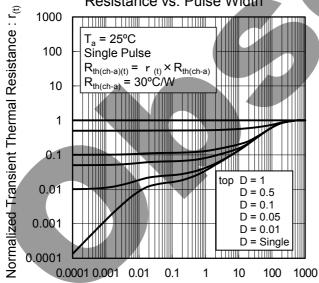
Junction Temperature : T_i [°C]

Fig.2 Maximum Safe Operating Area



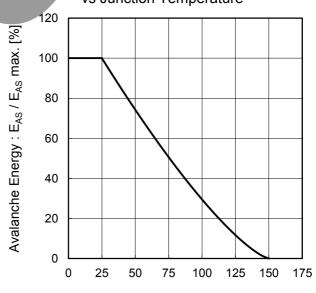
Drain - Source Voltage : V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width



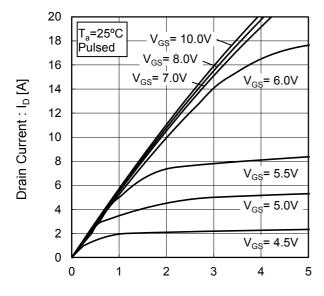
Pulse Width: Pw [s]

Fig.4 Avalanche Energy Derating Curve vs Junction Temperature



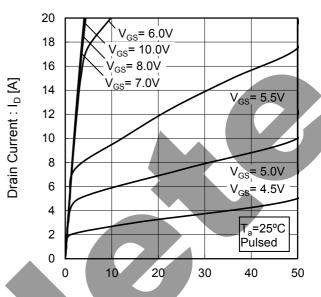
Junction Temperature : T_i [°C]

Fig.5 Typical Output Characteristics(I)



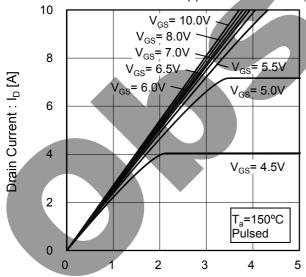
Drain - Source Voltage : V_{DS} [V]

Fig.6 Typical Output Characteristics(II)



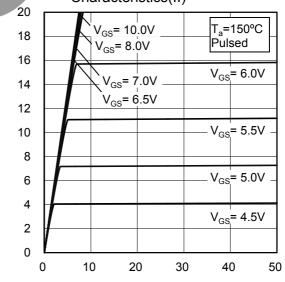
Drain - Source Voltage : V_{DS} [V]

Fig.7 T_j = 150°C Typical Output Characteristics(I)



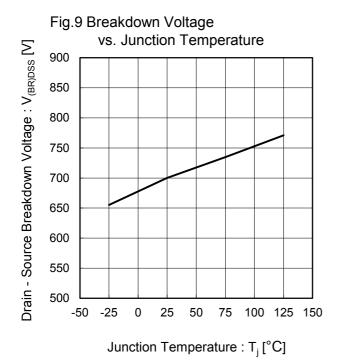
Drain - Source Voltage : V_{DS} [V]

Fig.8 T_j = 150°C Typical Output Characteristics(II)



Drain - Source Voltage : V_{DS} [V]

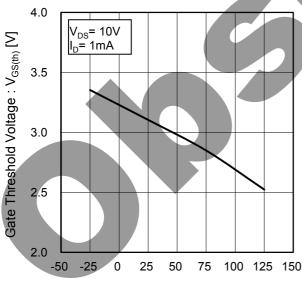
Drain Current: I_D [A]



100 V_{DS}= 10V 10 V_{DS}= 10V 11 11 125°C T_a=75°C T_a=25°C T_a=25°C T_a=25°C

Fig.10 Typical Transfer Characteristics

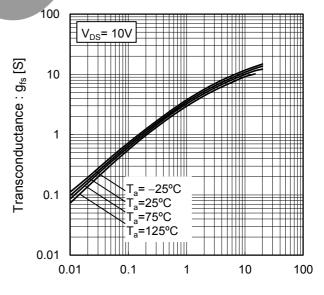
Fig.11 Gate Threshold Voltage vs. Junction Temperature



Junction Temperature : T_j [°C]

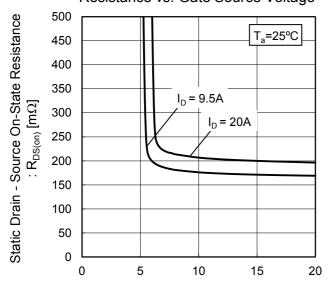
Fig.12 Transconductance vs. Drain Current

Gate - Source Voltage : V_{GS} [V]



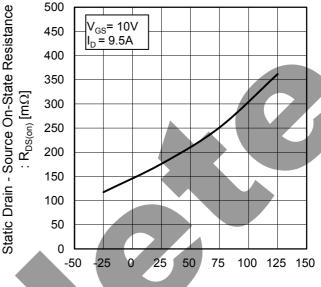
Drain Current : I_D [A]

Fig.13 Static Drain - Source On - State Resistance vs. Gate Source Voltage



Gate - Source Voltage : V_{GS} [V]

Fig.14 Static Drain - Source On - State
Resistance vs. Junction Temperature



Junction Temperature : T_i [°C]

Fig.15 Static Drain - Source On - State Resistance vs. Drain Current

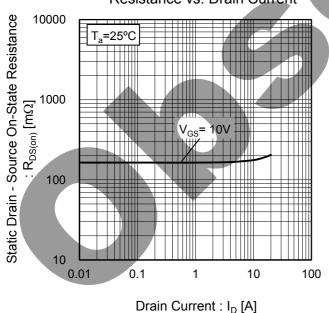
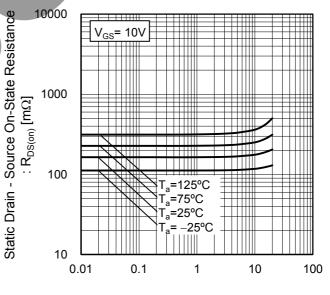
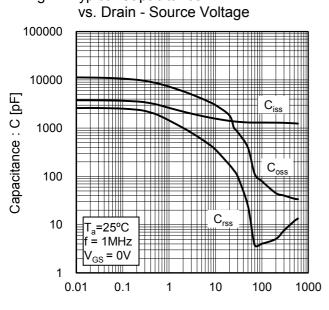


Fig. 16 Static Drain - Source On - State Resistance vs. Drain Current



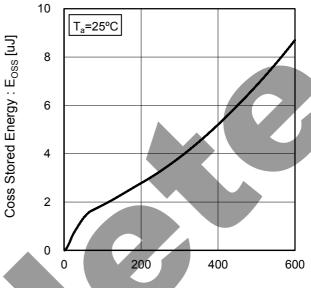
Drain Current : I_D [A]

Fig.17 Typical Capacitance



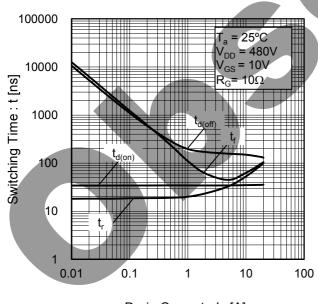
Drain - Source Voltage : V_{DS} [V]

Fig.18 Coss Stored Energy



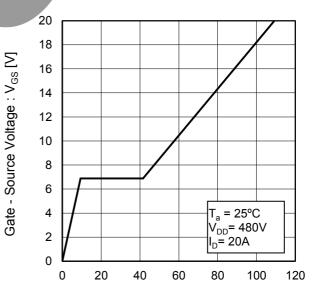
Drain - Source Voltage : V_{DS} [V]

Fig.19 Switching Characteristics



Drain Current : I_D [A]

Fig.20 Dynamic Input Characteristics

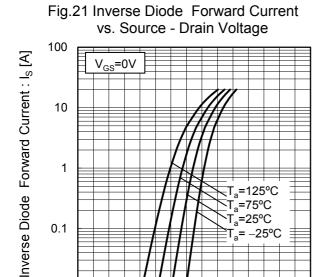


Total Gate Charge : Q_g [nC]

0.01

0.0

• Electrical characteristic curves



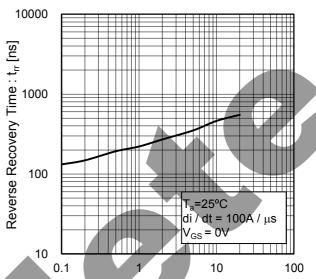
0.5

Source - Drain Voltage : V_{SD} [V]

1.0

1.5

Fig.22 Reverse Recovery Time vs.Inverse Diode Forward Current



Inverse Diode Forward Current : I_S [A]



Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

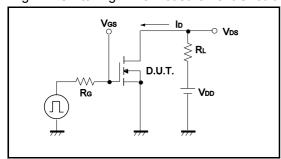


Fig.2-1 Gate Charge Measurement Circuit

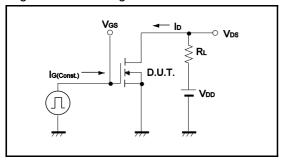


Fig.3-1 Avalanche Measurement Circuit

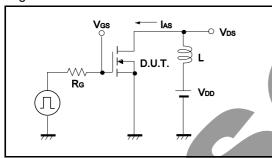


Fig.4-1 dv/dt Measurement Circuit

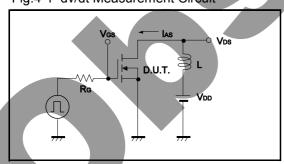


Fig.5-1 di/dt Measurement Circuit

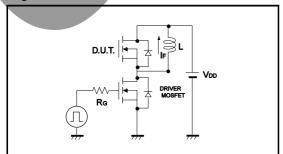


Fig.1-2 Switching Waveforms

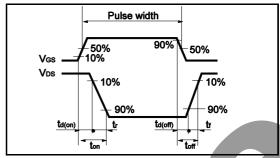


Fig.2-2 Gate Charge Waveform

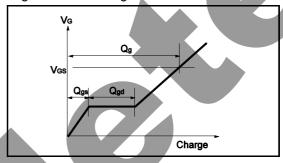


Fig.3-2 Avalanche Waveform

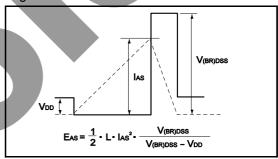


Fig.4-2 dv/dt Waveform

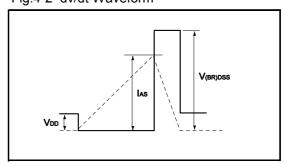
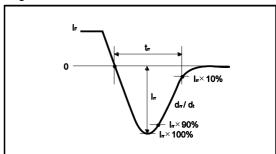
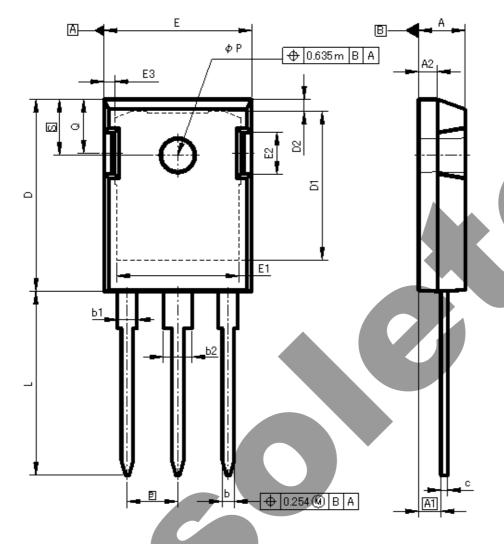


Fig.5-2 di/dt Waveform



●Dimensions (Unit : mm)

TO-247



DIM	MILIMETERS		INC	HES	
DIM	MIN	MAX	MIN	MAX	
A	4.83	5.21	0.190	0.205	
A1	2.29	2.54	0.090	0.100	
A2	1.91	2.16	0.075	0.085	
b	1.14	1.40	0.045	0.055	
b1	1.91	2.20	0.075	0.087	
b2	2.92	3.20	0.115	0.126	
С	0.61	0.80	0.024	0.031	
D	20.80	21.34	0.819	0.840	
D1	17.43	17.83	0.686	0.702	
E	15.75	16.13	0.620	0.635	
е	5.4	45	0.215		
N	3.0	00	3.0	000	
L	19.81	20.57	0.780	0.810	
L1	3.81	4.32	0.150	0.170	
ФР	3.55	3.65	0.140	0.144	
Q	5.59	6.20	0.220	0.244	
S	6.	15	0.2	40	

Dimension in mm / inches

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CLASSIV	CLASSII	CLASSⅢ	CLASSⅢ

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- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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