

## Product Summary

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	Q <sub>g</sub> Typ	I <sub>D</sub> T <sub>c</sub> = +25°C (Note 7)
40V	2.7mΩ @ V <sub>GS</sub> = 10V	68.6nC	100A

## Description

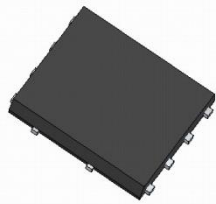
This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

## Applications

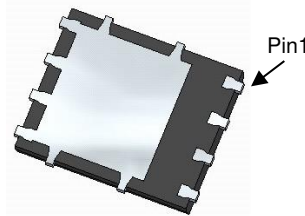
- Engine management systems
- Body control electronics
- DC-DC converters

Site 1:

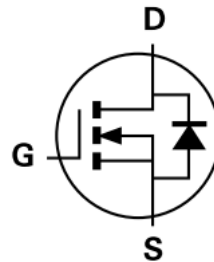
PowerDI5060-8



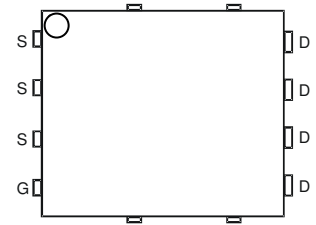
Top View



Bottom View



Internal Schematic



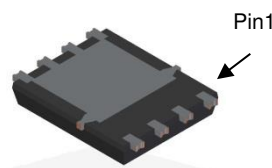
Top View  
Pin Configuration

Site 2:

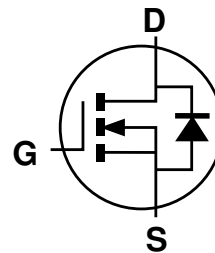
PowerDI5060-8/SWP (Type UX)



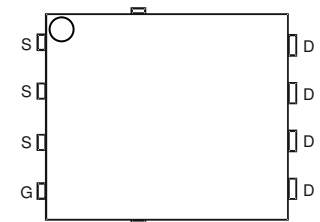
Top View



Bottom View



Internal Schematic



Top View  
Pin Configuration

## Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMTH4004SPS-13	PowerDI5060-8	2500	Tape & Reel
DMTH4004SPS-13	PowerDI5060-8/SWP (Type UX)	2500	Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

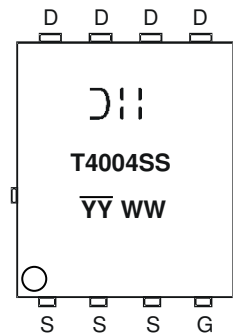
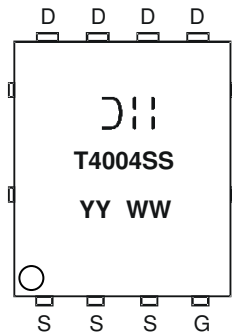
## Features

- Rated to +175°C – Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching Ensures More Reliable and Robust End Application
- Low R<sub>DS(ON)</sub> – Minimizes Power Losses
- Low Q<sub>g</sub> – Minimizes Switching Losses
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.**  
<https://www.diodes.com/quality/product-definitions/>
- **An automotive-compliant part is available under separate datasheet ([DMTH4004SPSQ](#))**

## Mechanical Data

- Package: PowerDI<sup>®</sup>5060-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.097 grams (Approximate)

## Marking Information



Ⓜ|| = Manufacturer's Marking  
 T4004SS = Product Type Marking Code  
 YYWW = Date Code Marking  
 YY or YY = Year (ex: 23 = 2023)  
 WW = Week (01 to 53)

## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V <sub>DSS</sub>	40	V
Gate-Source Voltage		V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 5)	T <sub>A</sub> = +25°C	I <sub>D</sub>	31	A
	T <sub>A</sub> = +70°C		26	
Continuous Drain Current (Note 6)	T <sub>C</sub> = +25°C (Note 7)	I <sub>D</sub>	100	A
	T <sub>C</sub> = +100°C		100	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I <sub>DM</sub>	350	A
Maximum Continuous Body Diode Forward Current (Note 5)		I <sub>S</sub>	100	A
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)		I <sub>SM</sub>	350	A
Avalanche Current, L=0.2mH		I <sub>AS</sub>	45	A
Avalanche Energy, L=0.2mH		E <sub>AS</sub>	200	mJ

## Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	P <sub>D</sub>	3.6	W
Thermal Resistance, Junction to Ambient (Note 5)		R <sub>θJA</sub>	41	°C/W
Total Power Dissipation (Note 6)	T <sub>C</sub> = +25°C	P <sub>D</sub>	167	W
Thermal Resistance, Junction to Case (Note 6)		R <sub>θJC</sub>	0.9	°C/W
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +175	°C

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.  
 6. Thermal resistance from junction to soldering point (on the exposed drain pad).  
 7. Package limited.

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS</b> (Note 8)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	40	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1	μA	V <sub>DS</sub> = 32V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS</b> (Note 8)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	2	—	4	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	2.3	2.7	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 90A
Diode Forward Voltage	V <sub>SD</sub>	—	0.9	1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A
<b>DYNAMIC CHARACTERISTICS</b> (Note 9)						
Input Capacitance	C <sub>iss</sub>	—	4,305	—	pF	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1MHz
Output Capacitance	C <sub>oss</sub>	—	1,441	—		
Reverse Transfer Capacitance	C <sub>rss</sub>	—	102	—		
Gate Resistance	R <sub>g</sub>	—	0.77	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz
Total Gate Charge	Q <sub>g</sub>	—	68.6	—	nC	V <sub>DD</sub> = 20V, I <sub>D</sub> = 90A, V <sub>GS</sub> = 10V
Gate-Source Charge	Q <sub>gs</sub>	—	16.8	—		
Gate-Drain Charge	Q <sub>gd</sub>	—	14.2	—		
Turn-On Delay Time	t <sub>D(ON)</sub>	—	9.5	—	ns	V <sub>DD</sub> = 20V, V <sub>GS</sub> = 10V, I <sub>D</sub> = 90A, R <sub>G</sub> = 3.5Ω
Turn-On Rise Time	t <sub>R</sub>	—	6.7	—		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	26.4	—		
Turn-Off Fall Time	t <sub>F</sub>	—	8.1	—		
Body Diode Reverse Recovery Time	t <sub>RR</sub>	—	52.4	—	ns	I <sub>F</sub> = 50A, di/dt = 100A/μs
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	—	78.2	—	nC	

Notes: 8. Short duration pulse test used to minimize self-heating effect.  
9. Guaranteed by design. Not subject to production testing.

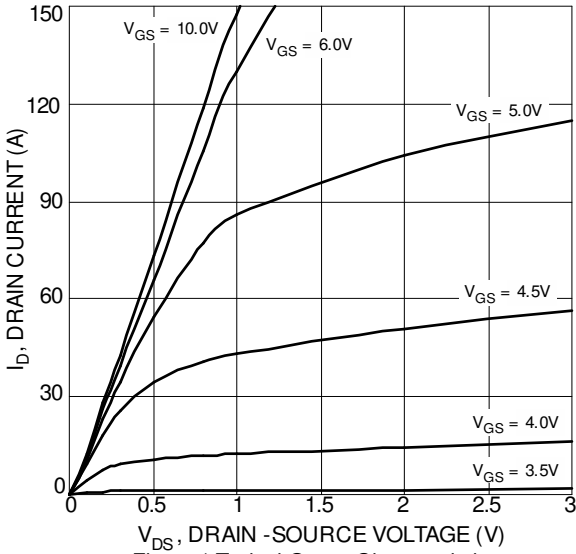


Figure 1 Typical Output Characteristics

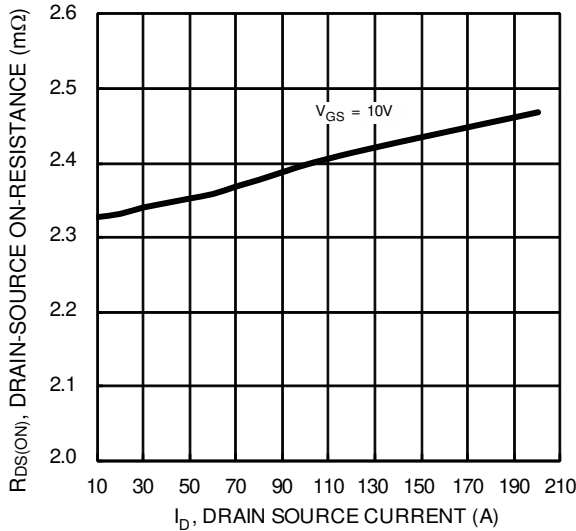


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

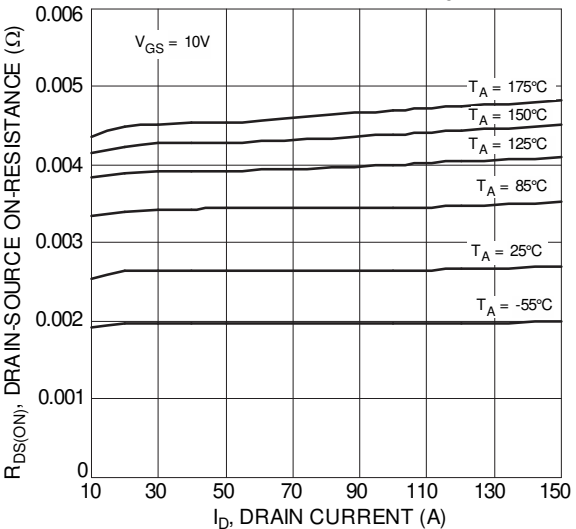


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

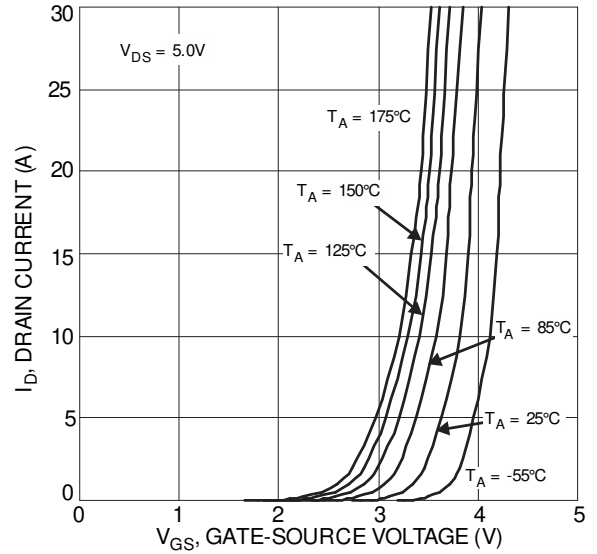


Figure 2 Typical Transfer Characteristics

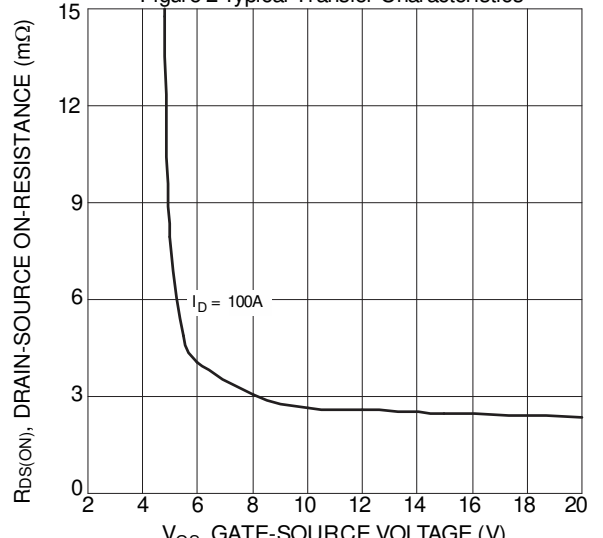


Figure 4 Typical Transfer Characteristics

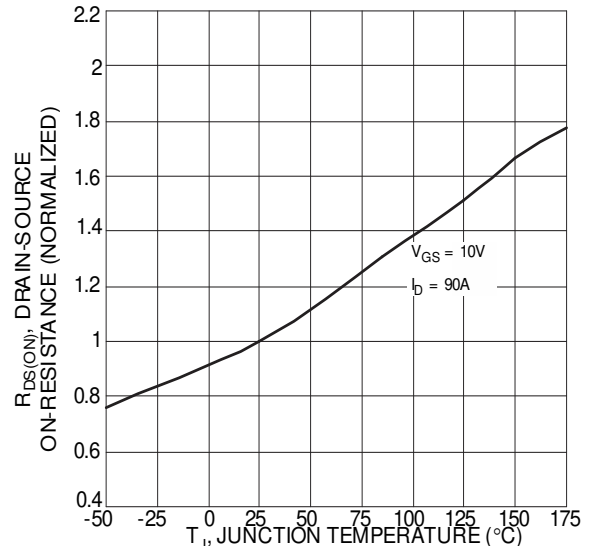


Figure 6 On-Resistance Variation with Temperature

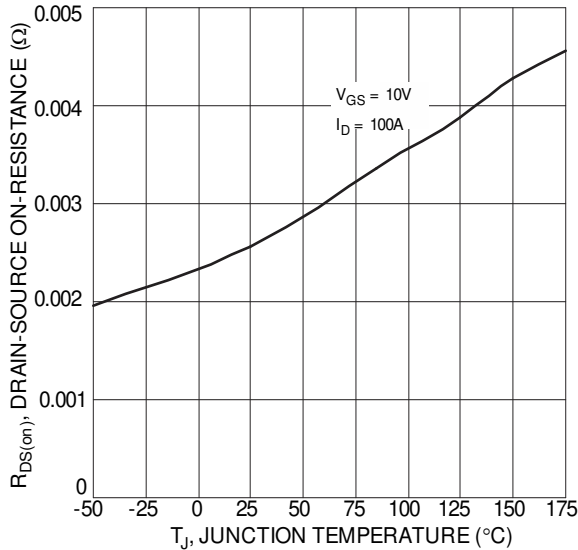


Figure 7 On-Resistance Variation with Temperature

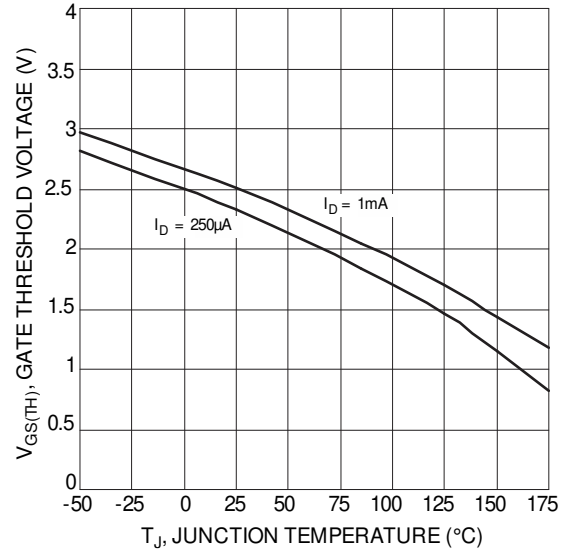


Figure 8 Gate Threshold Variation vs. Temperature

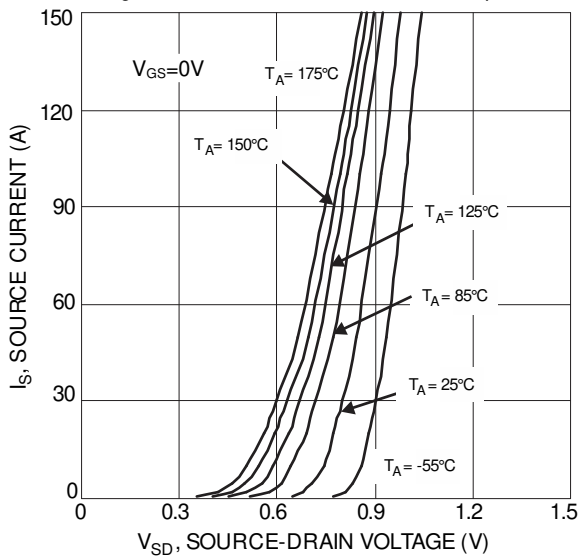


Figure 9 Diode Forward Voltage vs. Current

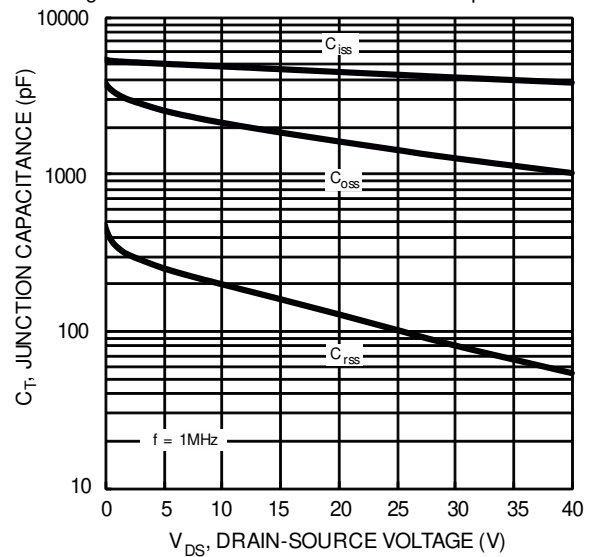


Figure 10 Typical Junction Capacitance

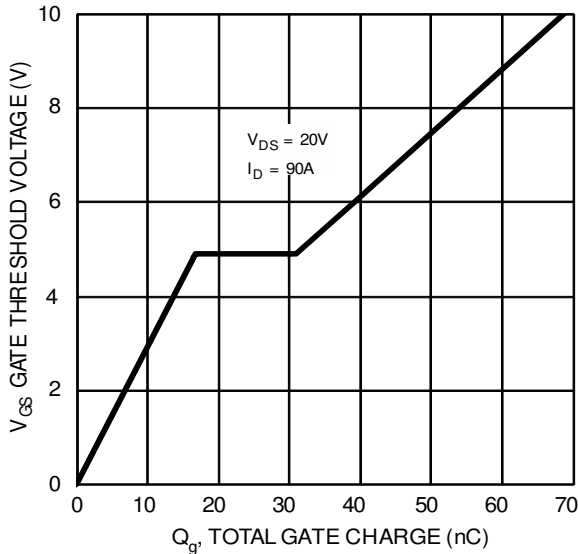


Figure 11 Gate Charge

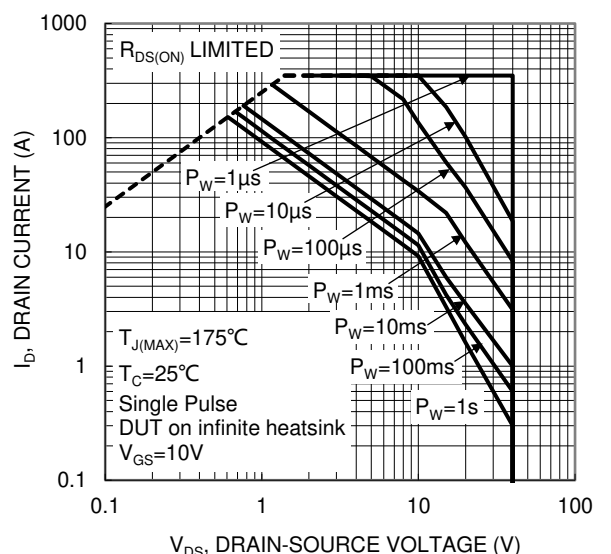
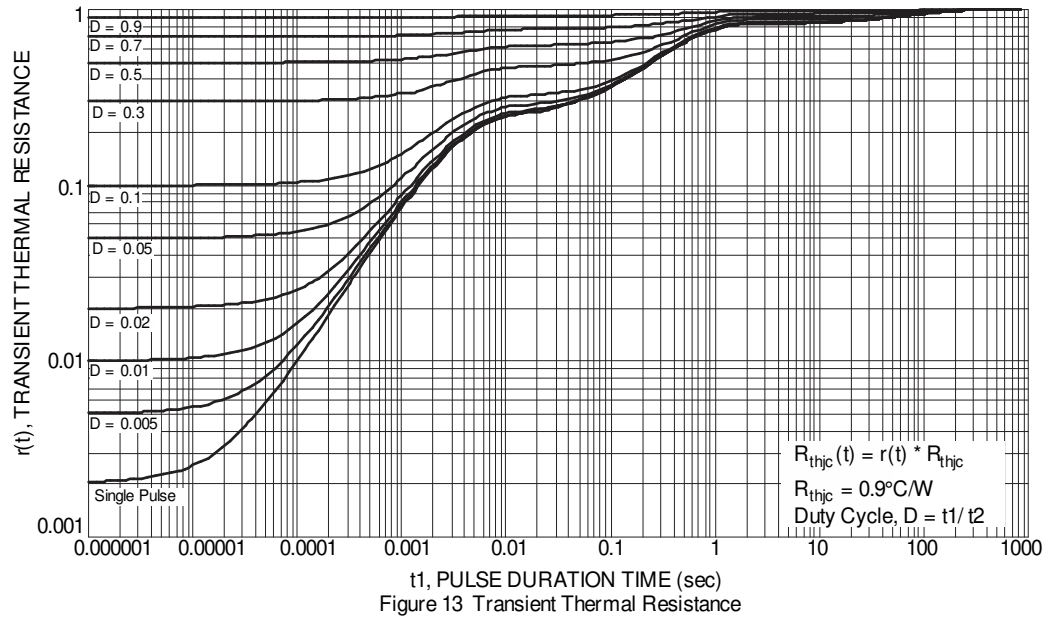


Figure 12. SOA, Safe Operation Area

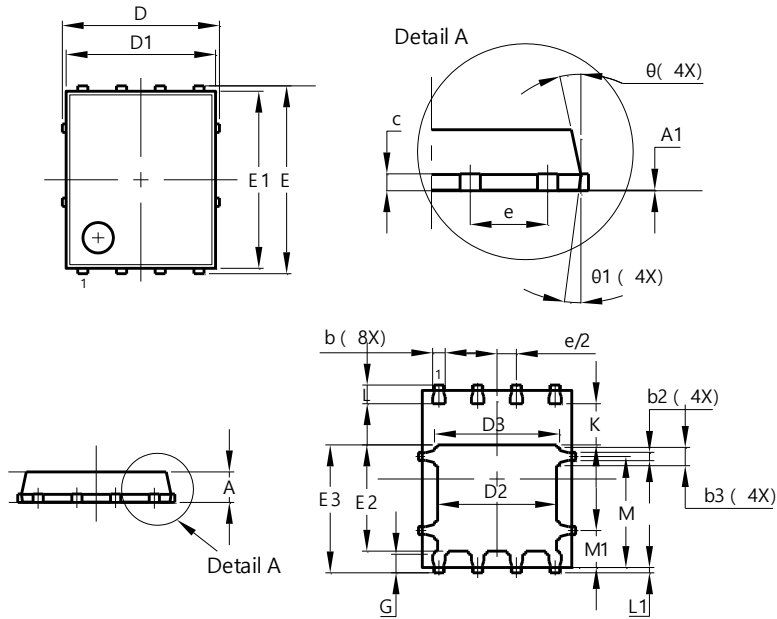


**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

Site 1:

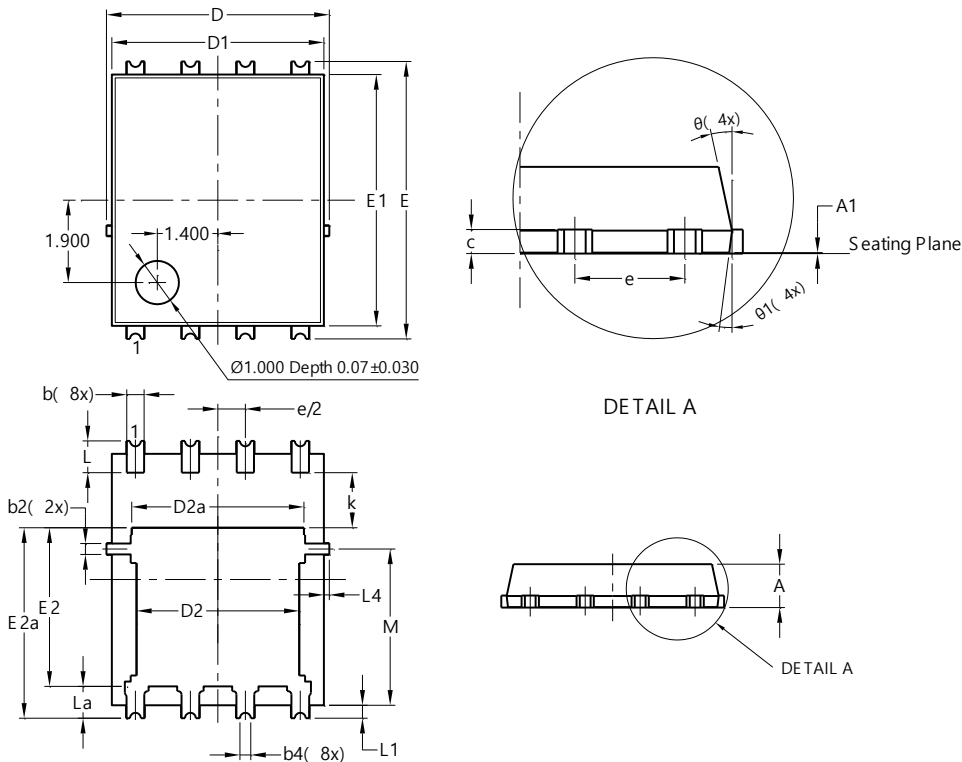
**PowerDI5060-8**



PowerDI5060-8			
Dim	Min	Max	Typ
<b>A</b>	0.90	1.10	1.00
<b>A1</b>	0.00	0.05	—
<b>b</b>	0.33	0.51	0.41
<b>b2</b>	0.200	0.350	0.273
<b>b3</b>	0.40	0.80	0.60
<b>c</b>	0.230	0.330	0.277
<b>D</b>	5.15 BSC		
<b>D1</b>	4.70	5.10	4.90
<b>D2</b>	3.70	4.10	3.90
<b>D3</b>	3.90	4.30	4.10
<b>E</b>	6.15 BSC		
<b>E1</b>	5.60	6.00	5.80
<b>E2</b>	3.28	3.68	3.48
<b>E3</b>	3.99	4.39	4.19
<b>e</b>	1.27 BSC		
<b>G</b>	0.51	0.71	0.61
<b>K</b>	0.51	—	—
<b>L</b>	0.51	0.71	0.61
<b>L1</b>	0.100	0.200	0.175
<b>M</b>	3.235	4.035	3.635
<b>M1</b>	1.00	1.40	1.21
<b>θ</b>	10°	12°	11°
<b>θ1</b>	6°	8°	7°
<b>All Dimensions in mm</b>			

Site 2:

**PowerDI5060-8/SWP (Type UX)**



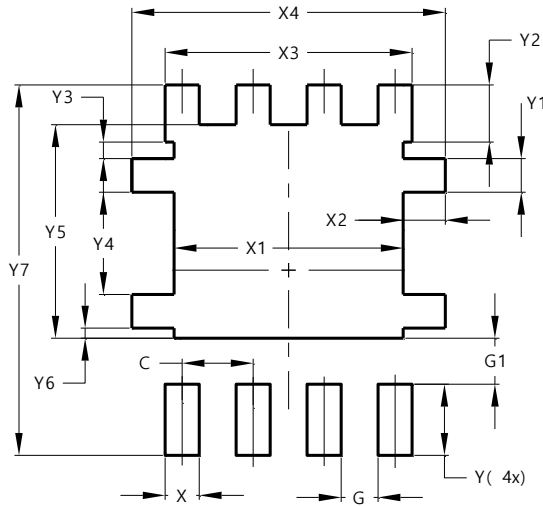
PowerDI5060-8/SWP (Type UX)			
Dim	Min	Max	Typ
<b>A</b>	0.90	1.10	1.00
<b>A1</b>	0	0.05	--
<b>b</b>	0.30	0.50	0.41
<b>b2</b>	0.20	0.35	0.25
<b>b4</b>	0.25REF		
<b>c</b>	0.230	0.330	0.277
<b>D</b>	5.15 BSC		
<b>D1</b>	4.70	5.10	4.90
<b>D2</b>	3.56	3.96	3.76
<b>D2a</b>	3.78	4.18	3.98
<b>E</b>	6.40 BSC		
<b>E1</b>	5.60	6.00	5.80
<b>E2</b>	3.46	3.86	3.66
<b>E2a</b>	4.195	4.595	4.395
<b>e</b>	1.27BSC		
<b>k</b>	1.05	--	--
<b>L</b>	0.635	0.835	0.735
<b>La</b>	0.635	0.835	0.735
<b>L1</b>	0.200	0.400	0.300
<b>L1a</b>	0.050REF		
<b>L4</b>	0.025	0.225	0.125
<b>M</b>	3.205	4.005	3.605
<b>θ</b>	10°	12°	11°
<b>θ1</b>	6°	8°	7°
<b>All Dimensions in mm</b>			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

Site 1:

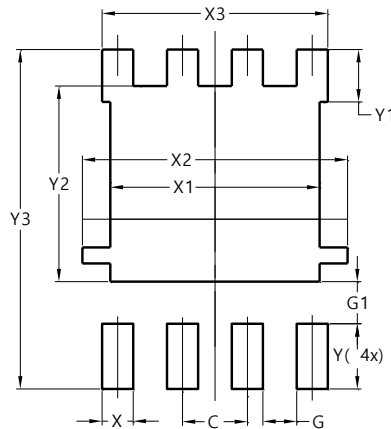
**PowerDI5060-8**



Dimensions	Value (in mm)
<b>C</b>	1.270
<b>G</b>	0.660
<b>G1</b>	0.820
<b>X</b>	0.610
<b>X1</b>	4.100
<b>X2</b>	0.755
<b>X3</b>	4.420
<b>X4</b>	5.610
<b>Y</b>	1.270
<b>Y1</b>	0.600
<b>Y2</b>	1.020
<b>Y3</b>	0.295
<b>Y4</b>	1.825
<b>Y5</b>	3.810
<b>Y6</b>	0.180
<b>Y7</b>	6.610

Site 2:

**PowerDI5060-8/SWP (Type UX)**



Dimensions	Value (in mm)
<b>C</b>	1.270
<b>G</b>	0.660
<b>G1</b>	0.820
<b>X</b>	0.610
<b>X1</b>	4.100
<b>X2</b>	5.190
<b>X3</b>	4.420
<b>Y</b>	1.270
<b>Y1</b>	1.020
<b>Y2</b>	3.810
<b>Y3</b>	6.610



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