

### Programmable Voltage Controlled Oscillator (VCXO)

**Output: LV-PECL** 

## VG7050EAN / ECN

: 50 MHz to 800 MHz

(Tuning resolution: 2.2 to 2.8 ×10<sup>-9</sup>)

 Supply voltage : 2.5 V / 3.3 V

 $\bullet$  External dimensions : EAN : 7.0 × 5.0 × 1.5 mm (8 pins) ECN: 7.0 × 5.0 × 1.5 mm (10 pins)

• Absolute Pull Range : ±0 × 10<sup>-6</sup> to ±180 × 10<sup>-6</sup> (12 steps selectable)

Features

• EAN: User-specified one startup frequency, APR and 7-bit I<sup>2</sup>C address • ECN: User-specified four startup frequency, APR and 7-bit I2C address

• User Programming : I<sup>2</sup>C Interface

Low jitter PLL technology

Applications

SONET/SDH, OTN, GbE, Fibre Channel





**Product Number** 

EAN: X1G004541xxxx00 ECN: X1G004561xxxx00





#### Specifications (characteristics)

Item	Symbol	Specifications	Conditions / Remarks	
Output frequency range	fo	50 MHz to 800 MHz	It can be changed by I <sup>2</sup> C	
Supply voltage	V <sub>CC</sub>	D: 2.5 V ± 0.125 V, C: 3.3 V ± 0.33 V		
Storage temperature	T_stg	-55 °C to +125 °C	Store as bare product after packing	
Operating temperature	T_use	-40 °C to +85 °C		
Frequency tolerance *1	f_tol	±50 × 10 <sup>-6</sup>	Includes frequency aging (10 years)	
Current consumption	I <sub>cc</sub>	90 mA Max.	OE Active, L_ECL=50 Ω	
Disable current	1 -0-	40 mA Max.	OE Inactive, Output Standby: Hi-Z mode	
	l_dis	70 mA Max.	OE Inactive, Output Standby: Fix mode	
Absolute pull range	APR	±0 × 10 <sup>-6</sup> to ±180 × 10 <sup>-6</sup> Min.	Vc = 1.65 V ± 1.35 V (Vcc = 3.3 V)	
	AFIX	$\pm 0 \times 10^{-6}$ to $\pm 180 \times 10^{-6}$ Min.	Vc = 1.25 V ± 1.00 V (Vcc = 2.5 V)	
Control voltage tuning range	Vc	0 V to Vcc		
Frequency change polarity	-	Positive slope		
Symmetry	SYM	45 % to 55 %	At outputs crossing point	
Output voltage	V <sub>OH</sub>	Vcc - 1.025 V Min.	DC characteristics	
	V <sub>OL</sub>	Vcc - 1.62 V Max.		
Output load condition	L_ECL	50 Ω	Termination to Vcc - 2.0 V	
Input voltage	V <sub>IH</sub>	70 % Vcc Min.	EAN : OE, SDA and SCL	
	V <sub>IL</sub>	30 % Vcc Max.	ECN : OE, FSEL0, FSEK1, SDA and SCL	
Rise time / Fall time	tr / tf	400 ps Max.	Between 20 % and 80 % of (VoH - VoL)	
Start-up time	t_str	10 ms Max.	Time at minimum supply voltage to be 0 s	

<sup>\*1</sup> Frequency tolerance includes initial frequency tolerance, temperature variation, supply voltage change, reflow drift and 10 years aging at +25 °C.

Product name (Standard form) VG7050 EAN SM18xxxx C J G H P Z 456789

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①Model

2)Output (E: LV-PECL)

③Parameter Designator (EAN: SM18xxxx, ECN: SM20xxxx )

(4) Supply voltage (C: 3.3 V Typ., D: 2.5 V Typ.)

⑤Frequency tolerance (J: ±50 × 10<sup>-6</sup>)

(G: -40 °C to +85 °C)

70E Function (H: Active High, L: Active Low)

(a) Absolute Pull Range (P: Programmable)

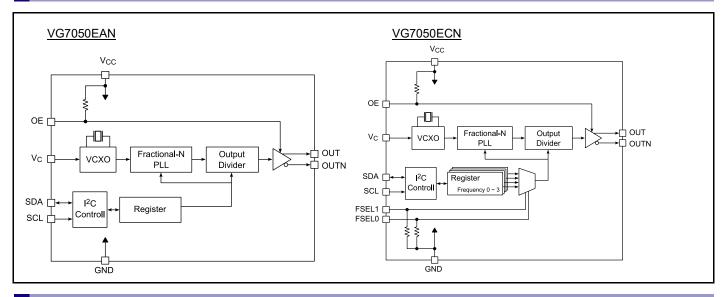
#### Phase Jitter

	Offset Frequency	125.00 MHz	156.25 MHz	250.00 MHz	425.00 MHz	622.08 MHz	669.33 MHz	794.73 MHz
Phase jitter*2 Typ.	12 kHz to 20 MHz	0.30 ps	0.26 ps	0.26 ps	0.25 ps	0.26 ps	0.26 ps	0.26 ps
	20 kHz to 50 MHz	0.30 ps	0.27 ps	0.27 ps	0.26 ps	0.27 ps	0.27 ps	0.27 ps
	50 kHz to 80 MHz	0.29 ps	0.27 ps	0.27 ps	0.26 ps	0.27 ps	0.27 ps	0.27 ps

<sup>\*2</sup> In order to achieve optimum jitter performance, it is recommended that the capacitor (0.1 µF + 10 µF) between Vcc and GND pin should be placed as close to the Vcc pin as possible.

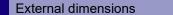


#### Block diagram

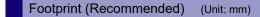


#### OE Function / OE Standby Type

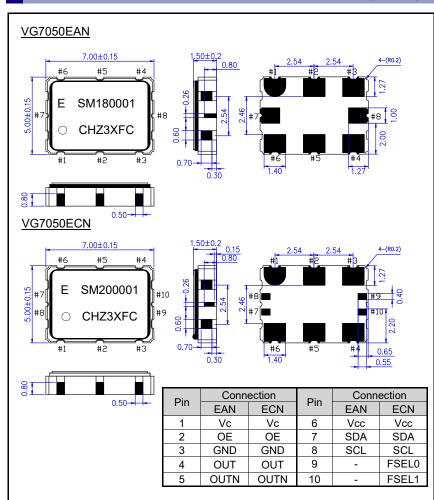
OE Function	OE Standby Type	Output Enable	Output Disable		
	OL Standby Type	OE pin	OE pin	OUT, OUTN pin	
H: High Active	7. High 7	"H" or "OPEN"	"L"	Lligh Impedance	
L: Low Active	Z: High-Z	"L" or "OPEN"	"H"	High Impedance	
H: High Active	F: Fix	"H" or "OPEN"	"L"	OUT = "L", OUTN = "H"	
L: Low Active	F. FIX	"L" or "OPEN"	"H"	001 - L,001N - H	

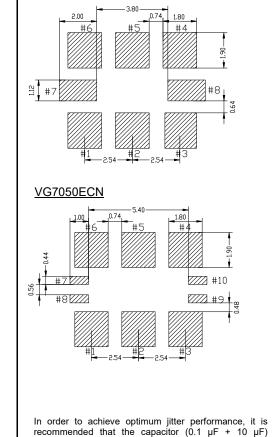


(Unit: mm)



VG7050EAN





between VCC and GND pin should be placed as close to

the VCC pin as possible.

# PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

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►Pb free.



► Complies with EU RoHS directive.

\*About the products without the Pb-free mark.

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