

MP34DT04-C1

MEMS audio sensor omnidirectional digital microphone

Datasheet - production data



Features

- Single supply voltage
- Low power consumption
- 120 dBSPL acoustic overload point
- 64 dB signal-to-noise ratio
- Omnidirectional sensitivity
- -26 dBFS sensitivity
- PDM output
- HCLGA package
 - Top-port design
 - SMD-compliant
 - EMI-shielded
 - ECOPACK[®], RoHS, and "Green" compliant

Applications

- Mobile terminals
- Laptop and notebook computers
- Portable media players
- VoIP
- Speech recognition
- A/V eLearning devices

- Gaming and virtual reality input devices
- Digital still and video cameras
- Antitheft systems

Description

The MP34DT04-C1 is an ultra-compact, lowpower, omnidirectional, digital MEMS microphone built with a capacitive sensing element and an IC interface.

The sensing element, capable of detecting acoustic waves, is manufactured using a specialized silicon micromachining process dedicated to produce audio sensors.

The IC interface is manufactured using a CMOS process that allows designing a dedicated circuit able to provide a digital signal externally in PDM format.

The MP34DT04-C1 has an acoustic overload point of 120 dBSPL with a 64 dB signal-to-noise ratio and –26 dBFS sensitivity.

The MP34DT04-C1 is available in a top-port, SMD-compliant, EMI-shielded package and is guaranteed to operate over an extended temperature range from -40 °C to +85 °C.

Table 1: Device summary

Order codes	Temp. range [ºC]	Package	Packing
MP34DT04TR-C1	-40 to +85	HCLGA (3x4 x1.095 mm) 4LD	Tape and reel

December 2016

DocID029851 Rev 2

This is information on a product in full production.

Contents

Contents

1	Pin des	cription	5
2	Acousti	ic and electrical specifications	6
	2.1	Acoustic and electrical characteristics	6
	2.2	Timing characteristics	7
	2.3	Frequency response	8
3	Applica	ition recommendations	9
4	Carrier	tape mechanical specifications	11
5	Process	s recommendations	12
6	Sensing	g element	14
7	Absolut	te maximum ratings	15
8	Functio	onality	16
	8.1	L/R channel selection	16
9	Packag	e information	17
	9.1	Soldering information	17
	9.2	HCLGA package information	18
10	Revisio	n history	



List of tables

Table 1: Device summary	.1
Table 2: Pin description	
Table 3: Acoustic and electrical characteristics	
Table 4: Distortion specifications	.6
Table 5: Timing characteristics	.7
Table 6: Frequency response mask for digital microphones	.8
Table 7: Absolute maximum ratings	15
Table 8: L/R channel selection	16
Table 9: Recommended soldering profile limits	17
Table 10: Document revision history	20



List of figures

Figure 1: Pin connections	5
Figure 2: Timing waveforms	
Figure 3: Frequency response and mask	8
Figure 4: MP34DT04-C1 electrical connections (top view)	
Figure 5: MP34DT04-C1 electrical connections for stereo configuration (top view)	10
Figure 6: Carrier tape without microphone (top view)	11
Figure 7: Carrier tape with microphone (top view)	
Figure 8: Recommended picking area	
Figure 9: Recommended picker design	13
Figure 10: Recommended soldering profile limits	
Figure 11: HCLGA (3x4x1.095) 4-lead package outline and mechanical data	
Figure 12: Land pattern	



57

1 Pin description

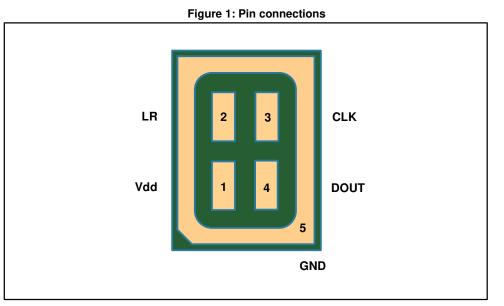


Table 2: Pin description

Pin #	Pin name	Function
1	Vdd	Power supply
2	LR	Left/Right channel selection
3	CLK	Synchronization input clock
4	DOUT	Left/Right PDM data output
5 (ground ring)	GND	0 V supply



2 Acoustic and electrical specifications

2.1 Acoustic and electrical characteristics

The values listed in the table below are specified for Vdd = 1.8 V, Clock = 2.4 MHz, T = 25 °C, unless otherwise noted.

Symbol	Parameter	Test condition	Min.	Typ. ⁽¹⁾	Max.	Unit
Vdd	Supply voltage		1.6	1.8	3.6	V
ldd	Current consumption in normal mode	Mean value		650		μA
lddPdn	Current consumption in power-down mode ⁽²⁾				10	μA
Scc	Short-circuit current		1		10	mA
AOP	Acoustic overload point			120		dBSPL
So	Sensitivity		-29	-26	-23	dBFS
SNR	Signal-to-noise ratio	A-weighted at 1 kHz, 94 dB SPL		64		dB (A)
PSR	Power supply rejection	100 mVpp sine 1 kHz		-70		dBFS
fclk	Input clock frequency ⁽³⁾		1.2	2.4	3.25	MHz
Ton	Turn-on time ⁽⁴⁾	Guaranteed by design			10	ms
Тор	Operating temperature range		-40		+85	°C
VIOL	Low-level logic input/output voltage	l _{out} = 1 mA	-0.3		0.35xVdd	V
VIOH	High-level logic input/output voltage	l _{out} = 1 mA	0.65xVdd		Vdd+0.3	V
CLOAD	Capacitive load				100	pF

Table 3: Acoustic and electrical characteristics

Notes:

⁽¹⁾Typical specifications are not guaranteed.

⁽²⁾Input clock in static mode.

⁽³⁾Duty cycle: min = 40% max = 60%

⁽⁴⁾Time from the first clock edge to valid output data.

Table 4: Distortion	specifications
---------------------	----------------

Parameter	Test condition	Typ. value ⁽¹⁾
Distortion	100 dBSPL (50 Hz - 4 kHz)	<1% THD + N
Distortion	115 dBSPL (1 kHz)	<5% THD + N

Notes:

⁽¹⁾Typical specifications are not guaranteed.

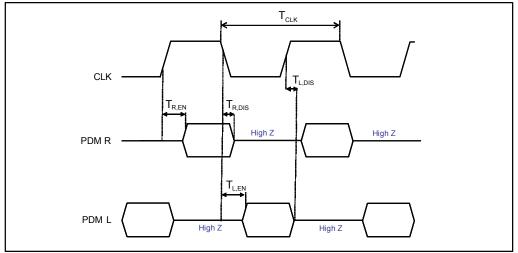


2.2 Timing characteristics

Table 5: Timing characteristics

Parameter	Description	Min.	Max.	Unit
f _{CLK}	Clock frequency for normal mode	1.2	3.25	MHz
f _{PD}	Clock frequency for power-down mode		0.23	MHz
Т _{СLК}	Clock period for normal mode	308	1000	ns
T _{R,EN}	Data enabled on DATA line, L/R pin = 1	70	90	ns
T _{R,DIS}	Data disabled on DATA line, L/R pin = 1	4.3	5.3	ns
T _{L,EN}	Data enabled on DATA line, L/R pin = 0	64	87	ns
T _{L,DIS}	Data disabled on DATA line, L/R pin = 0	3.5	4.3	ns

Figure 2: Timing waveforms





2.3 Frequency response

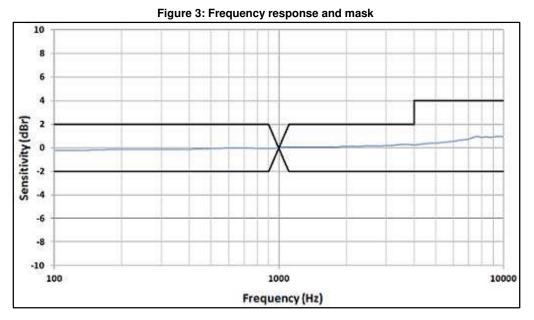


Table 6: Frequency response mask for digital microphones

Frequency / Hz ⁽¹⁾	Lower limit	Upper limit	Unit
1004000	-2	+2	dBr 1 kHz
400010000	-2	+4	dBr 1 kHz

Notes:

 $^{(1)}\text{At}$ T = 20 °C and acoustic stimulus = 1 Pa (94 dB SPL)



3 Application recommendations

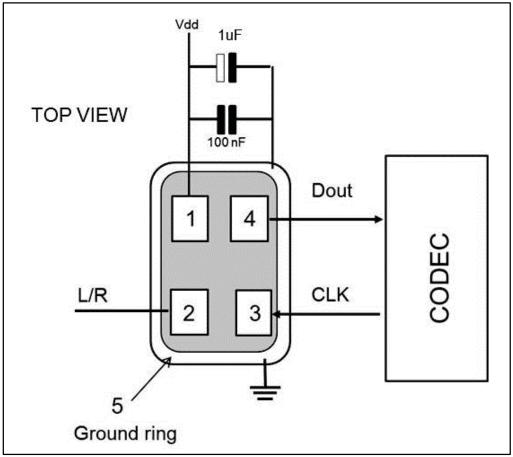
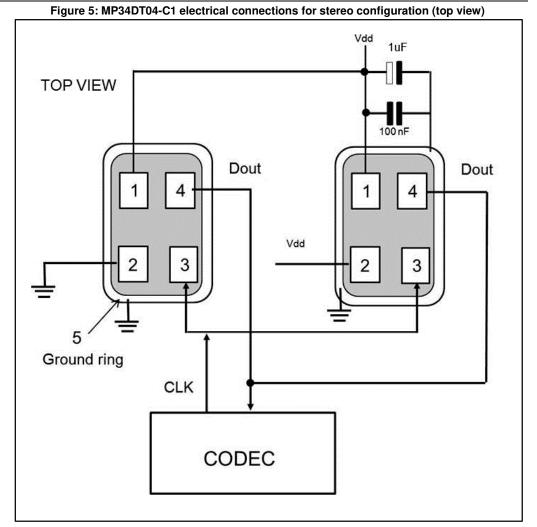


Figure 4: MP34DT04-C1 electrical connections (top view)



Application recommendations



Power supply decoupling capacitors (100 nF ceramic, 1 μ F ceramic) should be placed as near as possible to pin 1 of the device (common design practice).

The L/R pin must be connected to Vdd or GND (refer to Table 8: "L/R channel selection").



4 Carrier tape mechanical specifications

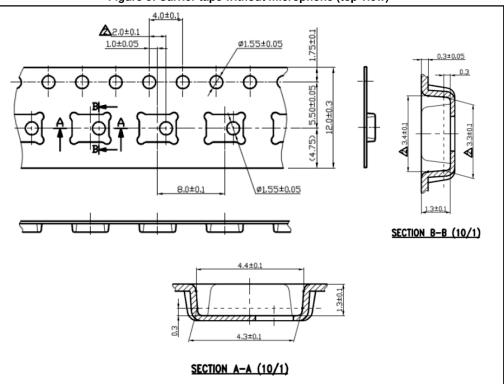
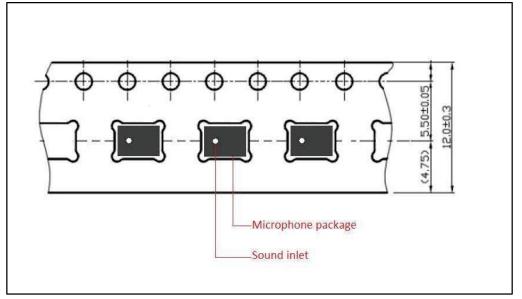


Figure 6: Carrier tape without microphone (top view)

Figure 7: Carrier tape with microphone (top view)





5 **Process recommendations**

To ensure a consistent manufacturing process it is strongly advised to comply with the following recommendations:

- The recommended pick-up area for the MP34DT04-C1 package must be defined using the worst case (ie. no device alignment during the picking process). This area has been defined considering all the tolerances of the components involved (reel, package, sound inlet). The picker tolerance shall be considered as well.
- To prevent damage to the MEMS membrane or incorrect pick-up and placement, do
 not pick up the component on the inlet area.
- For the package outline please refer to *Figure 7: "Carrier tape with microphone (top view)"*. Nozzle shape, size, and placement accuracy are the other key factors to consider when deciding on the coordinates for picking.
- Device alignment before picking is highly recommended.
- A vacuum force greater than 7 psi must be avoided.
- 1 kPa = 0.145 psi (lb/in²) = 0.0102 kgf/cm² = 0.0098 atm
- MSL (moisture sensitivity level) Class 3
- Maximum of 3 reflow cycles is recommended.
- All recommended dimensions (device safe-picking area) do not include the pick-andplace equipment tolerances.

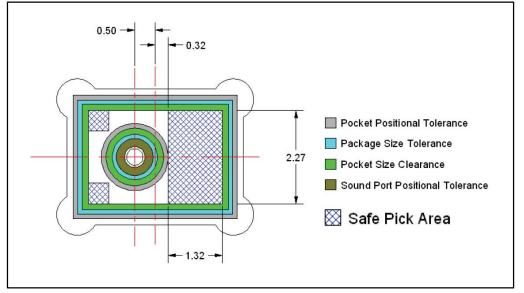


Figure 8: Recommended picking area

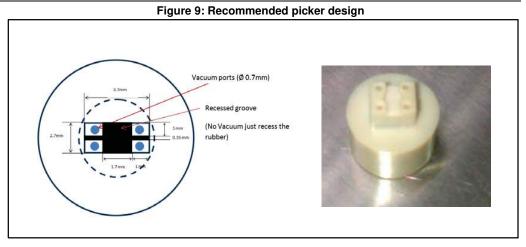
To have a safe pick-up "by design", ST strongly advises an ad hoc nozzle.

The following picker ensures that the holes for the vacuum and the air stream are ALWAYS away from the porthole of the device (4 vacuum ports located at each corner of the device).

The recommended nozzle also has a recess, in the form of a cross, which guarantees that the porthole is always left at atmospheric pressure. By using the recommended nozzle, the membrane will not suffer any sudden air disturbances during the picking or placing of the devices in the tape and reel.



Process recommendations





6 Sensing element

The sensing element shall mean the acoustic sensor consisting of a conductive movable plate and a fixed plate placed in a tiny silicon chip. This sensor transduces the sound pressure into the changes of coupled capacity between those two plates.

Omron Corporation supplies this element for STMicroelectronics.



7 Absolute maximum ratings

Stresses above those listed as "absolute maximum ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device under these conditions is not implied. Exposure to maximum rating conditions for extended periods may affect device reliability.

Symbol	Ratings	Maximum value	Unit
Vdd	Supply voltage	-0.3 to 5	V
Vin	Input voltage on any control pin	-0.3 to Vdd +0.3	V
Tstg	Storage temperature range	-40 to +125	°C
		±2000 (HBM)	
ESD	Electrostatic discharge protection	±200 (MM)	V
		±750 (CBM)	
ESD	Product standard EN 55024:2010 - 3 air discharge	±15000	V

Table	7:	Absolute	maximum	ratings
Iable		Absolute	maximum	ratings

Ø

This device is sensitive to mechanical shock, improper handling can cause permanent damage to the part.

This device is sensitive to electrostatic discharge (ESD), improper handling can cause permanent damage to the part.



8 Functionality

8.1 L/R channel selection

The L/R digital pad lets the user select the DOUT signal pattern as shown in *Table 8: "L/R channel selection"*. The L/R pin must be connected to Vdd or GND.

T	able	8:	L/R	channel	selection
-		•••		01101101	0010011011

L/R	CLK low	CLK high	
GND	Data valid	High impedance	
Vdd	High impedance	Data valid	



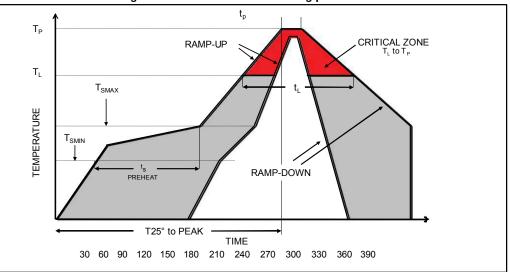
9 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK[®] is an ST trademark.

9.1 Soldering information

The HCLGA (3 x 4) 4LD package is also compliant with the RoHS and "Green" standards and is qualified for soldering heat resistance according to JEDEC J-STD-020.

Land pattern and soldering recommendations are available at www.st.com.



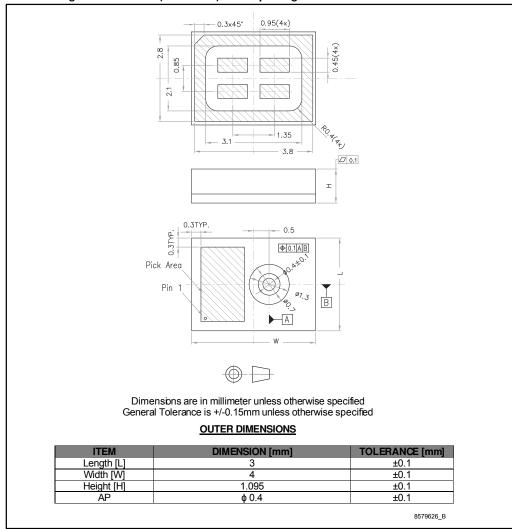


Description	Parameter	Pb free
Average ramp rate	T∟ to T _P	3 °C/sec max
Preheat Minimum temperature Maximum temperature Time (T _{SMIN} to T _{SMAX})	Tsmin Tsmax ts	150 °C 200 °C 60 sec to 120 sec
Ramp-up rate	T_{SMAX} to T_{L}	
Time maintained above liquids temperature Liquids temperature	tı. Tı	60 sec to 150 sec 217 °C
Peak temperature	TP	260 °C max
Time within 5 °C of actual peak temperature		20 sec to 40 sec
Ramp-down rate		6 °C/sec max
Time 25 °C (t25 °C) to peak temperature		8 minutes max



9.2 HCLGA package information

Figure 11: HCLGA (3x4x1.095) 4-lead package outline and mechanical data

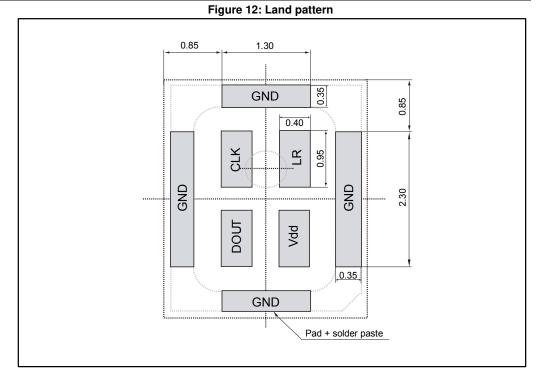


 The MEMS microphone plastic cap can exhibit some level of variation in color when the device is subjected to thermal processes. This variation does not affect acoustic or electrical performance.

2. Ring plating can be subject to change not affecting acoustic and electrical performance.



Package information





DocID029851 Rev 2

10 Revision history

Table 10: Document revision history	Table 10:	Document	revision	history
-------------------------------------	-----------	----------	----------	---------

Date	Version	Changes
04-Nov-2016	1	Initial release
05-Dec-2016	2	Updated current consumption in normal mode in <i>Table 3: "Acoustic and electrical characteristics"</i>



MP34DT04-C1

IMPORTANT NOTICE - PLEASE READ CAREFULLY

STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice. Purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST's terms and conditions of sale in place at the time of order acknowledgement.

Purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of Purchasers' products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

ST and the ST logo are trademarks of ST. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2016 STMicroelectronics - All rights reserved

