

μPA2739T1A

P-channel MOSFET

 $-30 \text{ V}, -85 \text{ A}, 2.8 \text{ m}\Omega$

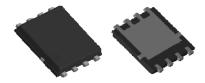
R07DS0885EJ0102 Rev.1.02 Nov 28, 2012

Description

The μ PA2739T1A is P-channel MOS Field Effect Transistors designed for high current switching applications.

Features

- $V_{DSS} = -30 \text{ V } (T_A = 25^{\circ}\text{C})$
- Low on-state resistance
 - R_{DS(on)} = 2.8 mΩ MAX. (V_{GS} = -10 V, I_D = -46 A)
 - R_{DS(on)} = 5.7 mΩ MAX. (V_{GS} = -4.5 V, I_D = -23 A)
- 4.5 V Gate-drive available
- Thin type surface mount package with heat spreader
- Halogen free



8-pin HVSON(6051)

Ordering Information

Part No.	LEAD PLATING	PACKING	Package
μ PA2739T1A-E2-AY*1	Pure Sn	Tape 3000 p/reel	8-pin HVSON(6051)
			0.1 g TYP.

Note: *1. Pb-free (This product does not contain Pb in external electrode.)

Absolute Maximum Ratings ($T_A = 25$ °C)

Item	Symbol	Ratings	Unit
Drain to Source Voltage (V _{GS} = 0 V)	V_{DSS}	-30	V
Gate to Source Voltage (V _{DS} = 0 V)	V_{GSS}	∓20	V
Drain Current (DC) (T _C = 25°C)	I _{D(DC)}	∓85	Α
Drain Current (pulse) *1	I _{D(pulse)}	∓180	Α
Total Power Dissipation *2	P _{T1}	1.5	W
Total Power Dissipation (PW = 10 sec) *2	P _{T2}	4.6	W
Total Power Dissipation (T _C = 25°C)	P _{T3}	83	W
Channel Temperature	T _{ch}	150	°C
Storage Temperature	T _{stg}	−55 to +150	°C
Single Avalanche Current *3	I _{AS}	-40	Α
Single Avalanche Energy *3	E _{AS}	160	mJ

Thermal Resistance

Channel to Ambient Thermal Resistance *2 R_{th(ch-A)} 83.3 °C/W Channel to Ambient Thermal Resistance *2 R_{th(ch-C)} 1.5 °C/W

Notes: *1. PW \leq 10 μ s, Duty Cycle \leq 1%

*2. Mounted on a glass epoxy board of 25.4 mm x 25.4 mm x 0.8 mmt

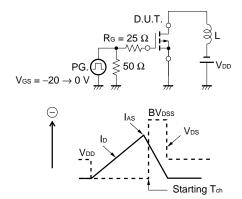
*3. Starting T_{ch} = 25°C, V_{DD} = –15 V, R_G = 25 Ω , V_{GS} = –20 \rightarrow 0 V, L = 100 μH

Electrical Characteristics (T_A = 25°C)

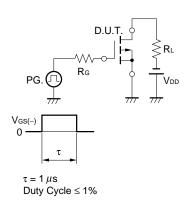
Item	Symbol	MIN.	TYP.	MAX.	Unit	Test Conditions
Zero Gate Voltage Drain Current	I _{DSS}			-1	μΑ	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$
Gate Leakage Current	I _{GSS}			∓100	nA	$V_{GS} = \mp 20 \text{ V}, V_{DS} = 0 \text{ V}$
Gate Cut-off Voltage	$V_{GS(off)}$	-1.0		-2.5	V	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$
Forward Transfer Admittance *1	y _{fs}	26			S	$V_{DS} = -10 \text{ V}, I_{D} = -23 \text{ A}$
Drain to Source On-state	R _{DS(on)1}		2.2	2.8	mΩ	$V_{GS} = -10 \text{ V}, I_D = -46 \text{ A}$
Resistance *1	R _{DS(on)2}		3.8	5.7	mΩ	$V_{GS} = -4.5 \text{ V}, I_D = -23 \text{ A}$
Input Capacitance	C _{iss}		6050		pF	$V_{DS} = -10 \text{ V},$
Output Capacitance	Coss		3000		pF	$V_{GS} = 0 V$,
Reverse Transfer Capacitance	C _{rss}		2420		pF	f = 1 MHz
Turn-on Delay Time	t _{d(on)}		27		ns	$V_{DD} = -15 \text{ V}, I_D = -23 \text{ A},$
Rise Time	t _r		140		ns	$V_{GS} = -10 \text{ V},$
Turn-off Delay Time	$t_{d(off)}$		310		ns	$R_G = 10 \Omega$
Fall Time	t _f		490		ns	
Total Gate Charge	Q_G		153		nC	$V_{DD} = -15 \text{ V},$
Gate to Source Charge	Q_{GS}		17		nC	$V_{GS} = -10 \text{ V},$
Gate to Drain Charge	Q_{GD}		70		nC	I _D = -23 A
Body Diode Forward Voltage *1	$V_{F(S-D)}$		0.85	1.2	V	I _F = 46 A, V _{GS} = 0 V
Reverse Recovery Time	t _{rr}		450		ns	I _F = 50 A, V _{GS} = 0 V,
Reverse Recovery Charge	Q _{rr}		1200		nC	di/dt = 100 A/μs

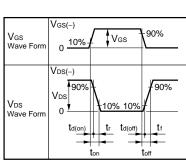
Note: *1. Pulsed

TEST CIRCUIT 1 AVALANCHE CAPABILITY



TEST CIRCUIT 2 SWITCHING TIME





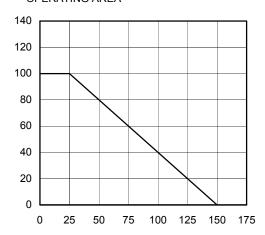
TEST CIRCUIT 3 GATE CHARGE

$$\begin{array}{c|c} D.U.T. \\ \hline \\ IG = -2 \text{ mA} \\ \hline \\ PG. \\ \hline \\ \end{array}$$

dT - Percentage of Rated Power - %

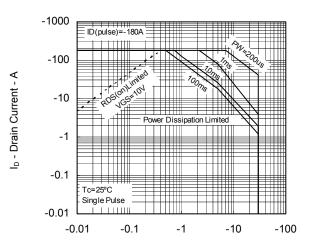
TYPICAL CHARACTERISTICS (T_A = 25°C)

DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



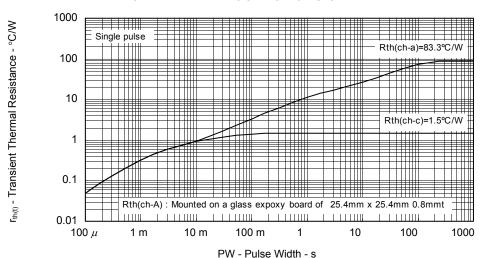
T_C - Case Temperature - °C

FORWARD BIAS SAFE OPERATING AREA

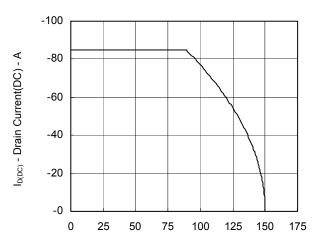


V_{DS} - Drain to Source Voltage - V

TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

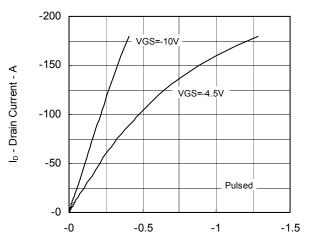


DRAIN CURRENT(DC) vs. CASE TEMPERATURE



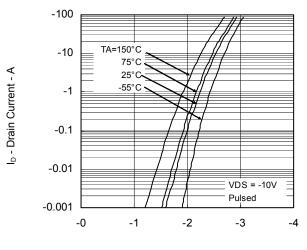
T_C - Case Temperature - °C

DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



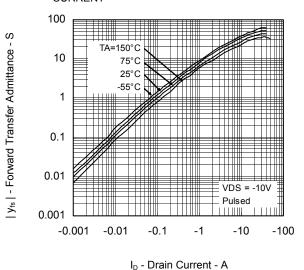
 $V_{\text{\scriptsize DS}}$ - Drain to Source Voltage - V

FORWARD TRANSFER CHARACTERISTICS

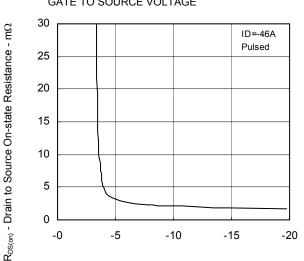


V_{GS} - Gate to Source Voltage - V

FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

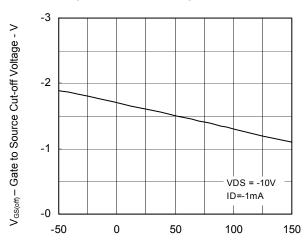


DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



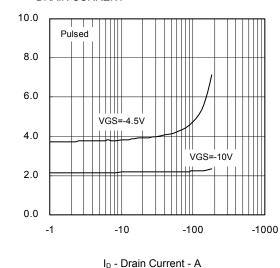
V_{GS} - Gate to Source Voltage - V

GATE TO SOURCE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE

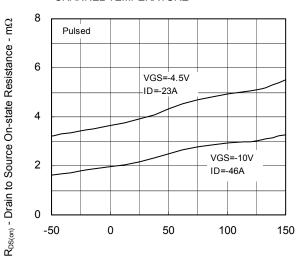


T_{ch} - Channel Temperature - C

DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



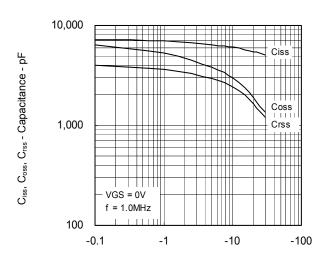
DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



T_{ch} - Channel Temperature - C

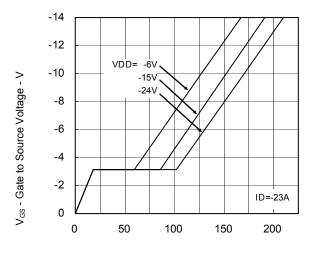
 $R_{\text{DS}(\text{on})}$ - Drain to Source On-state Resistance - $m\Omega$

CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



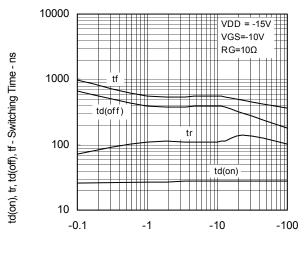
V_{DS} - Drain to Source Voltage - V

DYNAMIC INPUT CHARACTERISTICS



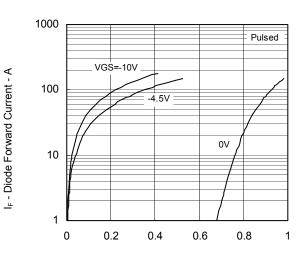
 $\ensuremath{\mathsf{Q}}_G$ - Gate Charge - nC

SWITCHING CHARACTERISTICS



ID - Drain Current - A

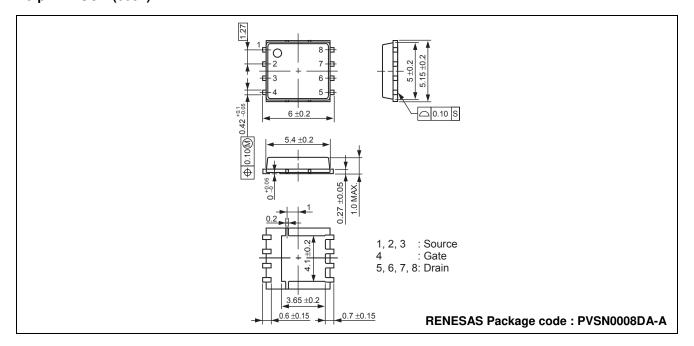
SOURCE TO DRAIN DIODE FORWARD VOLTAGE



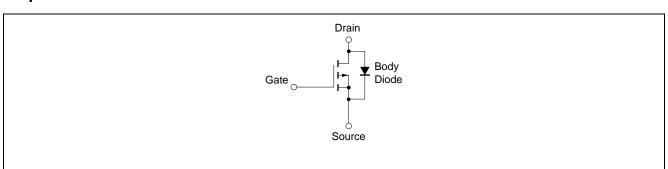
 $V_{\text{F(S-D)}}$ - Source to Drain Voltage - V

Package Drawings (Unit: mm)

8-pin HVSON (6051)



Equivalent Circuit



Remark Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

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