

TOSHIBA Photocoupler IRED & Photo-MOSFET

TLP170D

PBX

Modem · Fax Card

Telecommunication

Security Equipment

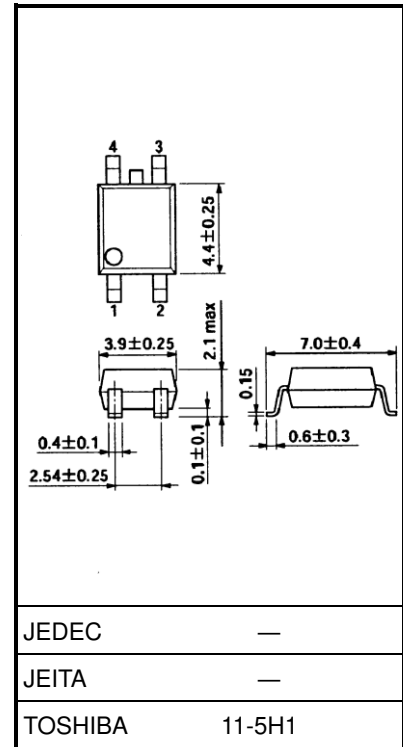
Measurement Equipment

The Toshiba TLP170D consists of an infrared emitting diode optically coupled to a photo-MOSFET in a 4-pin SOP package. This photorelay requires 1 mA of LED current to turn it on. It is suitable for applications that need electrical power saving.

- SOP 4 pin (2.54SOP4): 1-Form-A
- Peak off-state voltage: 200 V (min)
- Trigger LED current: 1 mA (max)
- ON-state current: 200 mA (max)
- ON-state resistance: 8 Ω (max)
- Isolation voltage: 1500 Vrms (min)
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A
File No.E67349
- VDE-approved: EN 60747-5-5 (Note 1)

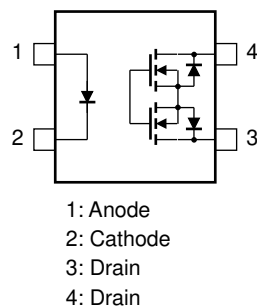
Note 1: When a VDE approved type is needed, please designate the **Option(V4)**.

Unit: mm

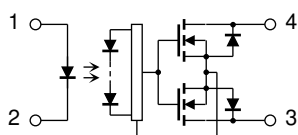


Weight: 0.1 g (typ.)

Pin Configuration (top view)



Internal Circuit



Start of commercial production
2009-06

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
LED	Forward current	I _F	50	mA
	Forward current derating (Ta ≥ 25°C)	ΔI _F /°C	-0.5	mA/°C
	Pulse forward current (100 μs pulse, 100 pps)	I _{FP}	1	A
	Reverse voltage	V _R	5	V
	Diode power dissipation	P _D	50	mW
	Diode power dissipation derating (Ta ≥ 25°C)	ΔP _D /°C	-0.5	mW/°C
	Junction temperature	T _j	125	°C
Detector	Off-state output terminal voltage	V _{OFF}	200	V
	On-state current	I _{ON}	200	mA
	On-state RMS current derating (Ta ≥ 25°C)	ΔI _{ON} /°C	-2.0	mA/°C
	Output power dissipation	P _C	300	mW
	Output power dissipation derating (Ta ≥ 25°C)	ΔP _C /°C	-3.0	mW/°C
	Junction temperature	T _j	125	°C
Storage temperature range		T _{stg}	-55 to 125	°C
Operating temperature range		T _{opr}	-40 to 85	°C
Lead soldering temperature (10 s)		T _{sol}	260	°C
Isolation voltage (AC, 60 s, R.H. ≤ 60 %) (Note 1)		BV _S	1500	V _{rms}

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note 1: Device considered a two-terminal device: pins 1 and 2 shorted together and pins 3 and 4 shorted together.

Recommended Operating Conditions

Characteristics	Symbol	Min	Typ.	Max	Unit
Supply voltage	V _{DD}	—	—	160	V
Forward current	I _F	—	2	25	mA
ON-state current	I _{ON}	—	—	160	mA
Operating temperature	T _{opr}	-20	—	65	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Individual Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
LED	Forward voltage	V_F	$I_F = 10 \text{ mA}$	1.0	1.15	1.3	V
	Reverse current	I_R	$V_R = 5 \text{ V}$	—	—	10	μA
	Capacitance	C_T	$V = 0 \text{ V}, f = 1 \text{ MHz}$	—	30	—	pF
Detector	OFF-state current	I_{OFF}	$V_{OFF} = 200 \text{ V}$	—	1	1000	nA
	Capacitance	C_{OFF}	$V = 0 \text{ V}, f = 1 \text{ MHz}$	—	90	—	pF

Coupled Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Trigger LED current	I_{FT}	$I_{ON} = 200 \text{ mA}$	—	0.4	1	mA
Return LED current	I_{FC}	$I_{OFF} = 100 \mu\text{A}$	0.1	—	—	mA
On-state resistance	R_{ON}	$I_{ON} = 200 \text{ mA}, I_F = 2 \text{ mA}$	—	5	8	Ω

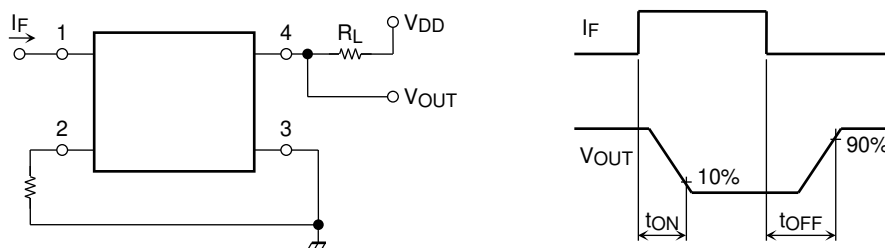
Isolation Characteristics (Ta = 25°C)

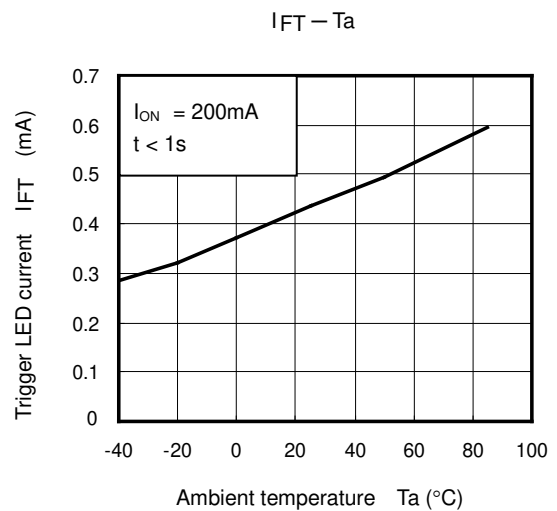
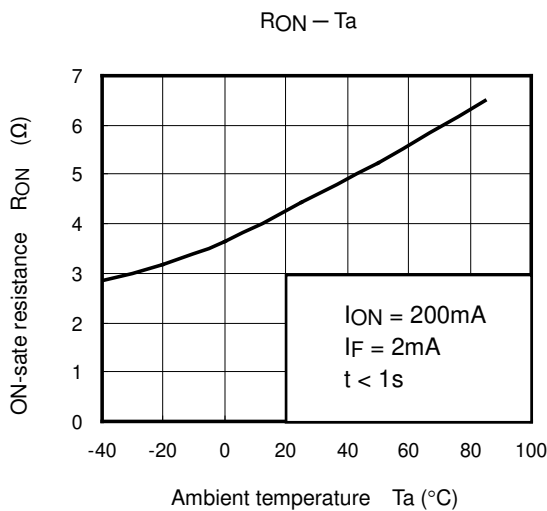
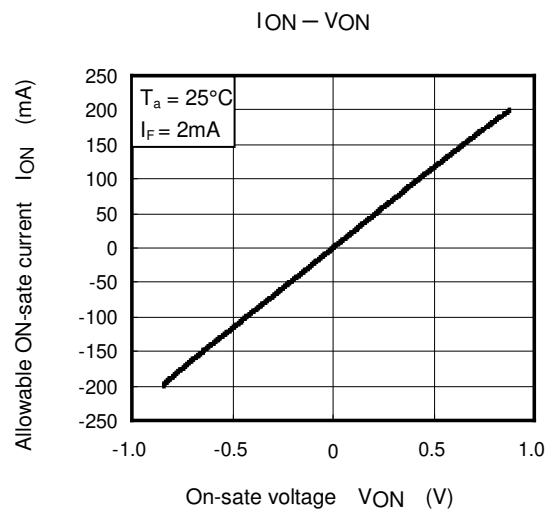
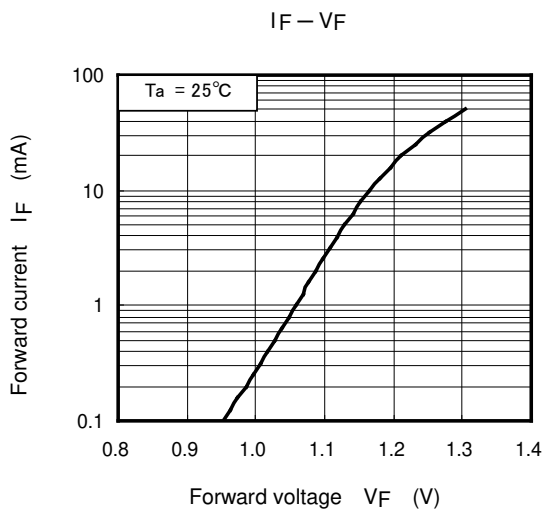
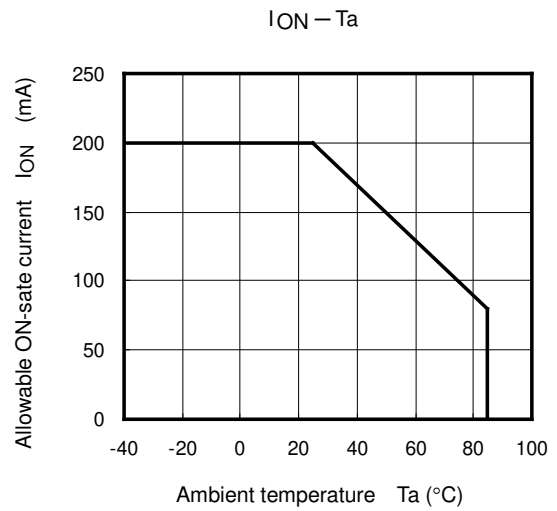
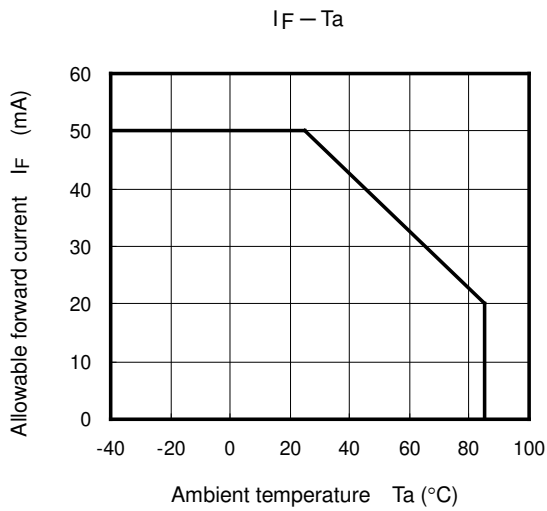
Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Capacitance input to output	C_S	$V_S = 0 \text{ V}, f = 1 \text{ MHz}$	—	0.8	—	pF
Isolation resistance	R_S	$V_S = 500 \text{ V}, \text{R.H.} \leq 60\%$	5×10^{10}	10^{14}	—	Ω
Isolation voltage	BV_S	AC, 60 s	1500	—	—	Vrms

Switching Characteristics (Ta = 25°C)

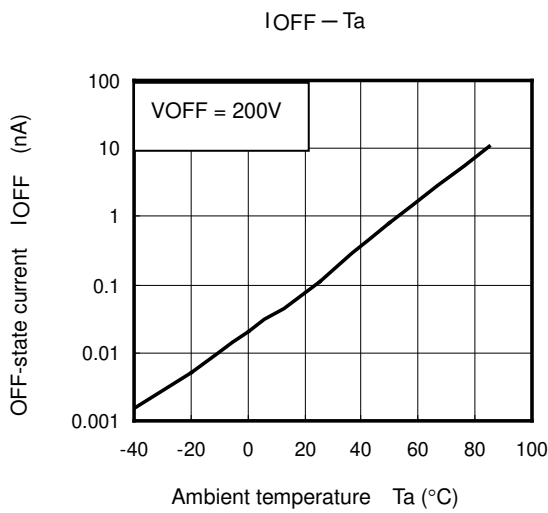
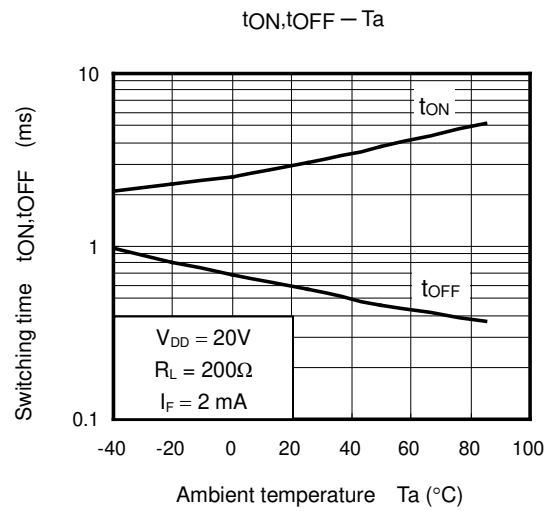
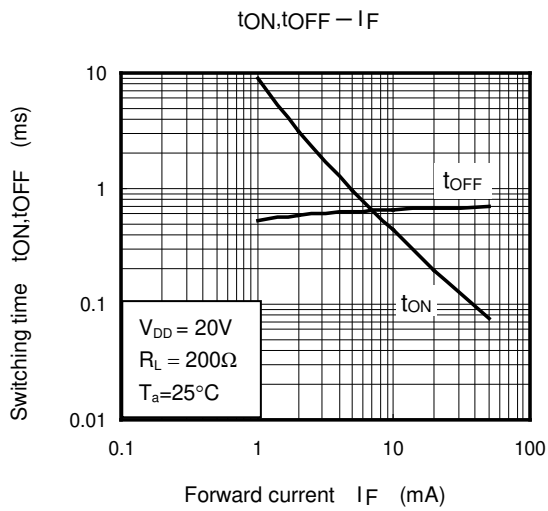
Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Turn-on time	t_{ON}	$R_L = 200 \Omega$ $V_{DD} = 20 \text{ V}, I_F = 2 \text{ mA}$ (Note2)	—	3.0	8.0	ms
Turn-on time	t_{ON}	$R_L = 200 \Omega$ $V_{DD} = 20 \text{ V}, I_F = 5 \text{ mA}$ (Note2)	—	—	5.0	ms
Turn-off time	t_{OFF}	$R_L = 200 \Omega$ $V_{DD} = 20 \text{ V}, I_F = 2 \text{ mA}$ (Note2)	—	0.6	3.0	ms

Note2: Switching time test circuit





NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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