

# Power manegement (Dual transistors)

Outline

SOT-363

SC-88

## <For Tr1>

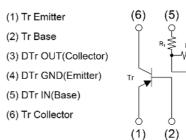
Parameter	Value
V <sub>CEO</sub>	-12V
I <sub>C</sub>	-500mA

## <For DTr2>

Parameter	Value
V <sub>CC</sub>	50V
I <sub>C(Max.)</sub>	100mA

# ● Features ● Inner circuit

- 1)Power switching circuit in a single package.
- 2) Mounting cost and area can be cut in half.



UMT6

# Application

Power manegement circuit

## Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
UMF5N	SOT-363 (UMT6)	2021	TR	180	8	3000	F5

# ● Absolute maximum ratings (T<sub>a</sub> = 25°C)

## <Tr1>

Parameter	Symbol	Limits	Unit
Collector-base voltage	$V_{CBO}$	-15	V
Collector-emitter voltage	$V_{CEO}$	-12	V
Emitter-base voltage	V <sub>EBO</sub>	-6	V
Calle atom or unworst	I <sub>C</sub>	-500	mA
Collector current	I <sub>CP</sub> *1	-1	Α

## <DTr2>

Parameter	Symbol	Limits	Unit
Supply voltage	V <sub>CC</sub>	50	V
Input voltage	V <sub>IN</sub>	-10 to 40	V
Output current	Io	30	mA
Collector current	I <sub>C(MAX)</sub>	100	mA

## <Tr1> <DTr2>

Parameter	Symbol	Limits	Unit
Power dissipation	P <sub>D</sub> *2*4	150	mW
Junction temperature	T <sub>j</sub>	150	°C
Range of storage temperature	T <sub>stg</sub>	-55 to 150	°C

# ● Electrical characteristics (T<sub>a</sub> = 25°C) <For Tr1>

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Collector-base breakdown voltage	BV <sub>CBO</sub>	I <sub>C</sub> = -10μA	-15	-	-	V
Collector-emitter breakdown voltage	BV <sub>CEO</sub>	I <sub>C</sub> = -1mA	-12	-	-	V
Emitter-base breakdown voltage	$BV_{EBO}$	I <sub>E</sub> = -10μA	-6	-	-	V
Collector cut-off current	I <sub>CBO</sub>	V <sub>CB</sub> = -15V	-	1	-100	nA
Emitter cut-off current	I <sub>EBO</sub>	V <sub>EB</sub> = -6V	-	1	-100	nA
Collector-emitter saturation voltage	$V_{CE(sat)}$	I <sub>C</sub> = -200mA, I <sub>B</sub> = -10mA	-	-100	-250	mV
DC current gain	h <sub>FE</sub>	$V_{CE} = -2V, I_{C} = -10mA$	270	-	680	-
Transition frequency	f <sub>T</sub>	V <sub>CE</sub> = -2V, I <sub>E</sub> = 10mA, f = 100MHz	-	260	-	MHz
Output capacitance	C <sub>ob</sub>	$V_{CB} = -10V, I_{E} = 0A,$ f = 1MHz	-	6.5	-	pF

## ● Electrical characteristics (Ta = 25°C) < For DTr2>

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
1 ( )	$V_{l(off)}$	$V_{CC} = 5V$ , $I_{O} = 100 \mu A$	-	-	0.5	V
Input voltage	V <sub>I(on)</sub>	V <sub>O</sub> = 0.3V, I <sub>O</sub> = 2mA	3	-	-	V
Output voltage	V <sub>O(on)</sub>	I <sub>O</sub> / I <sub>I</sub> = 10mA / 0.5mA	-	100	300	mV
Input current	I <sub>1</sub>	V <sub>I</sub> = 5V	-	-	180	μA
Output current	I <sub>O(off)</sub>	V <sub>CC</sub> = 50V, VI = 0V	-	-	500	nA
DC current gain	G <sub>l</sub>	V <sub>O</sub> = 5V, I <sub>O</sub> = 5mA	68	-	-	-
Input resistance	R <sub>1</sub>	-	32.9	47	61.1	kΩ
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	-	0.8	1.0	1.2	-
Transition frequency	f <sub>T</sub> *3	V <sub>CE</sub> = 10V, I <sub>E</sub> = -5mA, f = 100MHz		250	-	MHz

<sup>\*1</sup> Pw=1ms Single pulse

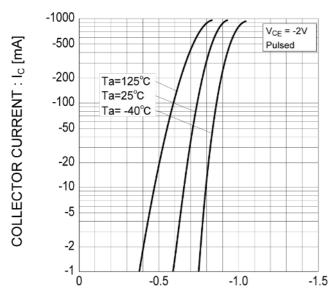
<sup>\*2</sup> Each termunal mounted on a reference land.

<sup>\*3</sup> Characteristics of built-in transistor.

<sup>\*4 120</sup>mW per element must not be exceeded.

## ● Electrical characteristic curves(T<sub>a</sub>=25°C) <For Tr1>

Fig.1 Ground Emitter Propagation Characteristics



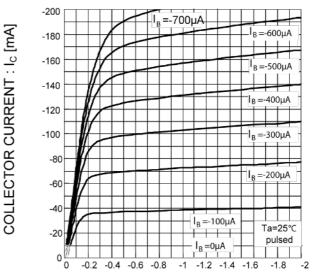


Fig.2 Typical Output Characteristics

BASE TO EMITTER VOLTAGE: VBE [V]

COLLECTOR TO EMITTER VOLTAGE: V<sub>CE</sub> [V]

Fig.3 DC Current Gain vs. Collector Current (I)

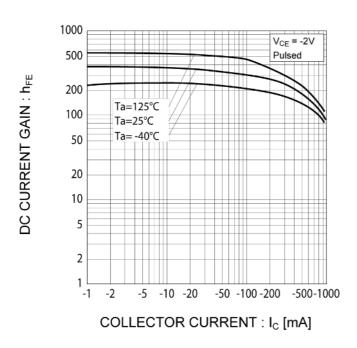
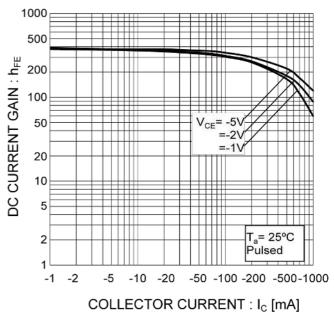


Fig.4 DC Current Gain vs. Collector Current (II)



# ● Electrical characteristic curves(T<sub>a</sub>=25°C) <For Tr1>

Fig.5 Collector-Emitter Saturation Voltage vs. Collector Current (I)

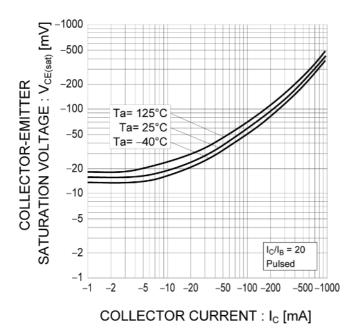
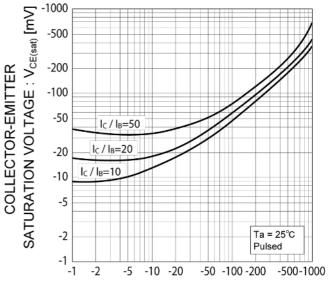


Fig.6 Collector-Emitter Saturation
Voltage vs. Collector Current (II)



COLLECTOR CURRENT: Ic [mA]

Fig.7 Base-Emitter Saturation Voltage vs. Collector Current

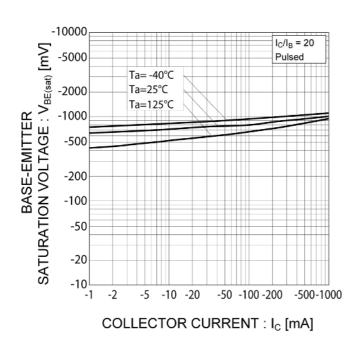
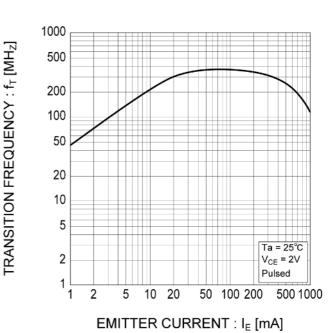


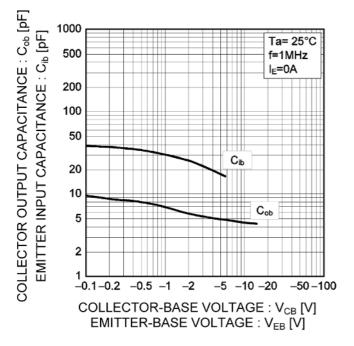
Fig.8 Gain Bandwidth Product vs. Emitter Current

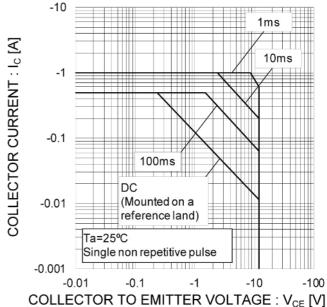


## ● Electrical characteristic curves(T<sub>a</sub>=25°C) <For Tr1>

Fig.9 Emitter Input Capacitance vs.
Emitter-Base Voltage
Collector Output Capacitance vs.
Collector-Base Voltage

Fig.10 Safe Operating Area





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## ● Electrical characteristic curves(T<sub>a</sub>=25°C) < For DTr2>

Fig.11 Input voltage vs. output current (ON characteristics)

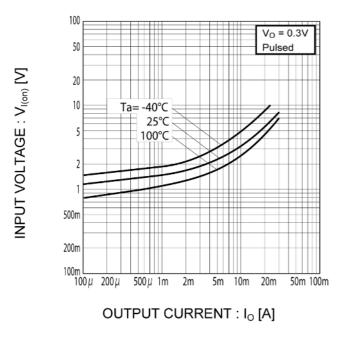


Fig.12 Output current vs. input voltage (OFF characteristics)

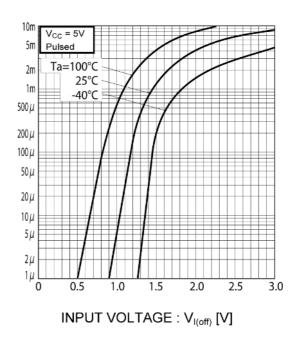


Fig.13 Output current vs. output voltage

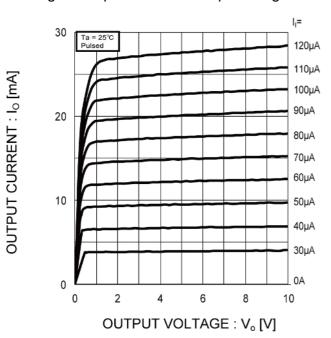
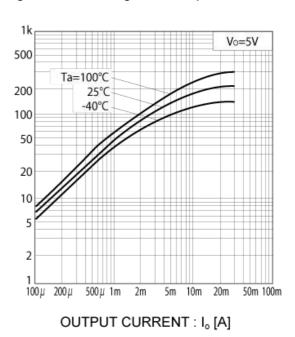


Fig.14 DC current gain vs. output current

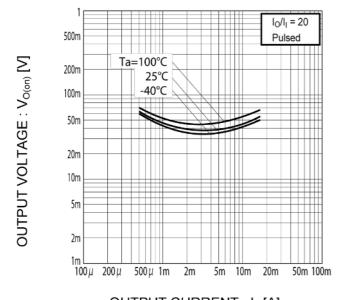


DC CURRENT GAIN: G

OUTPUT CURRENT : I<sub>o</sub> [A]

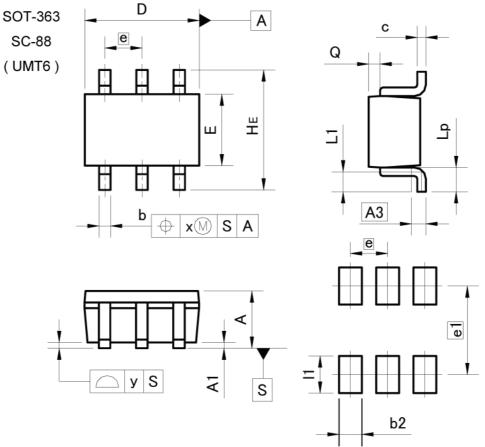
# ● Electrical characteristic curves(T<sub>a</sub>=25°C) < For DTr2>

Fig.15 Output voltage vs. output current



OUTPUT CURRENT : Io [A]

## ●外形寸法図



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM MILIME		ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	0.80	1.00	0.031	0.039
A1	0.00	0.10	0.000	0.004
A3	0.3	25	0.0	10
b	0.15	0.30	0.006	0.012
С	0.10	0.20	0.004	0.008
D	1.90	2.10	0.075	0.083
E	1.15	1.35	0.045	0.053
е	0.0	65	0.0	26
HE	2.00	2.20	0.079	0.087
L1	0.10	0.40	0.004	0.016
Lp	0.25	0.55	0.010	0.022
Q	0.10	0.30	0.004	0.012
х	=0	0.10	-	0.004
У	=8	0.10	24-5	0.004

DIM	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
b2	-0	0.40	9 <del></del>	0.016
e1	1.	55	0.0	061
11	<b>-</b> 2	0.65	1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 -	0.026

Dimension in mm/inches



Rev.003

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JAPAN	USA	EU	CHINA
CLASSⅢ	CL ACCIII	CLASS II b	CI ACCIII
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSIII

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  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
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- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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