TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ($L^2-\pi$ -MOSV)

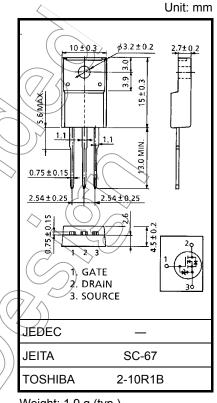
2SK2507

Chopper Regulator, DC–DC Converter and Motor Drive Applications

- 4-V gate drive
- Low drain-source ON resistance $: RDS (ON) = 0.034 \Omega$ (typ.)
- High forward transfer admittance $|Y_{fs}| = 16 \text{ S (typ.)}$
- Low leakage current $: IDSS = 100 \ \mu A \ (max) \ (VDS = 50 \ V)$
- Enhancement mode $: V_{th} = 0.8 \text{ to } 2.0 \text{ V} (V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	50	V
Drain-gate voltage (R _{GS} = 20 kΩ)		V _{DGR}	50	Ň
Gate-source voltage		V _{GSS}	±20	> v
Drain current	DC (Note 1)	I _D	25	А
	Pulse (Note 1)	I _{DP}	75	
Drain power dissipatio	n (Tc = 25°C)	PD	30	//w
Single pulse avalanche	e energy (Note 2)	E _{A\$}	138	mJ
Avalanche current		AR	25	A
Repetitive avalanche e	energy (Note 3)		3	Lm
Channel temperature	(Tch	150	°C
Storage temperature r	ange	T _{stg}	-55 to 150	°C



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics Symbol	Max	Unit
Thermal resistance, channel to case Rth (ch-c)	4.17	°C / W
Thermal resistance, channel to ambient Rth (ch-a)	62.5	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 25 V, T_{ch} = 25°C (initial), L = 272 µH, R_G = 25 Ω , I_{AR} = 25 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

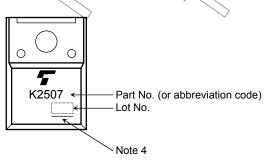
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I _{GSS}	V_{GS} = ±16 V, V_{DS} = 0 V	_	—	±10	μA
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 50 V, V _{GS} = 0 V	_		100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	50	_	—	V
Gate threshold v	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	0.8		2.0	V
Drain-source ON resistance		R _{DS (ON)}	V _{GS} = 4 V, I _D = 6 A	Æ	0.058	0.08	Ω
			V _{GS} = 10 V, I _D = 12 A	$\overline{2}$	0.034	0.046	52
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 12 A	8.0	16	—	S
Input capacitance		C _{iss}		_	900	—	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	~ <u> </u>	130		pF
Output capacitance		Coss		_	370	1	
Switching time	Rise time	tr	$V_{\rm GS} \stackrel{10\rm V}{}_{0\rm V} \prod \stackrel{I_{\rm D}}{\overset{I_{\rm D}}{=}} \stackrel{12\rm A}{\overset{\circ}{\sim}} V_{\rm out}$	- (75	×	
	Turn-on time	t _{on}	$\begin{array}{c} \begin{array}{c} \begin{array}{c} \begin{array}{c} \end{array} \\ \end{array} \\ \end{array} \\ \end{array} \\ \end{array} \\ \begin{array}{c} \end{array} \\ \end{array} \\ \end{array} \\ \end{array} \\ \begin{array}{c} \end{array} \\ \end{array} \\ \end{array} \\ \end{array} \\ \begin{array}{c} \end{array} \\ \end{array} \\ \end{array} \\ \end{array} \\ \begin{array}{c} \end{array} \\ \end{array} \\ \end{array} \\ \end{array} \\ \begin{array}{c} \end{array} \\ \begin{array}{c} \end{array} \\ \end{array} $	L (C	25) —	- ns
	Fall time	t _f	$\begin{array}{c} \downarrow \\ \uparrow \\ \neg \\ V_{DD} \doteq 30 V \end{array}$	$\overline{\mathcal{I}}$	30	_	
	Turn-off time	t _{off}	Duty $\leq 1\%$, t _w = 10 μ s) –	110	Ι	
Total gate charg plus gate−drain)		Qg		_	25	—	
Gate-source charge		Q _{gs}	V _{DD} ≈ 40 V, V _{GS} = 10 V, I _D = 25 A	—	19	—	nC
Gate-drain ("miller") charge		Qgđ		—	6	—	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)		_	_	_	25	A
Pulse drain reverse current (Note 1)	I _{DRP}	-			75	A
Forward voltage (diode)		I _{DR} = 25 A, V _{GS} = 0 V	_	_	-1.6	V
Reverse recovery time	trr	I _{DR} = 25 A, V _{GS} = 0 V, dI _{DR} / dt = 50 A / μs		60		ns
Reverse recovery charge	Qrr	10R = 23 A, vGS = 0 v, dIDR / dl = 50 A / B	_	45	_	μC

Marking

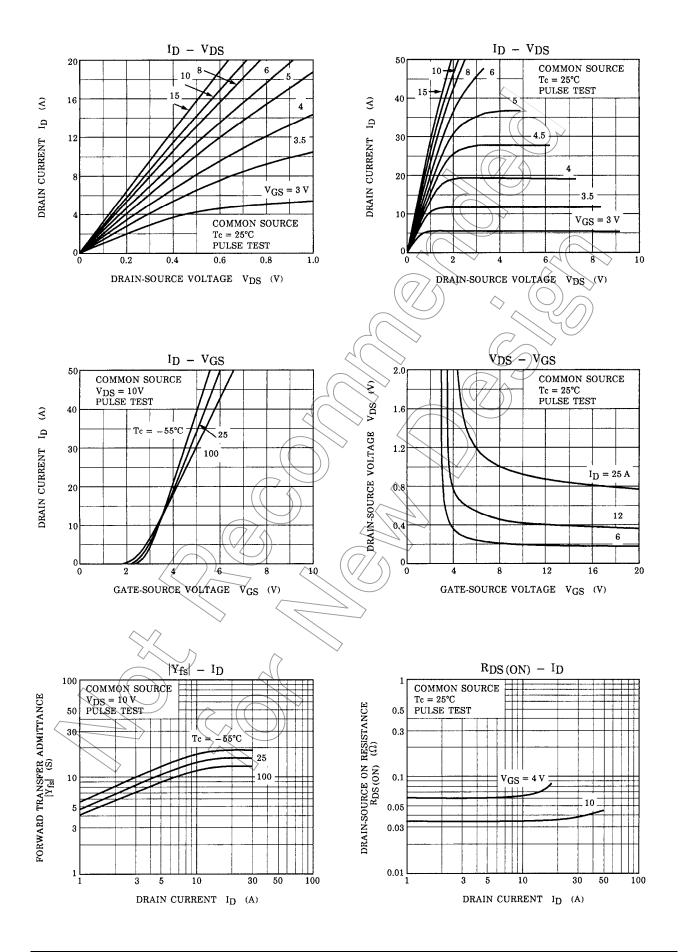


Note 4: A line under a Lot No. identifies the indication of product Labels. Not underlined: [[Pb]]/INCLUDES > MCV

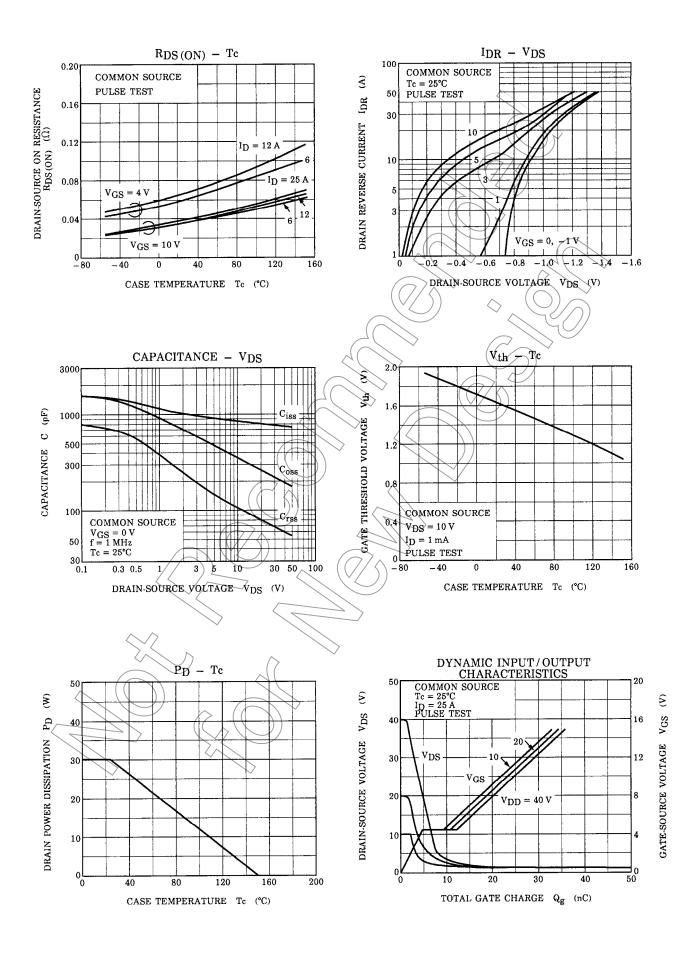
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

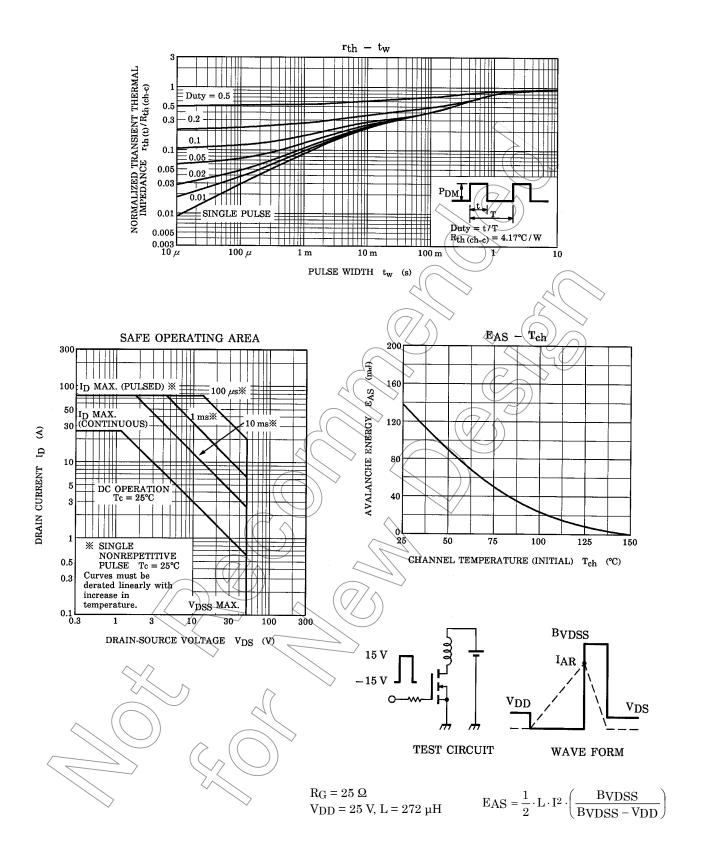
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