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FGA50T65SHD 650 V, 50 A Field Stop Trench IGBT

Features

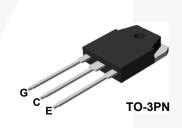
- Maximum Junction Temperature : T_J =175^oC
- · Positive Temperature Co-efficient for Easy Parallel Operating
- High Current Capability
- Low Saturation Voltage: $V_{CE(sat)}$ =1.6 V(Typ.) @ I_C = 50 A
- + 100% of the Parts Tested for $I_{\text{LM}}(1)$
- High Input Impedance
- Fast Switching
- Tighten Parameter Distribution
- · RoHS Compliant

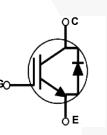
General Description

Using novel field stop IGBT technology, Fairchild's new series of field stop 3rd generation IGBTs offer the optimum performance for solar inverter, UPS, welder, telecom, ESS and PFC applications where low conduction and switching losses are essential.

Applications

• Solar Inverter, UPS, Welder, Telecom, ESS, PFC





Absolute Maximum Ratings T_c = 25°C unless otherwise noted

Symbol	Description		FGA50T65SHD	Unit
V _{CES}	Collector to Emitter Voltage		650	V
V _{GES}	Gate to Emitter Voltage		± 20	V
	Transient Gate to Emitter Voltage		± 30	V
I _C	Collector Current	@ T _C = 25°C	100	A
10	Collector Current	@ T _C = 100°C	50	A
I _{LM (1)}	Pulsed Collector Current @ $T_C = 25^{\circ}C$		150	A
I _{CM (2)}	Pulsed Collector Current		150	А
I _F	Diode Forward Current	@ T _C = 25°C	60	A
'F	Diode Forward Current	@ T _C = 100°C	30	А
I _{FM (2)}	Pulsed Diode Maximum Forward Curren	150	А	
P _D	Maximum Power Dissipation	@ T _C = 25°C	319	W
' D	Maximum Power Dissipation	@ T _C = 100°C	160	W
TJ	Operating Junction Temperature		-55 to +175	°C
T _{stg}	Storage Temperature Range		-55 to +175	°C
TL	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	°C

Notes:

1. V_{CC} = 400 V, V_{GE} = 15 V, I_C =150 A, R_G = 30 $\Omega,$ Inductive Load

2. Repetitive rating: Pulse width limited by max. junction temperature

April 2015

Thermal Characteristics

Symbol	Parameter	FGA50T65SHD	Unit
R _{0JC} (IGBT)	Thermal Resistance, Junction to Case, Max.	0.47	°C/W
$R_{\theta JC}$ (Diode)	Thermal Resistance, Junction to Case, Max.	1.25	°C/W
R _{θJA}	Thermal Resistance, Junction to Ambient, Max.	40	°C/W

Package Marking and Ordering Information

Pare Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FGA50T65SHD	FGA50T65SHD	TO-3PN	Tube	-	-	30

Electrical Characteristics of the IGBT T_C = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	teristics					
BV _{CES}	Collector to Emitter Breakdown Voltage	V _{GE} = 0V, I _C = 1 mA	650	-	-	V
ΔΒV _{CES} / ΔTJ	Temperature Coefficient of Breakdown Voltage	$I_{\rm C}$ = 1 mA, Reference to 25°C	-	0.6	-	V/ºC
I _{CES}	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0 V$	-	-	250	μA
I _{GES}	G-E Leakage Current	V _{GE} = V _{GES} , V _{CE} = 0 V	-	-	±400	nA
On Charac	teristics		-			
V _{GE(th)}	G-E Threshold Voltage	I _C = 50 mA, V _{CE} = V _{GE}	4.0	5.5	7.5	V
,		I _C = 50 A, V _{GE} = 15 V	-	1.6	2.1	V
V _{CE(sat)}	Collector to Emitter Saturation Voltage	$I_{C} = 50 \text{ A}, V_{GE} = 15 \text{ V},$ $T_{C} = 175^{\circ}\text{C}$	-	2.14	-	V
Dynamic C	characteristics		·			
C _{ies}	Input Capacitance		-	2516	-	pF
C _{oes}	Output Capacitance	V _{CE} = 30 V _, V _{GE} = 0 V, f = 1MHz	-	100	-	pF
C _{res}	Reverse Transfer Capacitance		-	31	-	pF
Switching	Characteristics					
t _{d(on)}	Turn-On Delay Time		-	22.4	-	ns
t _r	Rise Time	-	-	38.4	-	ns
t _{d(off)}	Turn-Off Delay Time	V _{CC} = 400 V, I _C = 50 A,	-	73.6	-	ns
t _f	Fall Time	$R_{G} = 6 \Omega, V_{GF} = 15 V,$	-	12.8	-	ns
Eon	Turn-On Switching Loss	Inductive Load, $T_C = 25^{\circ}C$	-	1280		uJ
E _{off}	Turn-Off Switching Loss		-	384	-	uJ
E _{ts}	Total Switching Loss	-	-	1664	-	uJ
t _{d(on)}	Turn-On Delay Time		-	20.8	-	ns
t _r	Rise Time		-	36.8	-	ns
t _{d(off)}	Turn-Off Delay Time	V _{CC} = 400 V, I _C = 50 A,	-	79.2	-	ns
t _f	Fall Time	R _G = 6 Ω, V _{GE} = 15 V,	-	11.2	-	ns
E _{on}	Turn-On Switching Loss	Inductive Load, T _C = 175°C	-	1920	-	uJ
				550		
E _{off}	Turn-Off Switching Loss		-	556	-	uJ

Electrical Characteristics of the IGBT (Continued)

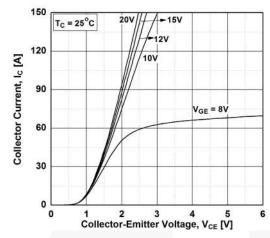
Symbol	Parameter	Test Conditions	Min.	Тур.	Max	Unit
Qg	Total Gate Charge	V _{CE} = 400 V, I _C = 50 A, V _{GE} = 15 V	-	87	-	nC
Q _{ge}	Gate to Emitter Charge		-	15.7	-	nC
Q _{gc}	Gate to Collector Charge		-	33.6	-	nC

Electrical Characteristics of the Diode T_C = 25°C unless otherwise noted

Symbol	Parameter		Test Conditions			Min.	Тур.	Мах	Unit
V _{FM}	Diode Forward Voltage	I- =	30 A	T _C =	25°C	-	2.3	2.7	V
[•] ⊢M	blode i erward verlage	'F	50 A	T _C =	175ºC	-	1.9	-	
E _{rec}	Reverse Recovery Energy			T _C =	175°C	-	50	-	uJ
t _{rr}	Diode Reverse Recovery Time	I _F =30 A, dI _F /dt = 200 A/μs	T _C =	25°C		34.6	-	ns	
			T _C =	175°C	-	197	-		
Q _{rr}	Diode Reverse Recovery Charge	1			25°C	-	58.6	-	nC
αn				T _C =	175°C	-	810	-	

Typical Performance Characteristics

Figure 1. Typical Output Characteristics





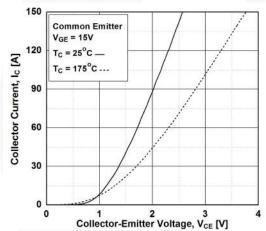


Figure 5. Saturation Voltage vs. V_{GE}

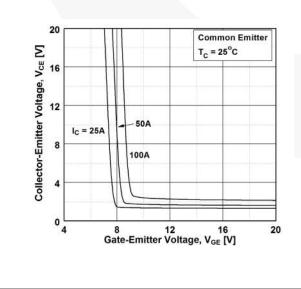
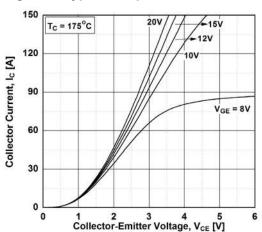
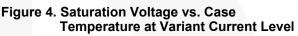


Figure 2. Typical Output Characteristics





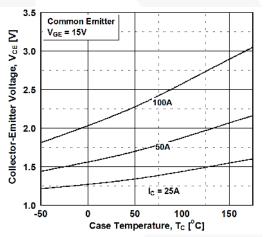
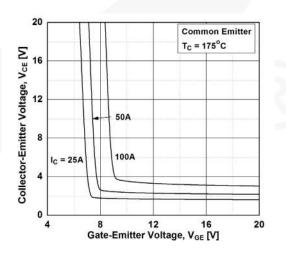


Figure 6. Saturation Voltage vs. V_{GE}



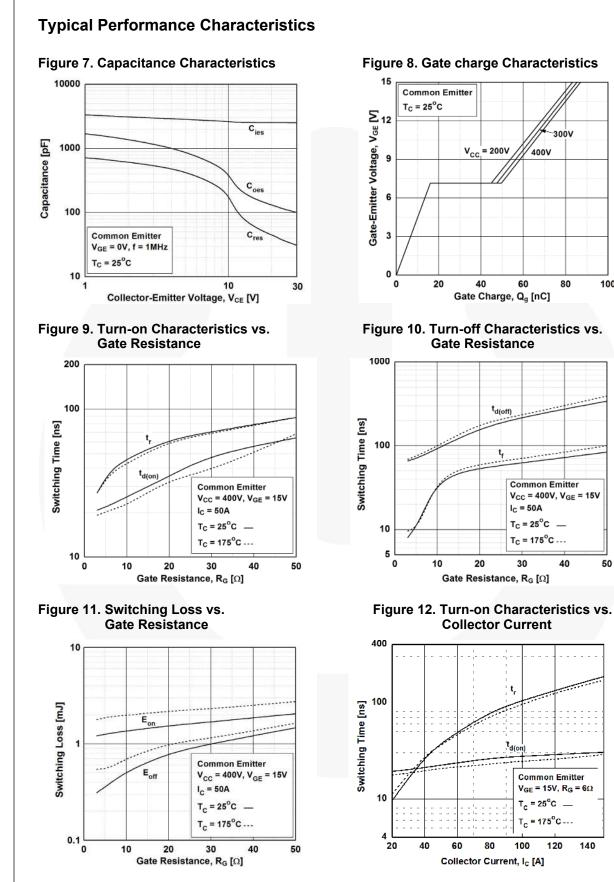


Figure 8. Gate charge Characteristics

-300γ

80

40

= 25°C

175°C ----

120

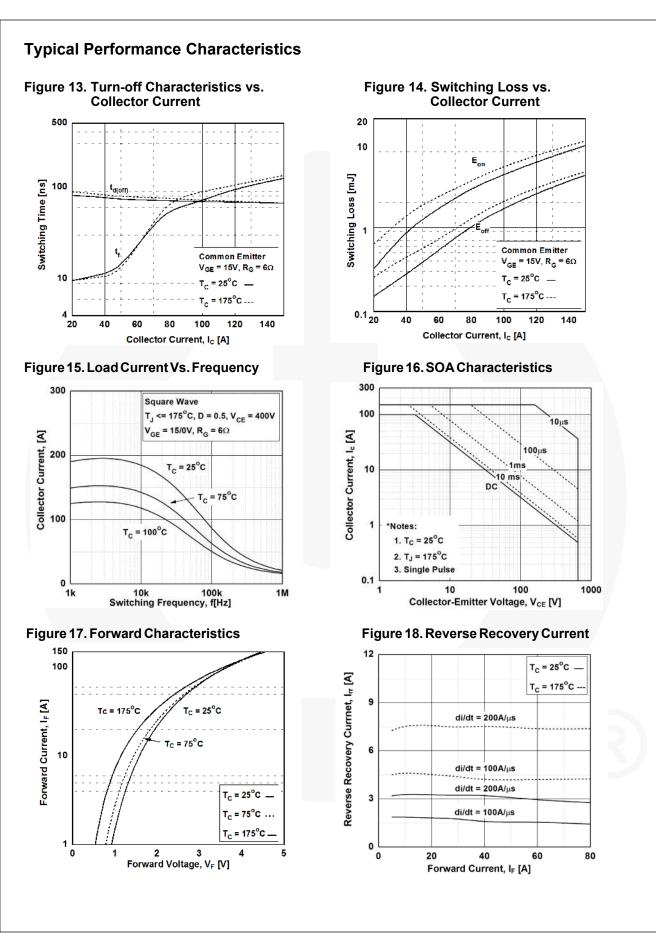
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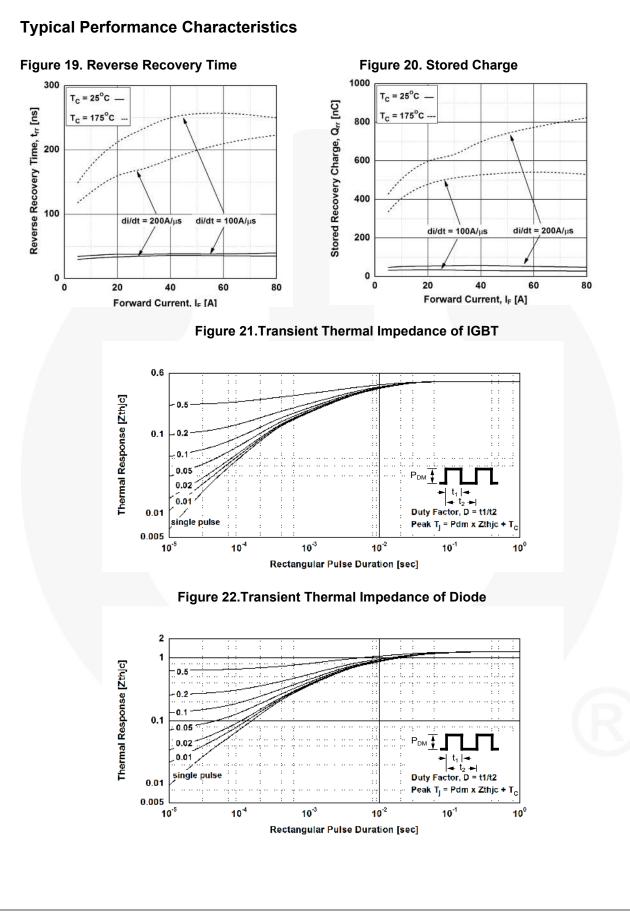
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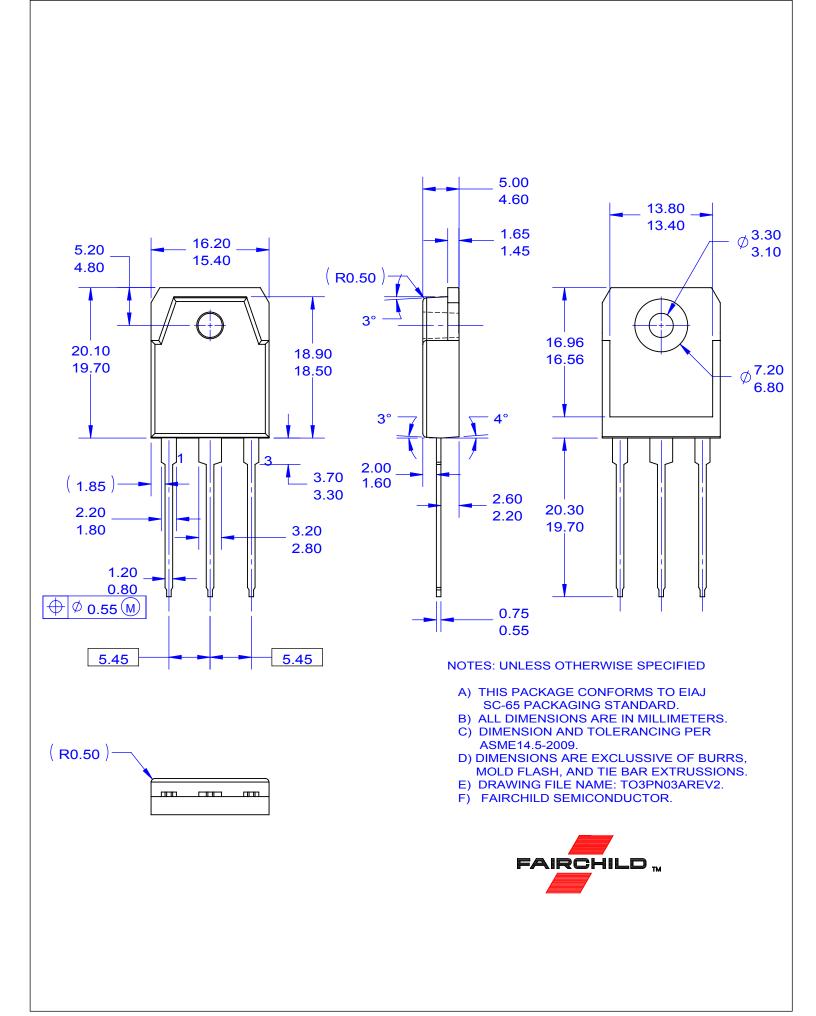
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400V

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