



DMN2040UVT

Product Summary

BV _{DSS}	RDS(ON) Max	I _D T _A = +25°C
20V	$24m\Omega @ V_{GS} = 4.5V$	6.7A
200	$32m\Omega @ V_{GS} = 2.5V$	5.8A

Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- **DC-DC Converters**
- **Power Management Functions**
- Backlighting

N-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Low Input Capacitance
- Low On-Resistance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

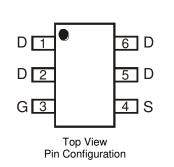
Mechanical Data

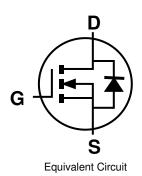
- Case: TSOT26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Tin Finish Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.013 grams (Approximate)



TSOT26

Top View





Ordering Information (Note 4)

	Part Number	Case	Packaging		
	DMN2040UVT-7	TSOT26	3,000/Tape & Reel		
	DMN2040UVT-13 TSOT26 10,000/Tape & Reel				
Notes:	Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.				

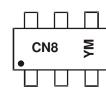
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



CN8 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: F = 2018) M = Month (ex: 9 = September)

Date Code Key

Duie Coue ney												
Year	201	8	2019		2020	20)21	2022		2023	1	2024
Code	F		G		Н			J		K		L
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage		V _{DSS}	20	V	
Gate-Source Voltage		V _{GSS}	±8	V	
Continuous Drain Current (Note 6) V_{GS} = 4.5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +75^{\circ}C$	ID	6.7 5.3	А
Maximum Body Diode Forward Current (Note 6)	•	I _S	1.2	А	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	40	А

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)		PD	1.2	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{ ext{ heta}JA}$	104	°C/W
Total Power Dissipation (Note 6)		PD	1.6	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{ ext{ heta}JA}$	77	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.) Characteristic Symbol Min Max Unit Test Condition Тур **OFF CHARACTERISTICS (Note 7)** Drain-Source Breakdown Voltage **BV**_{DSS} 20 V $V_{GS} = 0V, I_D = 250 \mu A$ Zero Gate Voltage Drain Current μA 1 $V_{DS} = 16V, V_{GS} = 0V$ IDSS Gate-Source Leakage ±100 nA $V_{GS}=\pm 8V,\,V_{DS}=0V$ IGSS ON CHARACTERISTICS (Note 7) V $V_{DS} = V_{GS}, I_D = 250 \mu A$ 0.4 1.5 Gate Threshold Voltage V_{GS(TH)} 18 24 $V_{GS} = 4.5V, I_D = 6.2A$ Static Drain-Source On-Resistance mΩ R_{DS(ON)} 24 32 V_{GS} = 2.5V, I_D = 5.2A Diode Forward Voltage V_{SD} 0.7 1.2 V $V_{GS} = 0V, I_{S} = 1.3A$ _ DYNAMIC CHARACTERISTICS (Note 8) 667 Input Capacitance Ciss _ _ $V_{DS} = 10V, V_{GS} = 0V,$ Output Capacitance Coss ____ 91 ___ pF f = 1.0MHzReverse Transfer Capacitance 83 Crss $V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$ Gate Resistance 1.2 Ω Rg _ _ Total Gate Charge Qq ____ 7.5 ____ $V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$ Gate-Source Charge 0.8 nC $V_{GS} = 4.5V, V_{DS} = 10V,$ Qgs ____ ____ Gate-Drain Charge 2.5 $I_{D} = 8.2A$ Qgd Turn-On Delay Time 3.9 _ _ tD(ON) Turn-On Rise Time ____ 5.1 _ $V_{DD} = 10V, V_{GS} = 4.5V,$ t_R ns Turn-Off Delay Time 21 _ $R_L = 10\Omega, R_q = 6\Omega$ tD(OFF) _ Turn-Off Fall Time 9.4 tF

Notes: 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.

6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate.

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t_{RR}

QRR

7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to production testing.

Reverse Recovery Time

Reverse Recovery Charge

12

3.4

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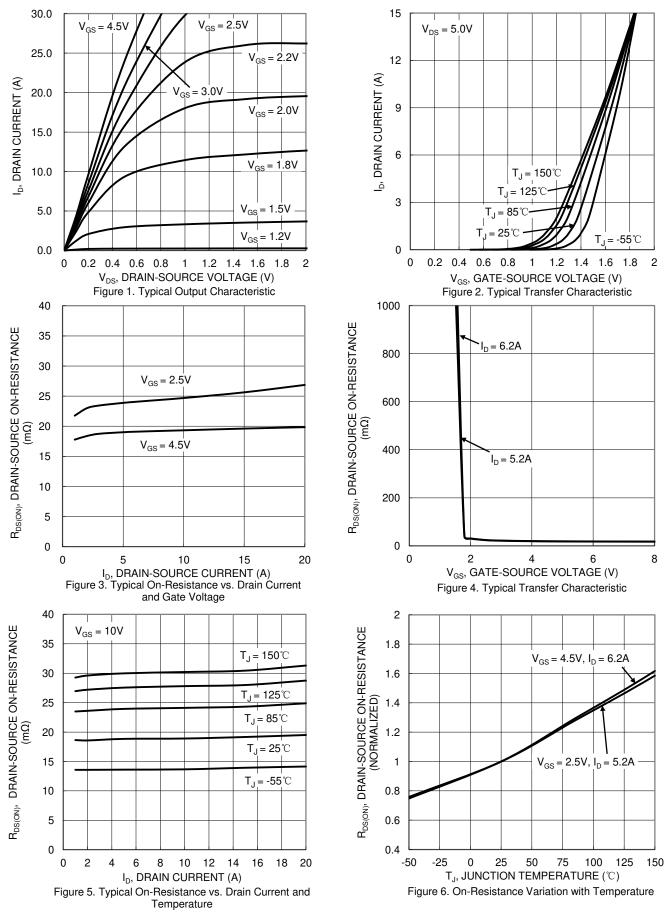
ns

nC

I_F = 5.0A, di/dt = 100A/µs

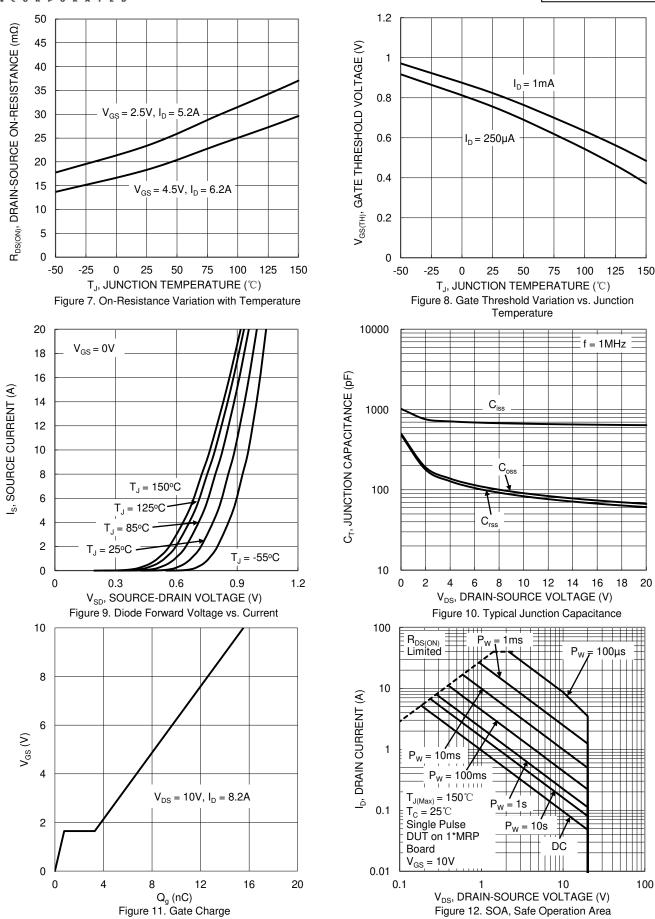


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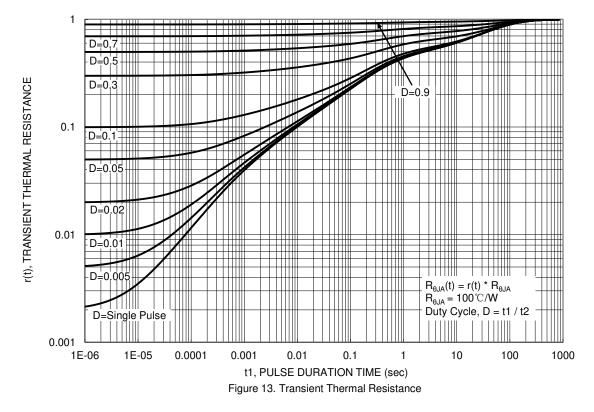


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DMN2040UVT Document number: DS40946 Rev. 2 - 2



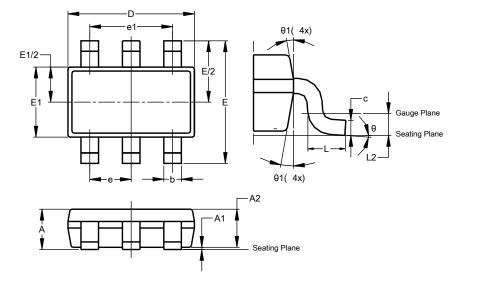




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

TSOT26



TSOT26						
Dim	Min Max Typ					
Α	-	1.00	-			
A1	0.010	0.100	-			
A2	0.840	0.900	-			
D	2.800	3.000	2.900			
ш	2	.800 BS	С			
E1	1.500 1.700 1.600					
b	0.300 0.450		-			
С	0.120	0.200	-			
е	0.950 BSC					
e1	1	1.900 BSC				
L	0.30 0.50 -					
L2	0.250 BSC					
θ	0° 8° 4°		4°			
θ1	4°	12°	_			
A	All Dimensions in mm					

Suggested Pad Layout

Y1

Please see http://www.diodes.com/package-outlines.html for the latest version.

Х

TSOT26

Dimensions	Value (in mm)
С	0.950
Х	0.700
Y	1.000
Y1	3.199



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