

BSN20BK 60 V, N-channel Trench MOSFET 18 December 2014

Product data sheet

1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Logic-level compatible
- Very fast switching
- Trench MOSFET technology
- ElectroStatic Discharge (ESD) protection: 2 kV HBM

3. Applications

- Relay driver
- High-speed line driver
- Low-side loadswitch
- Switching circuits

4. Quick reference data

Table 1. Qui	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage $T_j = 25 \text{ °C}$			-	-	60	V
V _{GS}	gate-source voltage			-20	-	20	V
ID	drain current	V _{GS} = 10 V; T _{amb} = 25 °C	[1]	-	-	265	mA
		V_{GS} = 10 V; T_{sp} = 25 °C		-	-	330	mA
Static characteristics							
R _{DSon}	drain-source on-state resistance	V_{GS} = 10 V; I _D = 200 mA; T _j = 25 °C		-	2.1	2.8	Ω

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 1 cm².



5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	3	D
2	S	source		
3	D	drain	1 2 TO-236AB (SOT23)	G G S S 017aaa255

6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
BSN20BK	TO-236AB	plastic surface-mounted package; 3 leads	SOT23			

7. Marking

Table 4. Marking codes	
Type number	Marking code
	[1]
BSN20BK	%4S

[1] % = placeholder for manufacturing site code

8. Limiting values

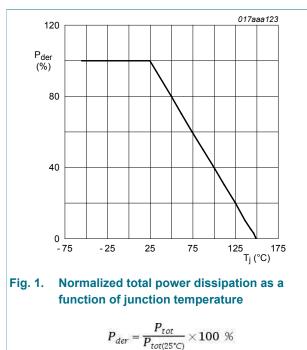
Table 5.Limiting values

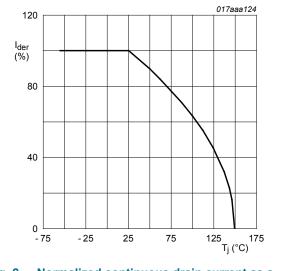
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	60	V
V _{GS}	gate-source voltage			-20	20	V
I _D	drain current	V _{GS} = 10 V; T _{amb} = 25 °C	[1]	-	265	mA
		V _{GS} = 10 V; T _{amb} = 100 °C	[1]	-	170	mA
		V _{GS} = 10 V; T _{sp} = 25 °C		-	330	mA
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	0.9	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	310	mW
			[1]	-	402	mW
		T _{sp} = 25 °C		-	1672	mW
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-dra	in diode					
ls	source current	T _{amb} = 25 °C	[1]	-	200	mA

Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 1 cm².
Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard for the standard

footprint.







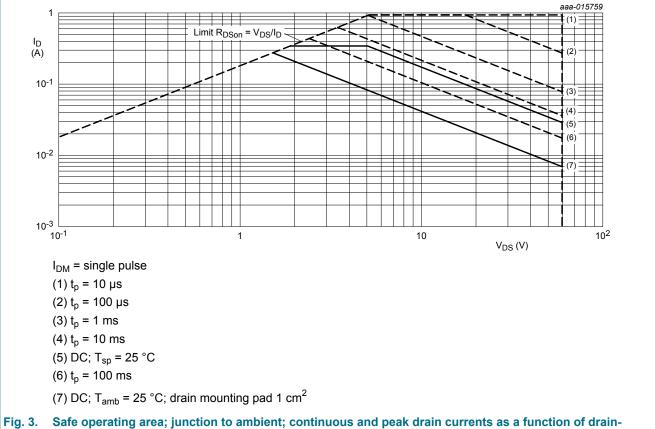
$$I_{der} = \frac{I_D}{I_{D(25^{\circ}C)}} \times 100 \%$$

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source voltage

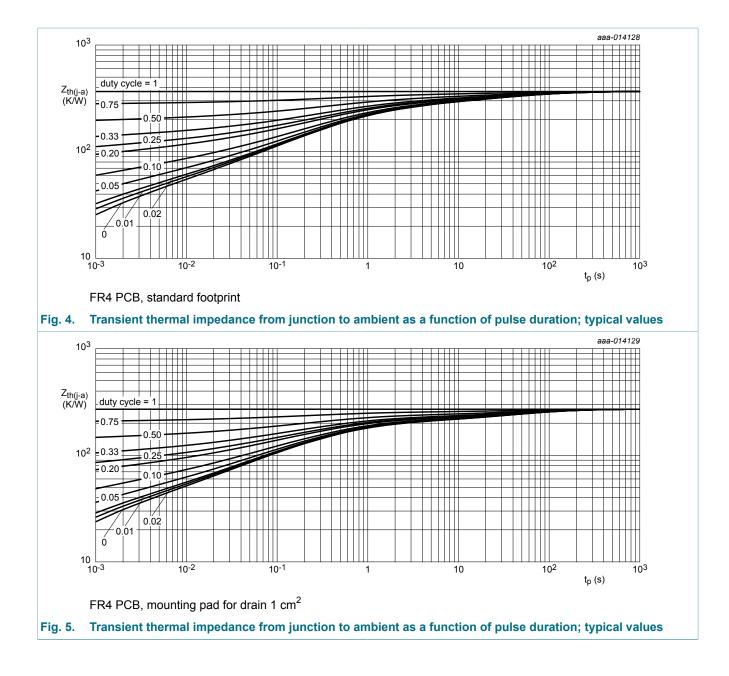
9. Thermal characteristics

Table 6. The	rmal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	351	404	K/W
			[2]	-	271	311	K/W
		t ≤ 5 s	[2]	-	210	241	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	65	75	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

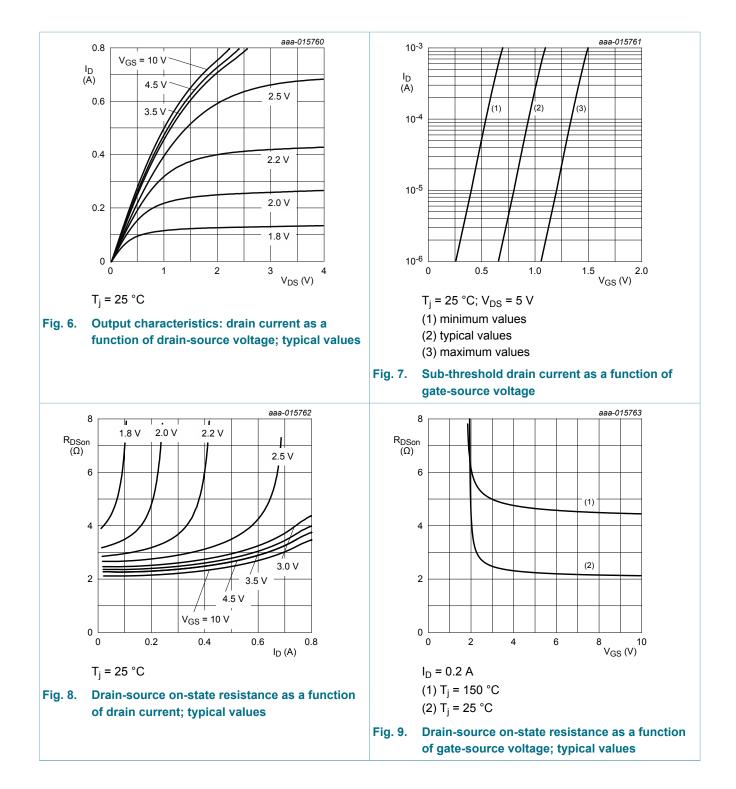
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and mounting pad for drain 1 cm².

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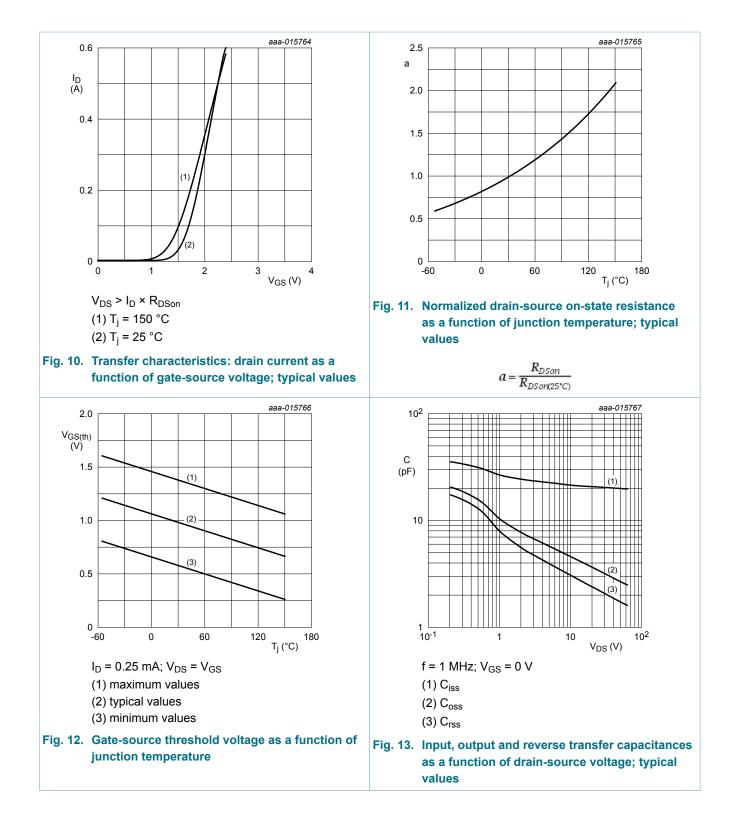
10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static char	racteristics		I I			
V _{(BR)DSS}	drain-source breakdown voltage	I_D = 250 µA; V_{GS} = 0 V; T_j = 25 °C	60	-	-	V
V _{GSth}	gate-source threshold voltage	I_D = 250 µA; V_{DS} = V_{GS} ; T_j = 25 °C	0.6	1	1.4	V
I _{DSS}	drain leakage current	V_{DS} = 60 V; V_{GS} = 0 V; T_j = 25 °C	-	-	1	μA
I _{GSS}	gate leakage current	V_{GS} = 20 V; V_{DS} = 0 V; T_j = 25 °C	-	-	10	μA
		V_{GS} = -20 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-10	μA
		V _{GS} = 10 V; V _{DS} = 0 V; T _j = 25 °C	-	-	1	μA
		V_{GS} = -10 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-1	μA
		$V_{GS} = 5 V; V_{DS} = 0 V; T_j = 25 °C$	-	-	0.3	μA
		V_{GS} = -5 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-0.3	μA
R _{DSon}	drain-source on-state	V_{GS} = 10 V; I _D = 200 mA; T _j = 25 °C	-	2.1	2.8	Ω
	resistance	V _{GS} = 10 V; I _D = 200 mA; T _j = 150 °C	-	4.3	5.7	Ω
		V _{GS} = 5 V; I _D = 200 mA; T _j = 25 °C	-	2.2	3.2	Ω
		V _{GS} = 2.5 V; I _D = 75 mA; T _j = 25 °C	-	2.6	4	Ω
9 _{fs}	forward transconductance	V_{DS} = 10 V; I _D = 200 mA; T _j = 25 °C	-	0.71	-	S
Dynamic c	haracteristics		I I	I		
Q _{G(tot)}	total gate charge	V_{DS} = 30 V; I _D = 200 mA; V _{GS} = 4.5 V;	-	0.49	-	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	0.12	-	nC
Q _{GD}	gate-drain charge	-	-	0.12	-	nC
C _{iss}	input capacitance	V_{DS} = 30 V; f = 1 MHz; V_{GS} = 0 V;	-	20.2	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	3.1	10	pF
C _{rss}	reverse transfer capacitance	-	-	2	7	pF
t _{d(on)}	turn-on delay time	V_{DS} = 30 V; I _D = 200 mA; V _{GS} = 4.5 V;	-	7.9	-	ns
t _r	rise time	R _{G(ext)} = 6 Ω; T _j = 25 °C	-	8.4	-	ns
t _{d(off)}	turn-off delay time		-	12.5	-	ns
t _f	fall time		-	5.1	-	ns
Source-dra	ain diode	1	II			
V _{SD}	source-drain voltage	I_{S} = 200 mA; V_{GS} = 0 V; T_{j} = 25 °C	-	0.86	1.2	V



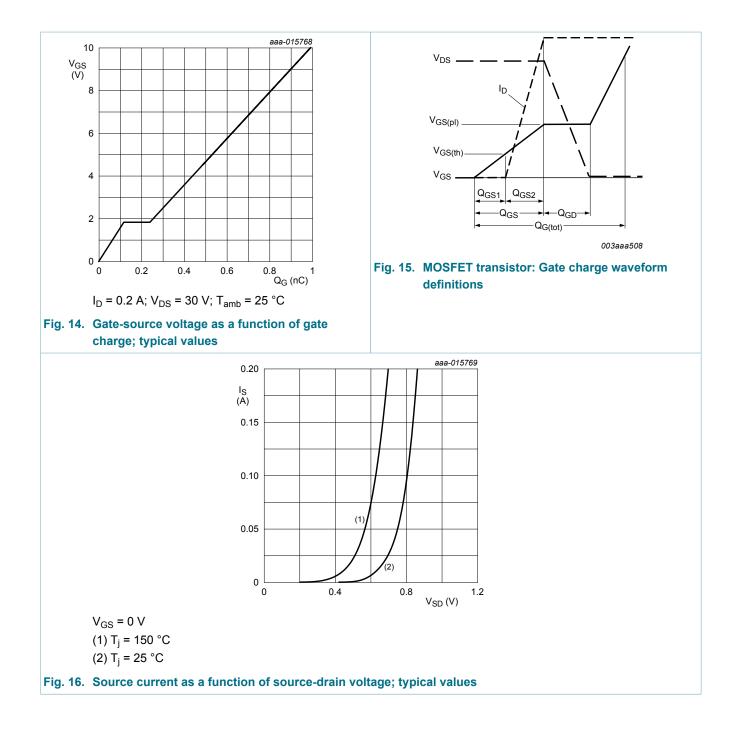
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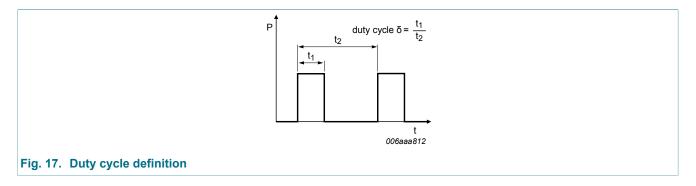


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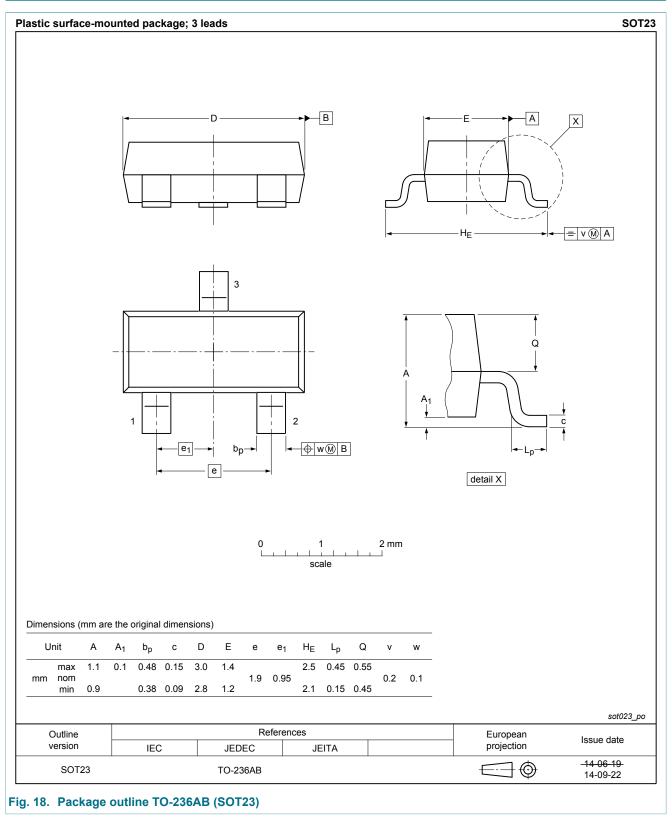
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11. Test information

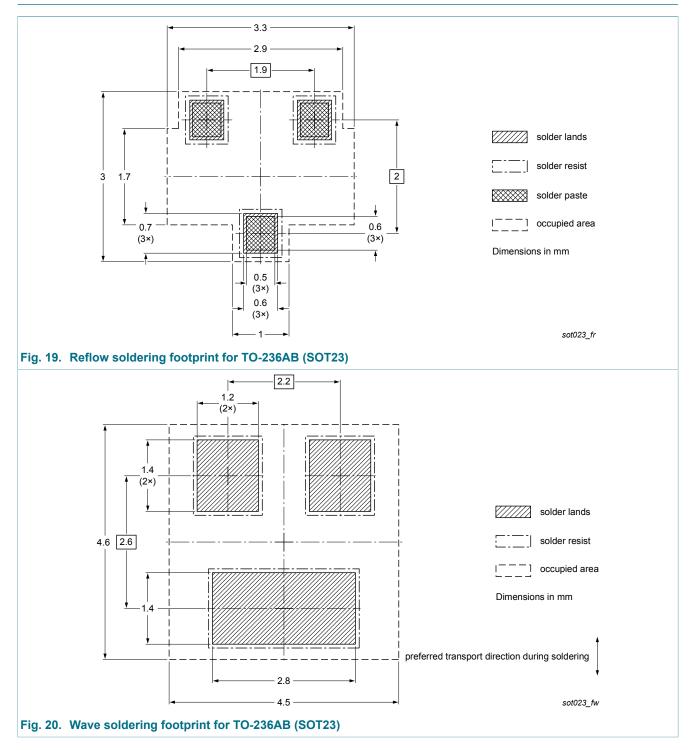


12. Package outline



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13. Soldering



14. Revision history

Table 8. Revision his	story			
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BSN20BK v.1	20141218	Product data sheet	-	-

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15. Legal information

15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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