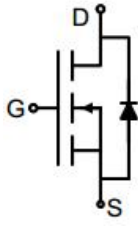
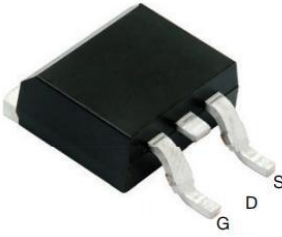


## N-Channel Enhancement Mode Power MOSFET

<p><b>Description</b></p> <p>The GC11N65M uses advanced trench technology to provide excellent <math>R_{DS(ON)}</math>, low gate charge. It can be used in a wide variety of applications.</p> <p><b>General Features</b></p> <ul style="list-style-type: none"> <li>● <math>V_{DS}</math> 650V</li> <li>● <math>I_D</math> (at <math>V_{GS} = 10V</math>) 11A</li> <li>● <math>R_{DS(ON)}</math> (at <math>V_{GS} = 10V</math>) &lt; 360mΩ</li> <li>● 100% Avalanche Tested</li> <li>● RoHS Compliant</li> </ul> <p><b>Application</b></p> <ul style="list-style-type: none"> <li>● Power switch</li> <li>● DC/DC converters</li> </ul>	 <p>Schematic diagram</p>  <p>TO-263</p>
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### Ordering Information

Device	Package	Marking	Packaging
GC11N65M	TO-263	GC11N65	800pcs/Reel

### Absolute Maximum Ratings $T_C = 25^\circ C$ , unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	650	V
Continuous Drain Current	$I_D$	11	A
Pulsed Drain Current (note1)	$I_{DM}$	44	A
Gate-Source Voltage	$V_{GS}$	$\pm 30$	V
Power Dissipation	$P_D$	78	W
Single pulse avalanche energy (note2)	$E_{AS}$	211	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 To 150	$^\circ C$

### Thermal Resistance

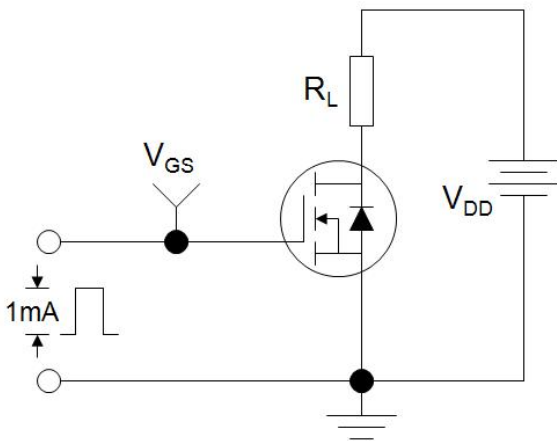
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	62	$^\circ C/W$
Maximum Junction-to-Case	$R_{thJC}$	1.6	$^\circ C/W$

Specifications $T_J = 25^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static Parameters</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	650	--	--	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 650V, V_{GS} = 0V$	--	--	1	$\mu A$
Gate-Source Leakage	$I_{GSS}$	$V_{GS} = \pm 30V$	--	--	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	2.5	3.5	4.0	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 5.5A$	--	320	360	m $\Omega$
Forward Transconductance	$g_{FS}$	$V_{GS} = 5V, I_D = 5.5A$	--	6	--	S
<b>Dynamic Parameters</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0V,$ $V_{DS} = 325V,$ $f = 1.0MHz$	--	768	--	pF
Output Capacitance	$C_{oss}$		--	19	--	
Reverse Transfer Capacitance	$C_{rss}$		--	0.3	--	
Total Gate Charge	$Q_g$	$V_{DD} = 325V,$ $I_D = 5.5A,$ $V_{GS} = 10V$	--	21	--	nC
Gate-Source Charge	$Q_{gs}$		--	4.5	--	
Gate-Drain Charge	$Q_{gd}$		--	7	--	
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = 325V,$ $I_D = 5.5A,$ $R_G = 25\Omega$	--	42	--	ns
Turn-on Rise Time	$t_r$		--	20	--	
Turn-off Delay Time	$t_{d(off)}$		--	122	--	
Turn-off Fall Time	$t_f$		--	6	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$	$T_C = 25^\circ\text{C}$	--	--	11	A
Body Diode Voltage	$V_{SD}$	$T_J = 25^\circ\text{C}, I_{SD} = 5.5A, V_{GS} = 0V$	--	--	1.2	V
Reverse Recovery Charge	$Q_{rr}$	$I_F = 5.5A, V_{GS} = 0V$ $di/dt = 100A/\mu s$	--	3	--	nC
Reverse Recovery Time	$T_{rr}$		--	280	--	ns

### Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. EAS condition :  $T_J = 25^\circ\text{C}, V_{DD} = 50V, V_{GS} = 10V, L = 0.5mH, R_G = 25\Omega$
3. Identical low side and high side switch with identical  $R_G$

### Gate Charge Test Circuit



### Switch Time Test Circuit

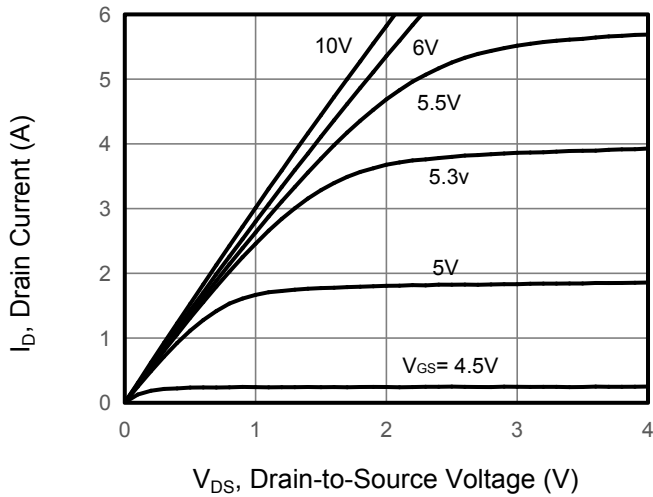


### EAS Test Circuit

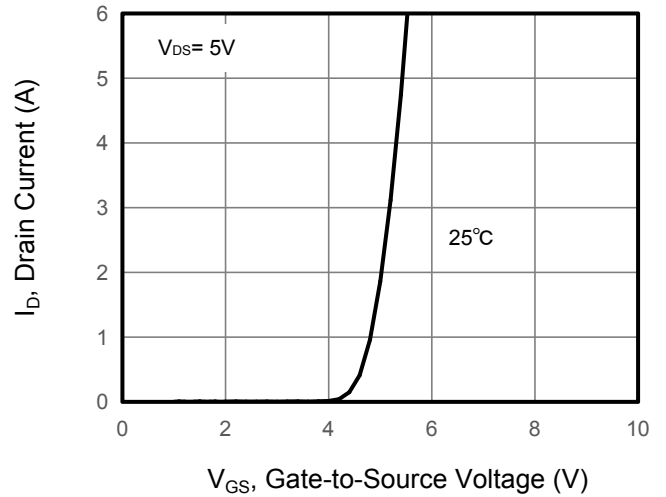


Typical Characteristics  $T_J = 25^\circ\text{C}$ , unless otherwise noted

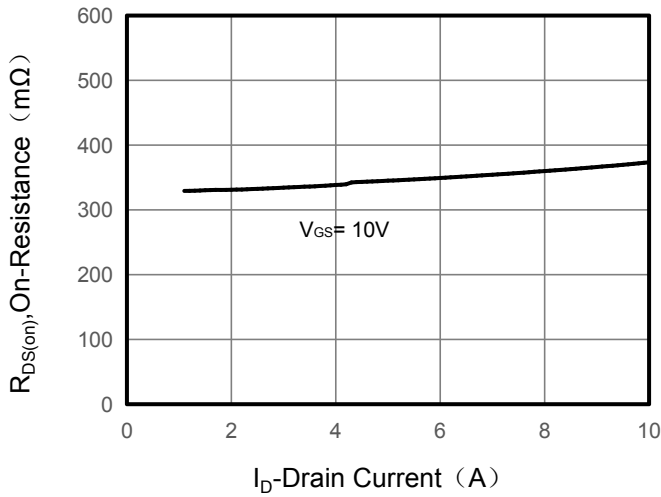
**Figure 1. Output Characteristics**



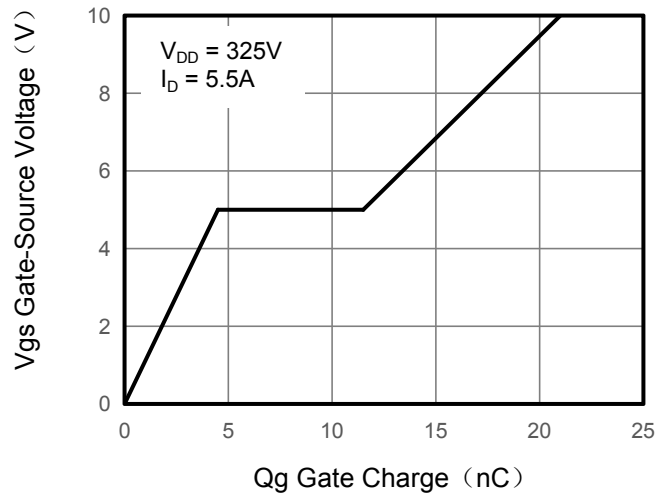
**Figure 2. Transfer Characteristics**



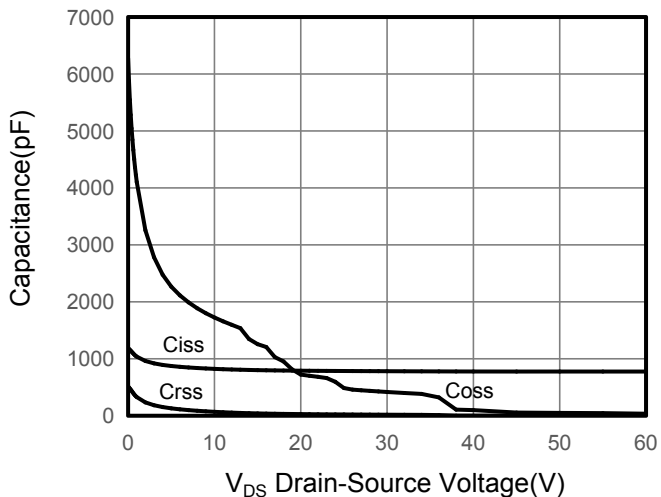
**Figure 3. Drain Source On Resistance**



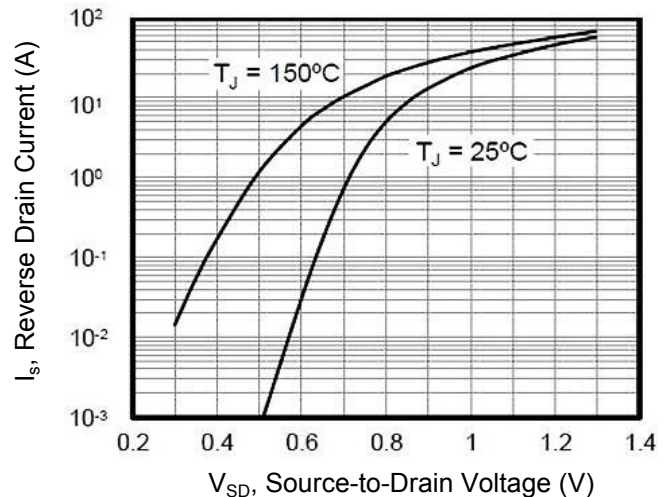
**Figure 4. Gate Charge**



**Figure 5. Capacitance**

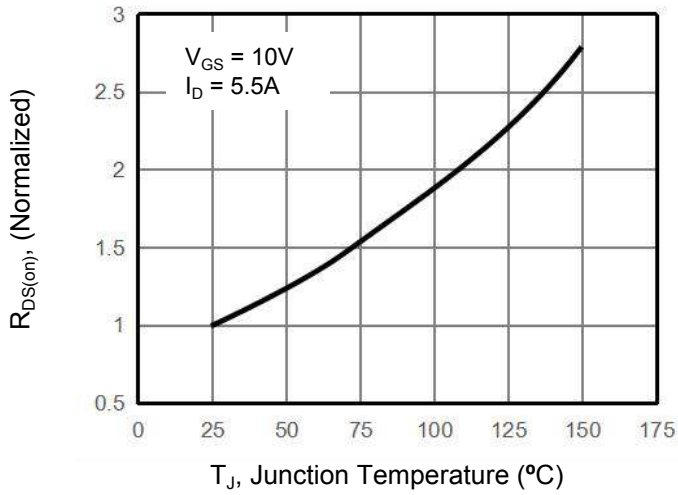


**Figure 6. Source-Drain Diode Forward**

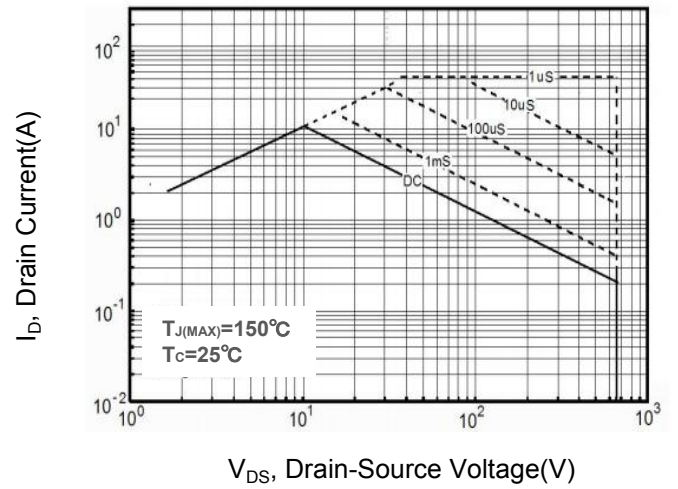


## Typical Characteristics $T_J = 25^\circ\text{C}$ , unless otherwise noted

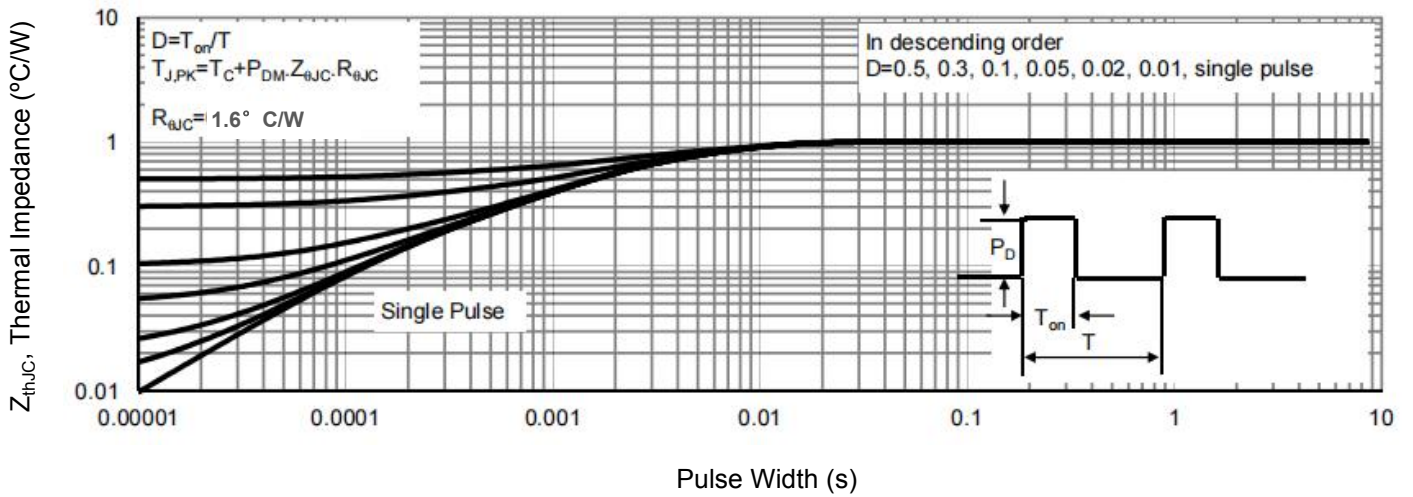
**Figure 7. Drain-Source On-Resistance**



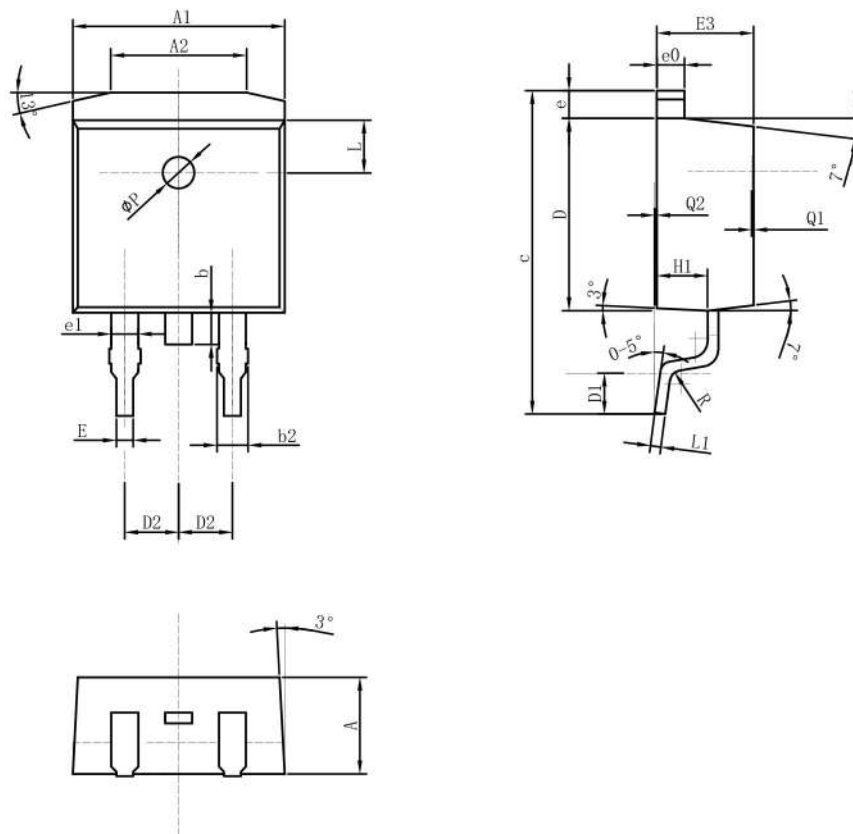
**Figure 8. Safe Operation Area**



**Figure 9. Normalized Maximum Transient Thermal Impedance**



## TO-263 Package Information



### COMMON DIMENSIONS

SYMBOL	mm		
	MIN	NOM	MAX
A	4.52	4.57	4.62
A1	9.95	10.00	10.05
A2	6.30	6.40	6.50
b	1.30	1.50	1.70
b2	1.17	1.27	1.37
c	14.80	15.00	15.20
D	9.05	9.10	9.15
D1	1.90	2.10	2.30
D2	-	2.54	-
E	-	0.80	-
E3	-	4.57	-
e	-	1.30	-
e0	-	1.30	-
e1	1.73	3	-
H1	-	2.40	-
L	-	2.50	-
L1	-	0.50	-
$\phi P$	-	1.50	-
R	-	0.50	-
Q1	0.10	-	0.15
Q2	0	-	0.02