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## maXTouch 640-node Touchscreen Controller Product Brief

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### Description

The mXT640U uses a unique charge-transfer acquisition engine to implement Microchip's patented capacitive sensing method. Coupled with a state-of-the-art CPU, the entire touchscreen sensing solution can measure, classify and track a number of individual finger touches with a high degree of accuracy in the shortest response time. The mXT640U allows for both mutual and self capacitance measurements, with the self capacitance measurements being used to augment the mutual capacitance measurements to produce reliable touch information.

### maXTouch<sup>®</sup> Adaptive Sensing Touchscreen Technology

- Up to 32 X (transmit) lines and 20 Y (receive) lines
- A maximum of 640 nodes can be allocated to the touchscreen
- Touchscreen size 8.17 inches (16:10 aspect ratio), assuming a sensor electrode pitch of 5.5 mm. Other sizes may be possible with different electrode pitches and appropriate sensor material
- Multiple touch support with up to 16 concurrent touches tracked in real time
- Dual-boot OS support for Microsoft<sup>®</sup> Windows<sup>®</sup> and Android

### Touch Sensor Technology

- Discrete/out-cell support including glass and PET film-based sensors
- On-cell/touch-on display support including TFT, IPS and OLED
- Synchronization with display refresh timing capability
- Support for standard (for example, Diamond) and proprietary sensor patterns (review of designs by Microchip recommended)

### Front Panel Material

- Works with PET or glass, including curved profiles (configuration and stack-up to be approved by Microchip)
- Glass 0.4 mm to 4.5 mm, dependent on screen size, touch size and stack-up
- Plastic 0.2 mm to 2.2 mm, dependent on screen size, touch size and stack-up

### Touch Performance

- Moisture/Water Compensation
  - No false touch with condensation or water drop up to 22 mm diameter
  - One-finger tracking with condensation or water drop up to 22 mm diameter

- Glove Support
  - Multiple-finger glove touches up to 1.5 mm thickness (subject to stack-up design)
  - Single-finger glove touch up to 5 mm thickness (subject to stack-up design)
- Mutual capacitance and self capacitance measurements supported for robust touch detection
- Noise suppression technology to combat ambient, charger noise, and power-line noise
  - Up to 240 Vpp between 1 Hz and 1 kHz sinusoidal waveform
  - Up to 20 Vpp between 1 kHz and 1 MHz sinusoidal waveform
- Stylus Support
  - Supports passive stylus with 1 mm contact diameter, subject to configuration, stack up, and sensor design
- Scan Speed
  - Up to 250 Hz one finger reporting rate, subject to configuration
  - Typical report rate for 10 touches  $\geq$ 100 Hz (subject to configuration)
  - Initial touch latency <20 ms for first touch from idle, subject to configuration
  - Configurable to allow for power and speed optimization

### On-chip Gestures

- Supports wake up/unlock gestures, including symbol recognition

### Keys

- Up to 32 nodes can be allocated as mutual capacitance sensor keys (subject to other configurations)
- Support for 3 Generic Keys in addition to the touchscreen array (subject to other configurations)
- Adjacent Key Suppression (AKS) technology is supported for false key touch prevention

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## Enhanced Algorithms

- Lens bending algorithms to remove display noise
- Touch suppression algorithms to remove unintentional large touches, such as palm
- Palm Recovery Algorithm for quick restoration to normal state

## Product Data Store Area

- Up to 32 bytes of user-defined data can be stored during production

## Power Saving

- Programmable timeout for automatic transition from active to idle states
- Pipelined analog sensing detection and digital processing to optimize system power efficiency

## Application Interfaces

- I<sup>2</sup>C-compatible slave mode: Standard/Fast mode 400 kHz, Fast-mode Plus 1 MHz, High-speed mode up to 3.4 MHz
- HID-I<sup>2</sup>C interface for Microsoft® Windows® 8.x and later versions
- Interrupt to indicate when a message is available
- SPI Debug Interface to read the real-time raw data for tuning and debugging purposes

## Power Supply

- Digital (Vdd) 3.3 V nominal
- Digital I/O (VddIO) 3.3 V nominal
- Analog (AVdd) 3.3 V nominal
- High voltage internal X line drive (XVdd) 6.6 V with internal voltage pump
- High voltage internal X line drive (XVdd) 9.9 V with internal voltage pump

## Packages

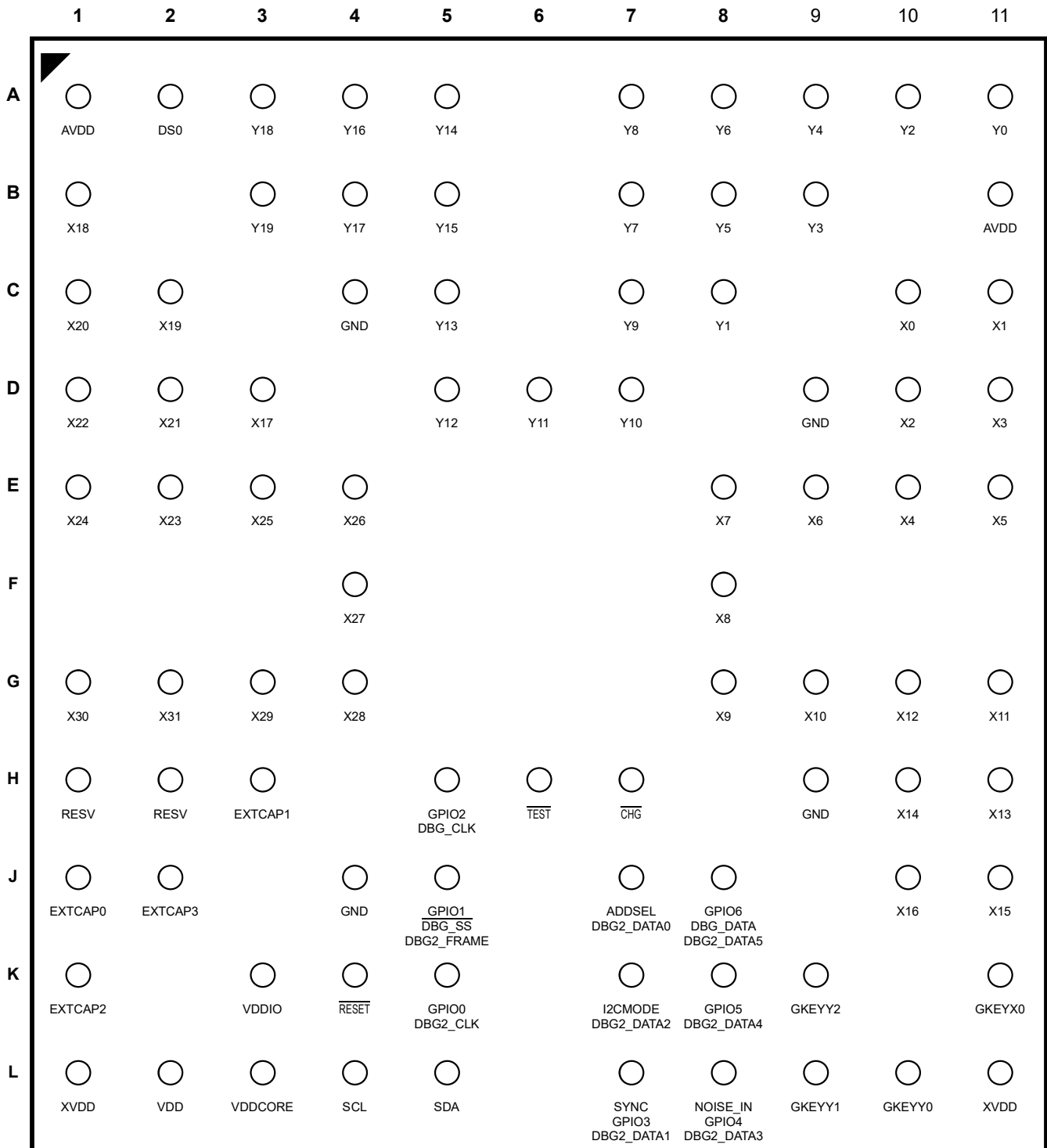
- 88-ball UFBGA 6 × 6 × 0.6 mm, 0.5 mm pitch
- 88-ball X1FBGA 6 × 6 × 0.45 mm, 0.5 mm pitch

## Operating Temperature

- -40°C to +85°C

## PIN CONFIGURATION

### 88-ball UFBGA/X1FBGA



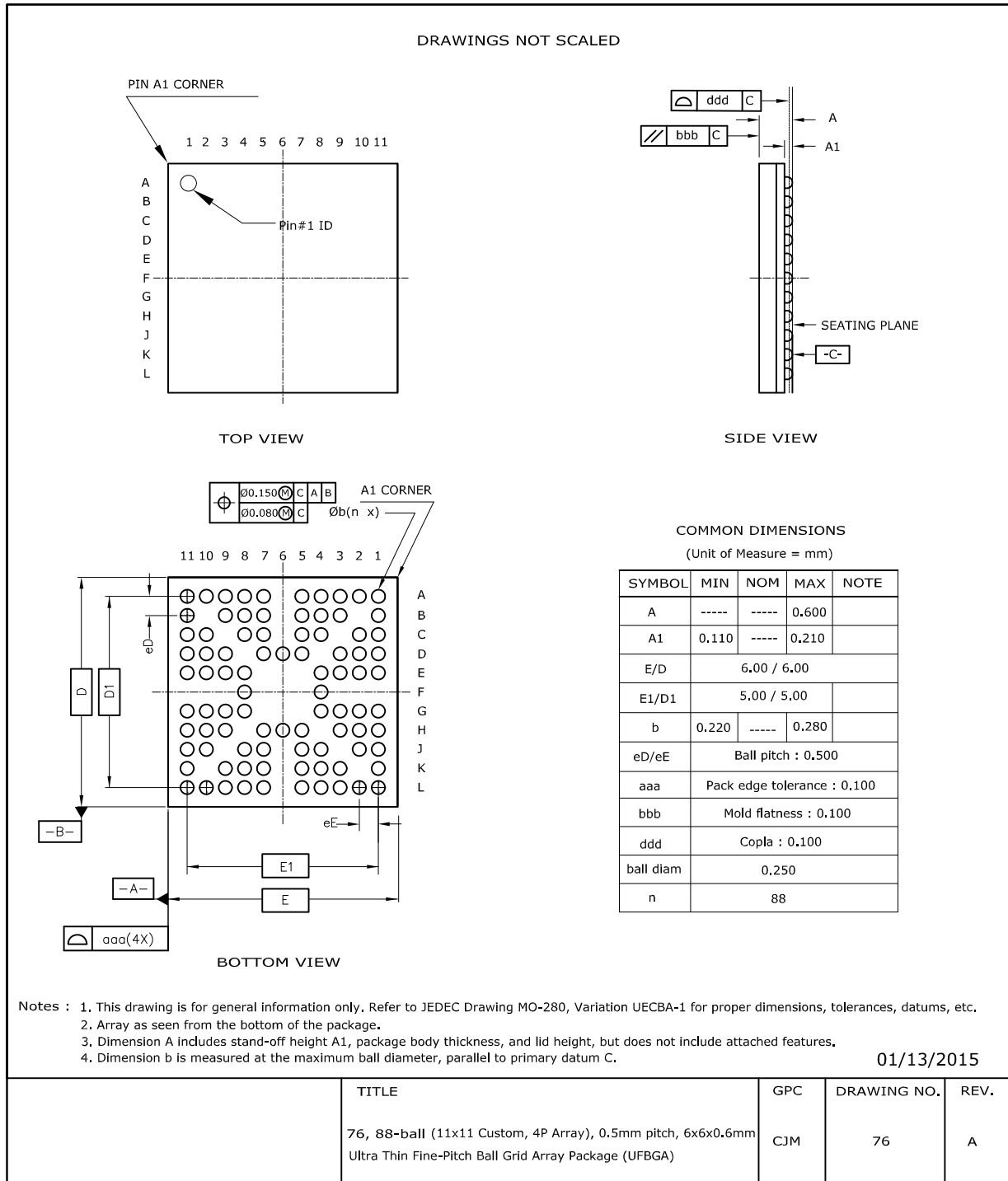
Top View

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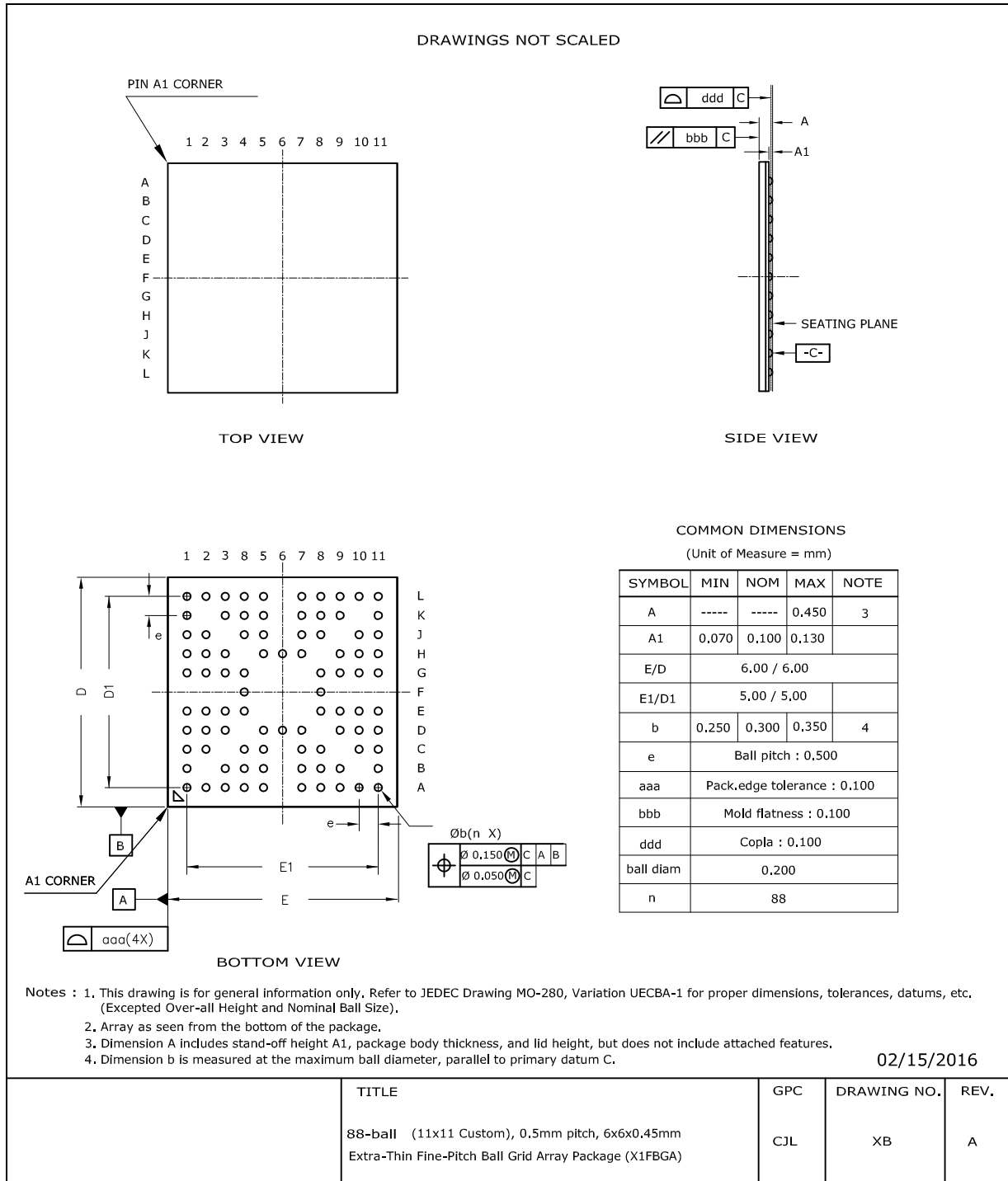
## 1.0 PACKAGING INFORMATION

The following sections give the technical details of the packages for the device.

### 1.1 88-ball UFBGA 6 × 6 × 0.6 mm



## 1.2 88-ball X1FBGA 6 × 6 × 0.45 mm



## APPENDIX A: REVISION HISTORY

### Revision A (September 2017)

Initial edition for firmware revision 1.1 – Release

## PRODUCT IDENTIFICATION SYSTEM

The table below gives details on the product identification system for maXTouch devices. See [“Orderable Part Numbers”](#) below for example part numbers for the mXT640U.

To order or obtain information, for example on pricing or delivery, refer to the factory or the listed sales office.

<u>PART NO.</u>	-XXX	[X]	[XX]	[X]	[XXX]
Device	Package	Temperature Range	Sample Type	Tape and Reel Option	Pattern
Device:	Base device name				
Package:	A	=	QFP (Plastic Quad Flatpack)		
	CCU	=	UFBGA (Ultra Thin Fine-pitch Ball Grid Array)		
	C2U	=	UFBGA (Ultra Thin Fine-pitch Ball Grid Array)		
	NHU	=	UFBGA (Ultra Thin Fine-pitch Ball Grid Array)		
	C4U	=	X1FBGA (Extra Thin Fine-pitch Ball Grid Array)		
	MAU	=	XQFN (Super Thin Quad Flat No Lead Sawn)		
	MA5U	=	XQFN (Super Thin Quad Flat No Lead Sawn)		
	UU	=	WLCSP (Wafer Level Chip Scale Package)		
Temperature Range:	<i>Blank</i>	=	-40°C to +85°C (Grade 3)		
	T	=	-40°C to +85°C (Grade 3)		
	B	=	-40°C to +105°C (Grade 2)		
Sample Type:	<i>Blank</i>	=	Release Sample		
	ES	=	Pre-release (Engineering) Sample		
Tape and Reel Option:	<i>Blank</i>	=	Standard Packaging (Tube or Tray)		
	R	=	Tape and Reel <sup>(1)</sup>		
Pattern:	QTP, SQTP, Code or Special Requirements (Blank Otherwise)				

**Note 1:** Tape and Reel identifier only appears in the catalog part number description. This identifier is used for ordering purposes and is not printed on the device package. See [“Orderable Part Numbers”](#) below or check with your Microchip Sales Office for package availability with the Tape and Reel option.

## Orderable Part Numbers

Orderable Part Number	Firmware Revision	Description
ATMXT640U-CCU023 (Supplied in trays)	1.1.AA	88-ball UFBGA 6 × 6 × 0.6 mm, RoHS compliant Industrial grade sample; not suitable for automotive characterization
ATMXT640U-CCUR023 (Supplied in tape and reel)		
ATMXT640U-C4U023 (Supplied in trays)	1.1.AA	88-ball X1FBGA 6 × 6 × 0.45 mm, RoHS compliant Industrial grade sample; not suitable for automotive characterization
ATMXT640U-C4UR023 (Supplied in tape and reel)		

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NOTES:



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- Microchip products meet the specification contained in their particular Microchip Data Sheet.
- Microchip believes that its family of products is one of the most secure families of its kind on the market today, when used in the intended manner and under normal conditions.
- There are dishonest and possibly illegal methods used to breach the code protection feature. All of these methods, to our knowledge, require using the Microchip products in a manner outside the operating specifications contained in Microchip's Data Sheets. Most likely, the person doing so is engaged in theft of intellectual property.
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