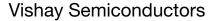
# VS-U5FX120FA60



# FRED Pt<sup>®</sup> Gen 5 Hyperfast Rectifier Diode, 600 V, 120 A



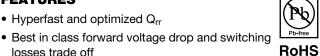
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PRIMARY CHARACTERISTICS						
V <sub>R</sub>	600 V					
V <sub>F</sub> (typical) at 60 A, per diode	1.6 V					
t <sub>rr</sub> (typical) at 60 A, per diode	63 ns					
$I_{F(DC)}$ per module at $T_C = 100 \text{ °C}$	120 A					
Туре	Modules - diode, FRED Pt <sup>®</sup>					
Package	SOT-227					
Circuit configuration	Two separate diodes, parallel pin-out					

## **FEATURES**

losses trade off

Hyperfast and optimized Q<sub>rr</sub>



COMPLIANT

- · Optimized for high speed operation
- 175 °C maximum operating junction temperature
- · Electrically isolated base plate
- Large creepage distance between terminal
- · Simplified mechanical designs, rapid assembly
- Designed and gualified for industrial level
- UL approved file E78996
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

## **DESCRIPTION / APPLICATIONS**

Featuring a unique combination of low conduction and switching losses, the VS-U5FX120FA60 is the right choice for high frequency converters, both soft switched / resonant. The semiconductor in the SOT-227 package is isolated from the copper base plate, allowing for common heatsinks and compact assemblies to be built.

These modules are specifically designed to improve efficiency of PFC and output rectification stages of EV / HEV battery charging stations, booster stage of solar inverters, and UPS applications, these devices are perfectly matched to operate with MOSFETs or high speed IGBTs.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Cathode to anode voltage	V <sub>R</sub>		600	V		
Continuous forward current per diode	I <sub>F</sub>	T <sub>C</sub> = 100 °C	60	٨		
Single pulse forward current per diode	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	450	A		
Maximum power dissipation per module	PD	T <sub>C</sub> = 100 °C	214	W		
RMS isolation voltage	VISOL	Any terminal to case, t = 1 min	2500	V		
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C		

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 $^{\circ}$ C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Cathode to anode breakdown voltage	V <sub>BR</sub>	I <sub>R</sub> = 100 μA	600	-	-		
Forward voltage	V <sub>FM</sub>	I <sub>F</sub> = 60 A	-	1.6	2.2	V	
		I <sub>F</sub> = 60 A, T <sub>J</sub> = 150 °C	-	1.26	-		
	I <sub>RM</sub>	V <sub>R</sub> = 600 V	-	0.23	40		
Reverse leakage current		$T_{\rm J} = 125 \ ^{\circ}C, \ V_{\rm R} = 600 \ V$	-	27	-	μA	
		$T_{J} = 150 \ ^{\circ}C, V_{R} = 600 \ V$	-	128	-		

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# Vishay Semiconductors

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t <sub>rr</sub>	$T_J = 25 \ ^\circ C$	I <sub>F</sub> = 60 A, di <sub>F</sub> /dt = 1000 A/μs, V <sub>R</sub> = 400 V	-	63	-	ns
Reverse recovery time		T <sub>J</sub> = 125 °C		-	79	-	
Dook roopyony ourrent	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	14	-	A
Peak recovery current		T <sub>J</sub> = 125 °C		-	32	-	
	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	0.4	-	
Reverse recovery charge		T <sub>J</sub> = 125 °C		-	1.6	-	μC
Junction capacitance	CT	V <sub>R</sub> = 600 V, f = 1 MHz		-	46.1	-	pF

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance junction to case, per diode			-	-	0.70	
Thermal resistance junction to case, per module	R <sub>thJC</sub>		-	-	0.35	°C/W
Thermal resistance case to heatsink, per module	R <sub>thCS</sub>	Flat, greased surface	-	0.05	-	
Weight			-	30	-	g
		Torque per diode	-	-	1.1 (9.7)	Nm (lbf.in)
Mounting torque		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style				SOT	Г-227	

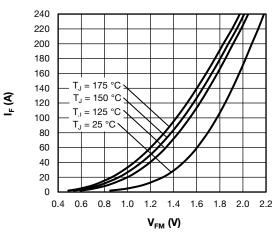


Fig. 1 - Typical Forward Voltage Drop Characteristics

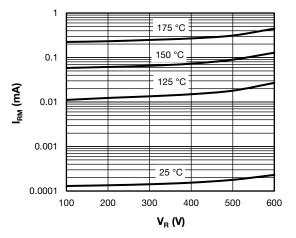


Fig. 2 - Typical Values of Reverse Current



## **Vishay Semiconductors**



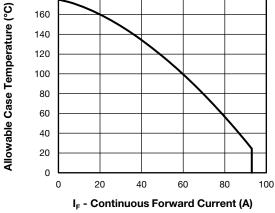


Fig. 3 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Diode)

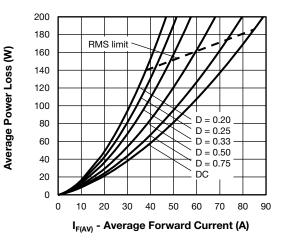


Fig. 4 - Average Power Loss vs. Average Forward Current

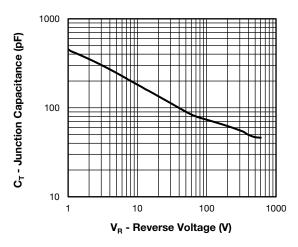


Fig. 5 - Typical Junction Capacitance vs. Reverse Voltage

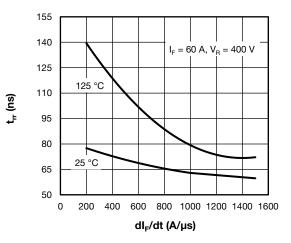


Fig. 6 - Diode Reverse Recovery Time vs. dl<sub>F</sub>dt

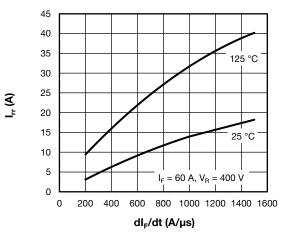


Fig. 7 - Diode Reverse Recovery Current vs. dl<sub>F</sub>dt

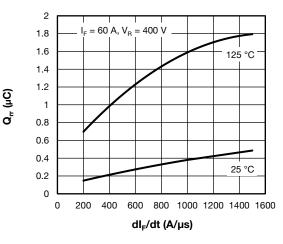


Fig. 8 - Diode Reverse Recovery Charge vs. dl<sub>F</sub>dt

Revision: 19-May-2022

3

Document Number: 96940

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## **VS-U5FX120FA60** www.vishay.com 1 +++ Z<sub>thJc</sub> - Thermal Impedance Junction to Case (°C/W) 0.50 0.1 0.20 0.10 0.05 0.02 0.01 DC 0.01 0.00001 0.0001 0.001 0.01 0.1 1 10

t<sub>1</sub> - Rectangular Pulse Duration (s)

Fig. 9 - Maximum Thermal Impedance Junction to Case

#### **Device code** VS-U5F Х 120 F Α 60 (2) (3) (4)(5) 1 (6)(7 Vishay Semiconductors product 1 U5F = Gen 5 FRED Pt<sup>®</sup> family 2 3 X = Hyperfast FRED Pt<sup>®</sup> diode 4 Current rating per module (120 = 120 A) \_ 5 F = circuit configuration (two separate diodes, parallel pin-out) 6 Package indicator (SOT-227 standard insulated base) Voltage rating (60 = 600 V)7

### **CIRCUIT CONFIGURATION** CIRCUIT CIRCUIT **CIRCUIT DRAWING CONFIGURATION CODE** Lead Assignment 4 3 $4 \cap$ O 3 Two separate diodes, F parallel pin-out 10 $\bigcirc 2$ 1 2

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95423					
Packaging information	www.vishay.com/doc?95425					

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Revision: 19-May-2022

4

Document Number: 96940





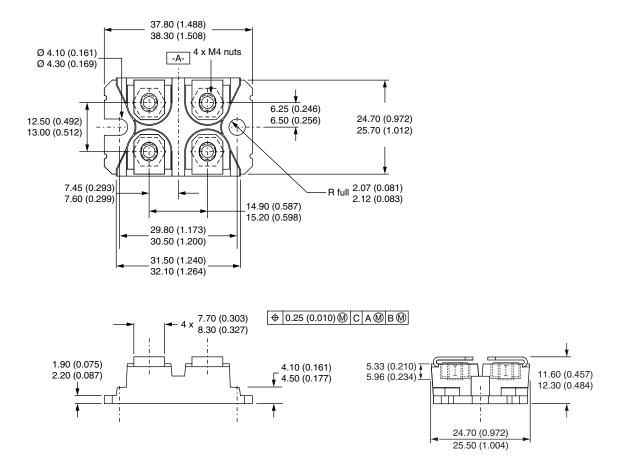
## **Vishay Semiconductors**

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SOT-227 Generation 2

## **DIMENSIONS** in millimeters (inches)



### Note

• Controlling dimension: millimeter



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